RoHS



JL3I75V65SE2E7SN

3-Level NPC Inverter Module with 650V Trench Stop IGBTs

Features

- Electrical features
 - 650V Trench Stop IGBTs
 - Low Inductive Design
 - Low Switching Losses
 - Low Inductive Layout
 - Thermistor
- · Mechanical features
 - Compact Design
 - Solderable Pins
 - Al₂O₃ Substrate with Low Thermal Resistance

JINLAN LE2PACK DYYWW ROHS JL3175V65SE2E7SN XXXXX

LE2 Pack

Typical Applications

- 3-Level-Applications
- · Solar Applications
- · UPS Systems

JINLAN

JL3I75V65SE2E7SN

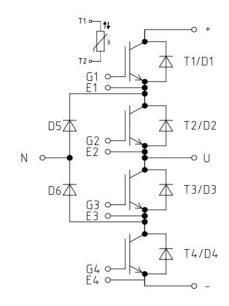
YYWW

XXXXX

QR code

- = Company Name
- = Specific Device Code
- = Year and Work Week Code
- = Serial Number
- = Custom Assembly Information

Description





Package Insulation coordination

Parameter	Symbol	Note or test condition	Values	Unit
Isolation test voltage	V _{ISOL}	RMS,f=50Hz,t=60s	2.5	kV
Internal isolation		basic insulation(class 1,IEC 61140)	Al ₂ O ₃	
Creepage distance	d _{creep}	terminal to heatsink	11.5	mm
Clearance	d _{clear}	terminal to heatsink	10	mm
Comparative tracking index (electrical)	СТІ		>200	
RTI Elec.	RTI	housing	140	$^{\circ}$

Package Characteristic values

					Values		
Parameter	Symbol	Note or test condit	cion	Min.	Тур.	Max.	Unit
Stray Inductance	LCE				15		nH
Module Lead Resistance, Terminal to Chip	Rcc'+EE'				2.00		mΩ
Storage Temperature Range	Т _{втв}			-40		125	$^{\circ}$
Mounting torque for module mounting	М	-Mounting according to valid application note		40		80	N
Weight	G				39		g



IGBT (T1, T2, T3, T4)

Absolute Maximum Ratings (Tc = 25°C unless otherwise noted)

Symbol	Description	Value	Unit
Vces	Collector-Emitter Voltage	650	V
V_{GES}	Gate-Emitter Voltage	±30	V
I_{CDC}	Continuous Collector Current @ Tc = 80°C (TJMAX = 175°C)	75	Α
I _{CM}	Pulsed Collector Current, t_p limited by $T_{v_j max}$	150	Α
Tjmax	Maximum Junction Temperature	175	°C
J	Power Dissipation @ T _C = 25°C	270	W
P _D	Power Dissipation @T _C = 100 °C	135	W

Characteristics (Tc=25°C unless otherwise noted)

Symbol	Parameter	Test Co	ndition	Min	Тур	Max	Unit
		I _C =50A,	T _{vj} = 25 °C		1.25	1	
	Collector Emitter Seturation Valtage	V _{GE} = 15V	T _{vj} = 150 °C		1.45		 v
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	I _C =75A,	T _{vj} = 25 °C		1.35	1.80	V
		V _{GE} = 15V	T _{vj} = 150 °C		1.55	1	
V _{GE(TH)}	Gate-Emitter Threshold Voltage	I _C =1mA,	V _{CE} =VGE	4.0	4.75	5.5	٧
Ices	Collector-Emitter Cutoff Current	V _{GE} = 0 V,	V _{CE} = 650 V			500	μ
I _{GES}	Gate-Emitter Leakage Current	$V_{GE} = \pm 30 \text{ V}, V_{CI}$	≡ = 0 V, T _{vj} = 25°C			±200	n/
RGint	Internal Gate Resistance	T _{vj} =	25°C		0.8		Ω
Cies	Input Capacitance	V _{CE} =25V	',V _{GE} =0V,		5800		р
Coes	Output capacitance	f=1	MHz		206		р
Cres	Reverse Transfer				40		р
Q _G	Gate Charge	V _{CE} = 480 V _{GE} = -5 V	/, I _C = 75 A / to +15 V		0.197		μ
td(on)	Turn-On Delay Time				62	-	
tr	Rise Time	V _{CE} =400\	/ Ic=75A		30		_
td(off)	Turn-off Delay Time		+15 V, R _g =5Ω		261	-	n
tf	Fall Time		ve Load		45	1	
Eon	Turn-On Switching Loss per Pulse	Tvj=	25°C		3.9		
Eoff	Turn Off Switching Loss per Pulse				1.4	-	m
td(on)	Turn−On Delay Time				TBD		
tr	Rise Time				TBD	1	
td(off)	Turn-off Delay Time	V _{CE} =400\	/ Ic=75A		TBD	-	n
tf	Fall Time	V _{GE} = −5 V to	+15 V, R _g =5Ω		TBD		
Eon	Turn-on Switching Loss per Pulse		75°C		5.1		
Eoff	Turn Off Switching Loss per Pulse			-	2.5	-	m
RthJC	Thermal resistance	Junction-to-Ca	ase (per IGBT)		0.48	ŀ	K/
T _{vj op}		Temperature under	switching conditions	-40		150 ¹⁾	°C

 $^{^{1)}}T_{vj\,op} > 150\,^{\circ}C$ is only allowed for operation at overload conditions.



Diode (D1, D2, D3, D4)

Absolute Maximum Ratings (Tc = 25°C unless otherwise noted)

Symbol	Description	Value	Unit
V_{RRM}	Repetitive Peak Reverse Voltage	650	٧
I _F	Diode Continuous Forward Current	75	Α
I _{FM}	Diode Maximum Forward Current t _p =1ms	150	Α

Characteristics (Tc=25℃ unless otherwise noted)

Symbol	Parameter	Test Condition	Min	Тур	Max	Unit
.,	Die de Fernand Velle ne	I _F = 75 A, T _J = 25°C		1.50	2.50	.,
VF	Diode Forward Voltage	I _F = 75 A, T _J = 175°C		1.40	1	V
Trr	Reverse Recovery Time			142	ŀ	ns
IRM	Peak Reverse Recovery Current	$V_{CE}=400V$ $V_{GE}=-5 V \text{ to } +15 V$		40	ŀ	Α
Qrr	Recovered Charge	$I_F=75A,R_g=5\Omega$ $Tvi=25^{\circ}C$		2.35	I	μC
Erec	Reverse Recovery Energy	.,		0.32	I	mJ
Trr	Reverse Recovery Time	V _{CE} =400V		213	-	ns
IRM	Peak Reverse Recovery Current	V_{GE} = -5 V to +15 V		60	-	Α
Qrr	Recovered Charge	l₅=75A, R ₉ =5Ω, Tvi=175°C		5.78	1	μC
Erec	Reverse Recovery Energy	1.1, 110 0		0.48	1	mJ
RthJC	Thermal resistance	Junction-to-Case (per diode)		0.53	1	K/W
T _{vj op}		Temperature under switching conditions	-40		150 ²⁾	${\mathbb C}$

 $^{^{2)}}T_{\nu j\,op}$ > 150 $^{\circ}{\rm C}$ is only allowed for operation at overload conditions.

Diode (D5, D6)

Absolute Maximum Ratings (Tc = 25°C unless otherwise noted)

Symbol	Description	Value	Unit
V _{RRM}	Repetitive Peak Reverse Voltage	650	٧
l _F	Diode Continuous Forward Current	75	Α
I _{FM}	Diode Maximum Forward Current t _p =1ms	150	Α

Characteristics (Tc=25℃ unless otherwise noted)

Symbol	Parameter	Test Condition	Min	Тур	Max	Unit
.,	Die de Francoul Wellere	I _F = 75 A, T _J = 25°C		1.50	2.50	.,
VF	Diode Forward Voltage	I _F = 75 A, T _J = 175°C		1.40		V
Trr	Reverse Recovery Time		-	142		ns
IRM	Peak Reverse Recovery Current	$V_{CE}=400V$ $V_{GE}=-5 V to +15 V$	1	40		Α
Qrr	Recovered Charge	$I_F=75A,R_g=5\Omega$ $Tvi=25^{\circ}C$	1	2.35		μC
Erec	Reverse Recovery Energy	,	1	0.32		mJ
Trr	Reverse Recovery Time	V _{CE} =400V	1	213		ns
IRM	Peak Reverse Recovery Current	$V_{GE} = -5 \text{ V to } +15 \text{ V}$	1	60		Α
Qrr	Recovered Charge	I _F =75A, R _g =5Ω, Tvi=175°C	-	5.78		μC
Erec	Reverse Recovery Energy	11, 110 0		0.48		mJ
RthJC	Thermal resistance	Junction-to-Case (per diode)		0.53		K/W
T _{vj op}		Temperature under switching conditions	-40		150 ³⁾	$^{\circ}$

 $^{^{3)}}T_{\nu j\,op}$ > 150 $^{\circ}{\rm C}$ is only allowed for operation at overload conditions.

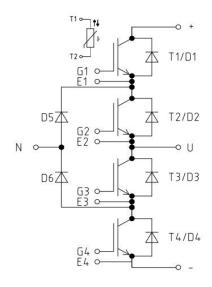


NTC Characteristics (Tc = 25°C unless otherwise noted)

Symbol	Parameter	Test Condition	Min	Тур	Max	Unit
R ₂₅	Rated Resistance			5.0		kΩ
ΔR/R	Deviation of R100	Tc=100 ℃,R100=493.3Ω	-5		5	%
P ₂₅	Power Dissipation				20.0	mW
B _{25/50}	B-value	R ₂ =R ₂₅ exp[B _{25/50} (1/T ₂ - 1/(298.15K))]	-	3375		K
B _{25/80}	B-value	R ₂ =R ₂₅ exp[B _{25/80} (1/T ₂ - 1/(298.15K))]		3411		K
B _{25/100}	B-value	R ₂ =R ₂₅ exp[B _{25/100} (1/T ₂ - 1/(298.15K))]		3433		K

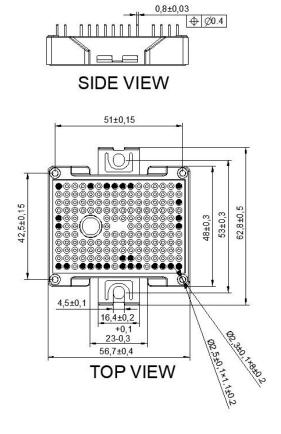


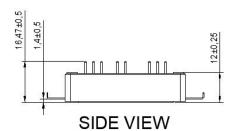
CIRCUIT DIAGRAM

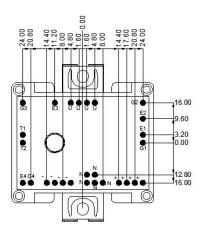


PACKAGE DIMENSION

Dimensions in Millimeters









REVISION HISTORY

Document version	Date of release	Description of changes
Rev.00	2024-12-31	Preview



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