



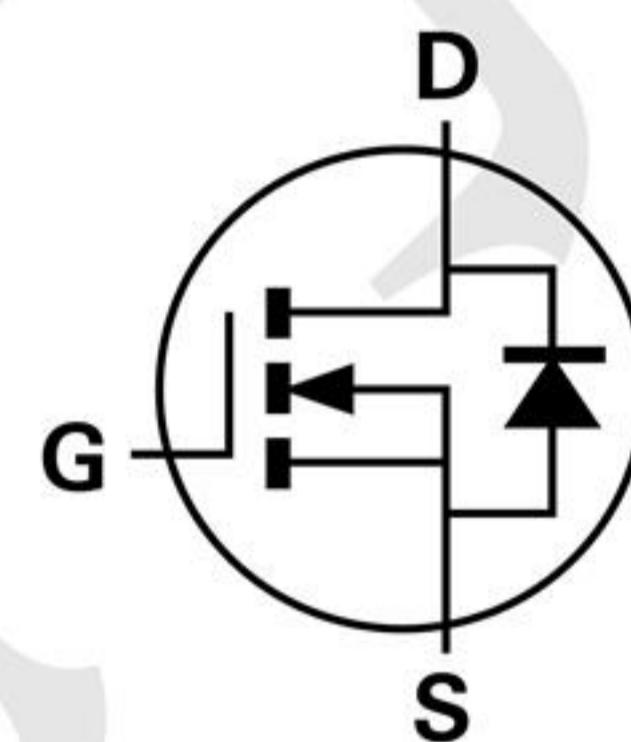
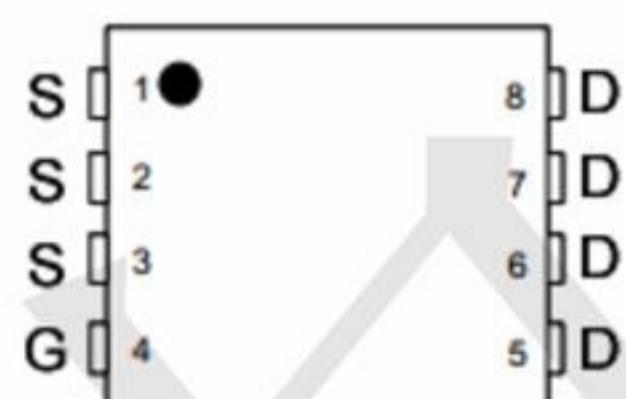
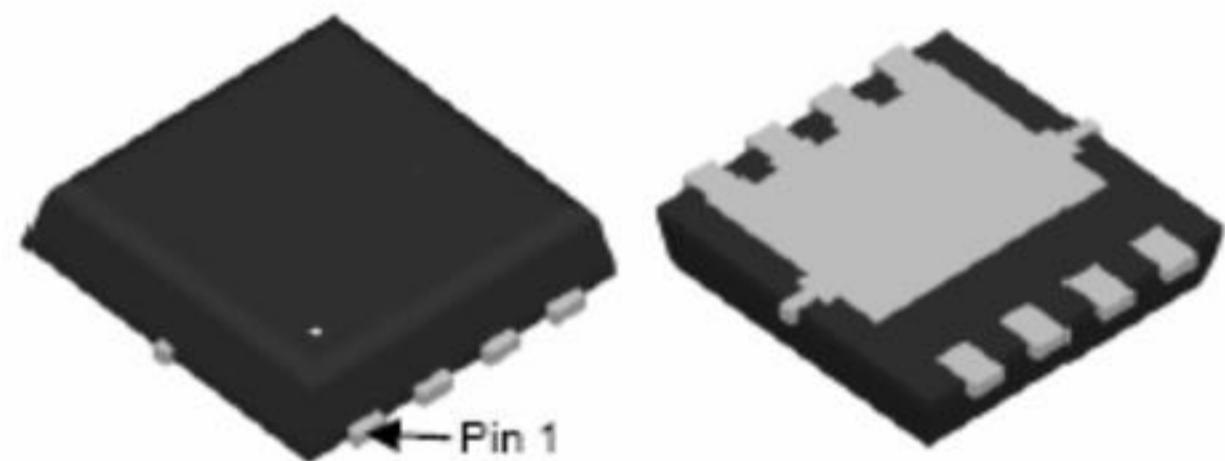
Features

- $R_{DS(ON)}$, $V_{GS}@10V, I_D@16A < 9m\Omega$
- $R_{DS(ON)}$, $V_{GS}@4.5V, I_D@8A < 13m\Omega$
- High switching speed
- Improved dv/dt capability
- Low Gate Charge
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2.0

Application

- Load/Power Switching
- Interfacing Switching
- Battery Management for Ultra Small Portable Electronics
- Logic Level Shift

Package and Pin Configuration



Absolute Maximum Ratings ($T_A=25^\circ C$ unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current $T_C=25^\circ C$	I_D	42	A
$T_C=100^\circ C$	I_D	26	
Pulsed Drain Current ^(Note 1)	I_{DM}	168	
Power Dissipation $T_C=25^\circ C$	P_D	35	W
$T_C=100^\circ C$	P_D	14	
Continuous Drain Current $T_A=25^\circ C$	I_D	10	A
$T_A=70^\circ C$	I_D	8	
Power Dissipation $T_A=25^\circ C$	P_D	2.0	W
Power Dissipation $T_A=70^\circ C$	P_D	1.3	
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55~150	°C
Typical Thermal Resistance ^(Note 4,5)	Junction to Case	$R_{\theta JC}$	3.6
	Junction to Ambient	$R_{\theta JA}$	62.5



Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	30	-	-	V
Gate Threshold Voltage	$V_{\text{GS(th)}}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	1.0	1.7	2.5	
Drain-Source On-State Resistance	$R_{\text{DS(on)}}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=16\text{A}$	-	6.5	9	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=8\text{A}$	-	9.5	13	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}}=30\text{V}, V_{\text{GS}}=0\text{V}$	-	-	1.0	μA
Gate-Source Leakage Current	I_{GSS}	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	± 100	nA
Dynamic <small>(Note 6)</small>						
Total Gate Charge	Q_g	$V_{\text{DS}}=15\text{V}, I_{\text{D}}=20\text{A}, V_{\text{GS}}=4.5\text{V}$ <small>(Note 2,3)</small>	-	7.1	-	nC
Gate-Source Charge	Q_{gs}		-	3.1	-	
Gate-Drain Charge	Q_{gd}		-	2.0	-	
Input Capacitance	C_{iss}	$V_{\text{DS}}=25\text{V}, V_{\text{GS}}=0\text{V}, f=1.0\text{MHZ}$	-	763	-	pF
Output Capacitance	C_{oss}		-	132	-	
Reverse Transfer Capacitance	C_{rss}		-	81	-	
Turn-On Delay Time	$t_{\text{d(on)}}$	$V_{\text{DS}}=15\text{V}, I_{\text{D}}=15\text{A}, V_{\text{GS}}=10\text{V}, R_{\text{G}}=6\Omega$ <small>(Note 2,3)</small>	-	5.4	-	ns
Turn-On Rise Time	t_r		-	86	-	
Turn-Off Delay Time	$t_{\text{d(off)}}$		-	20	-	
Turn-Off Fall Time	t_f		-	10	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I_s	---	-	-	42	A
Diode Forward Voltage	V_{SD}	$I_s=1\text{A}, V_{\text{GS}}=0\text{V}$	-	0.7	1.0	V

Typical Characteristics

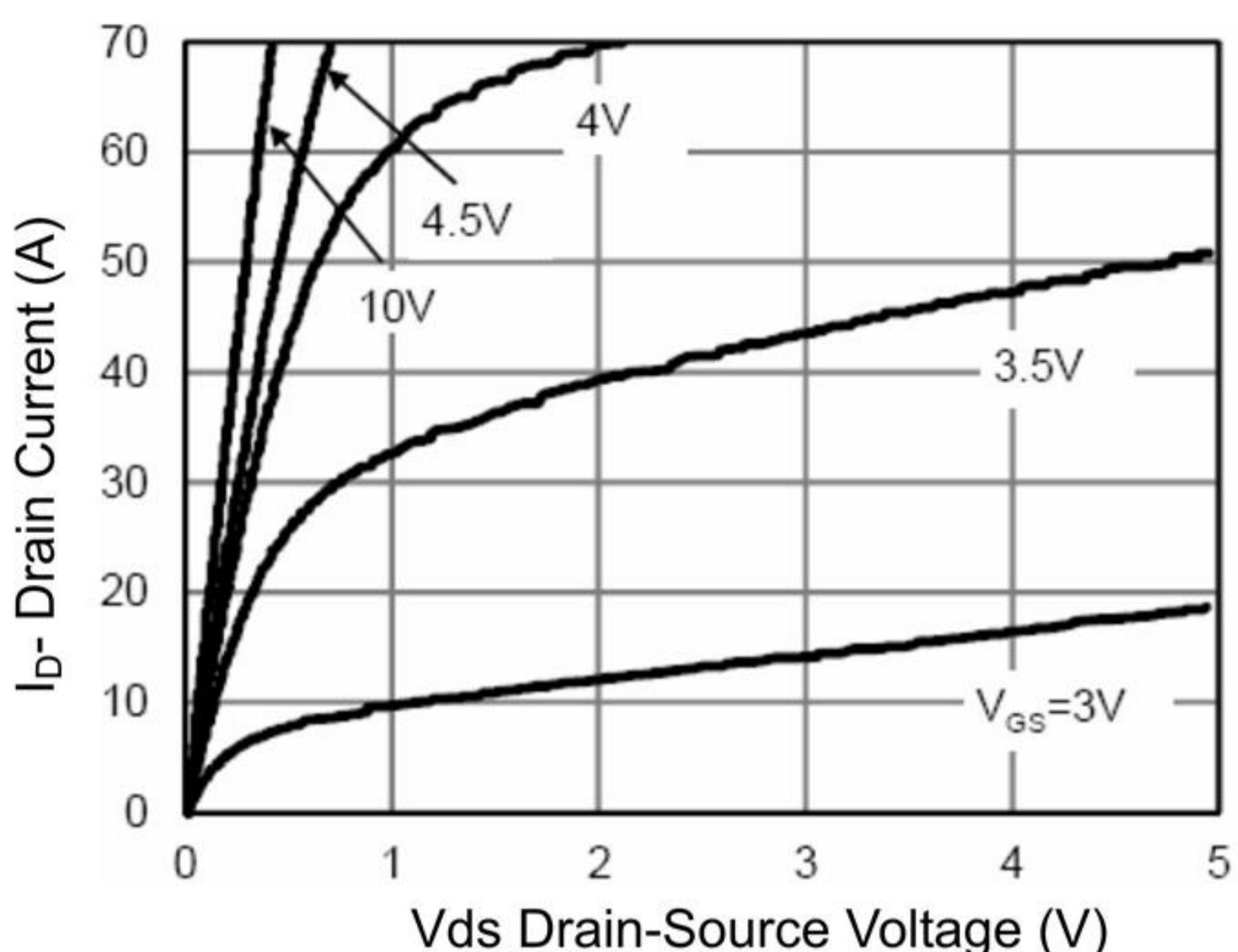


Figure 1 Output Characteristics

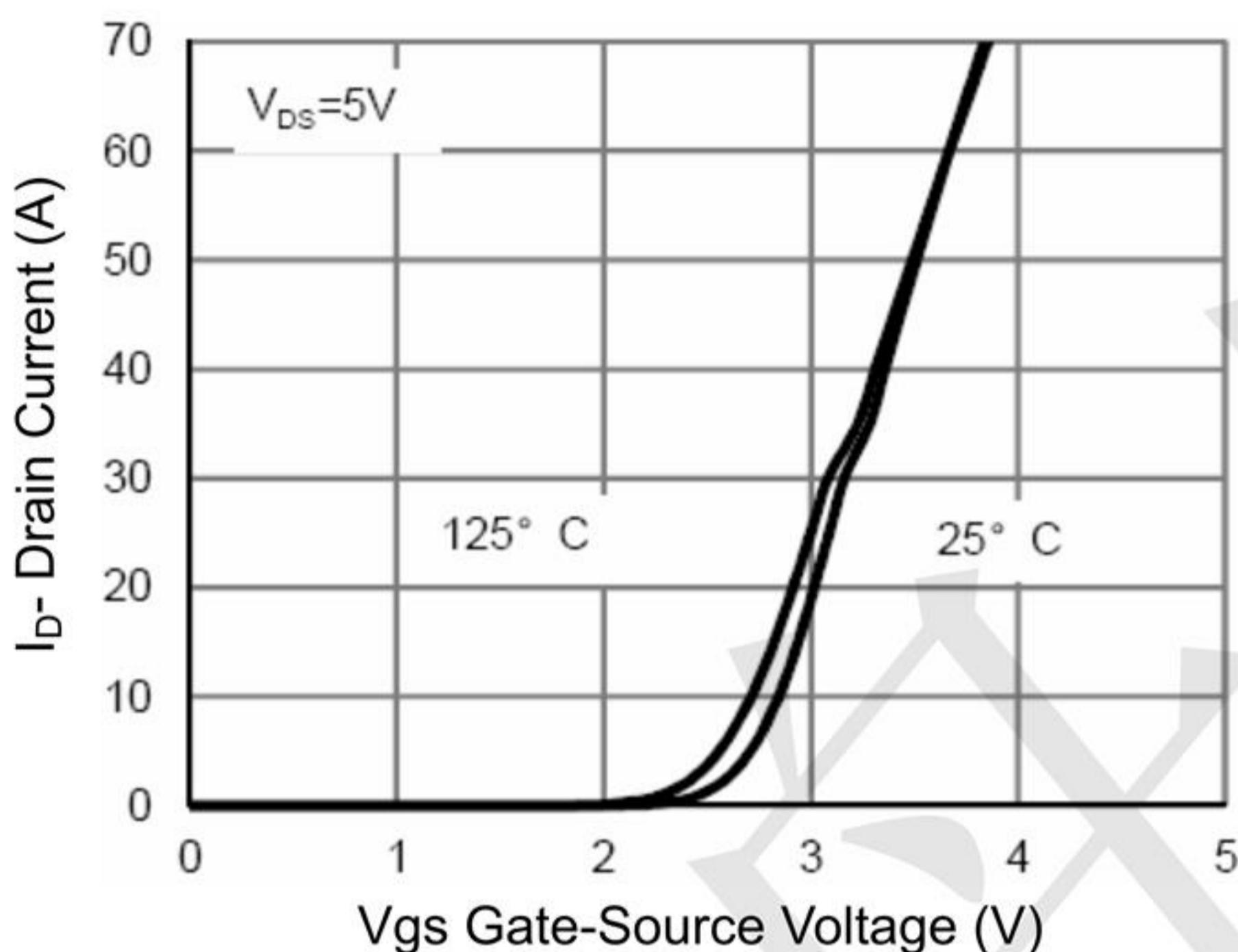


Figure 2 Transfer Characteristics

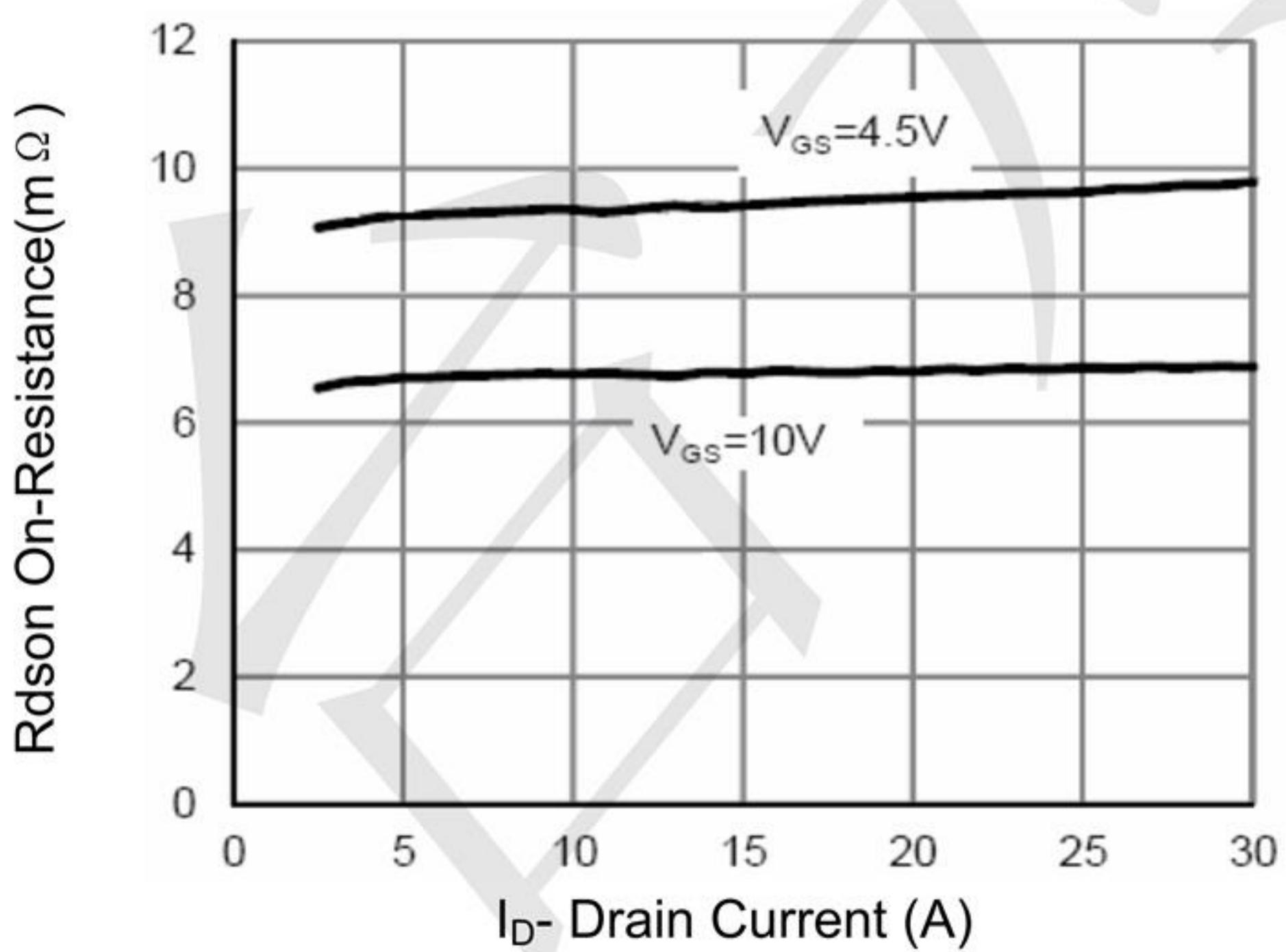


Figure 3 Rdson- Drain Current

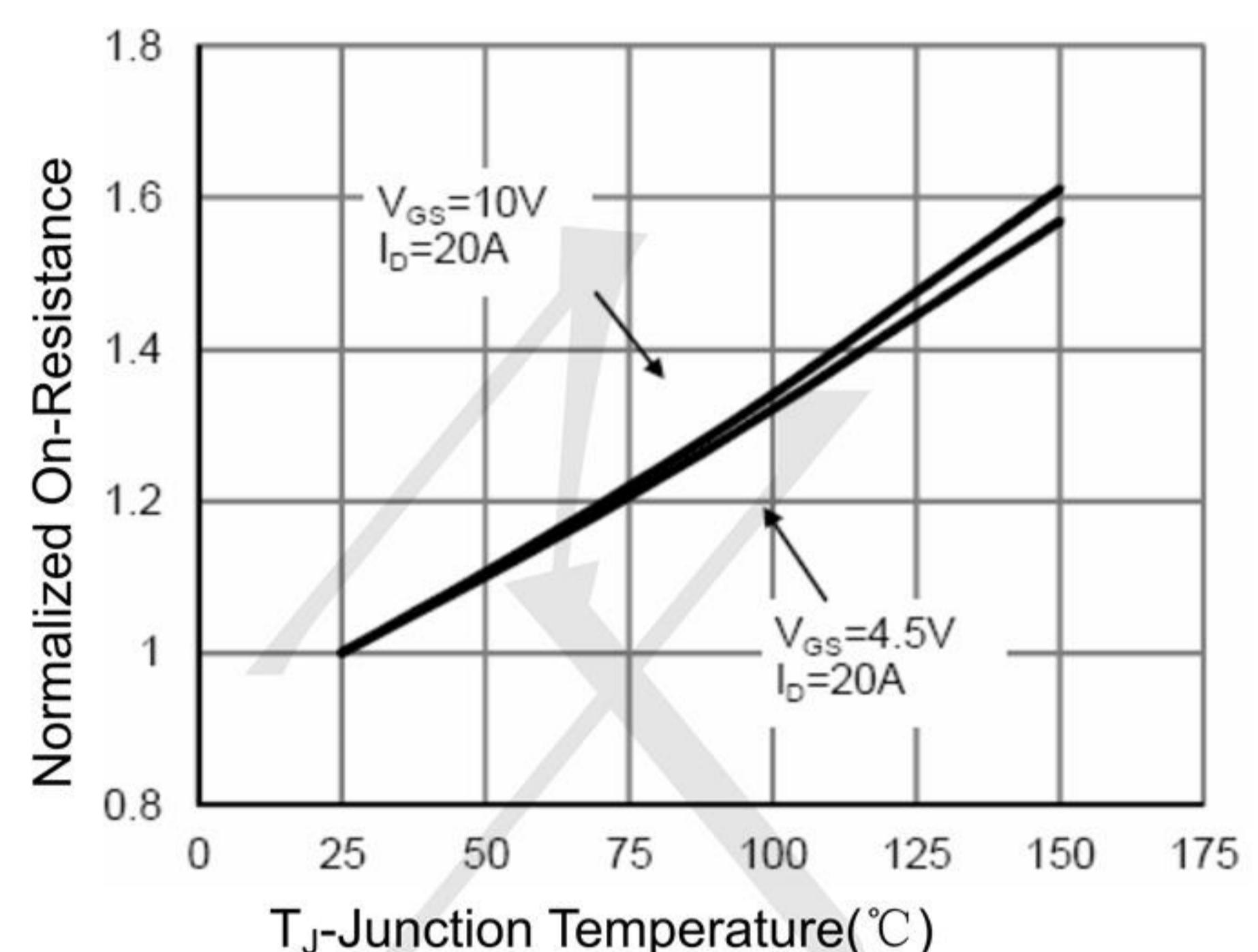


Figure 4 Rdson-Junction Temperature

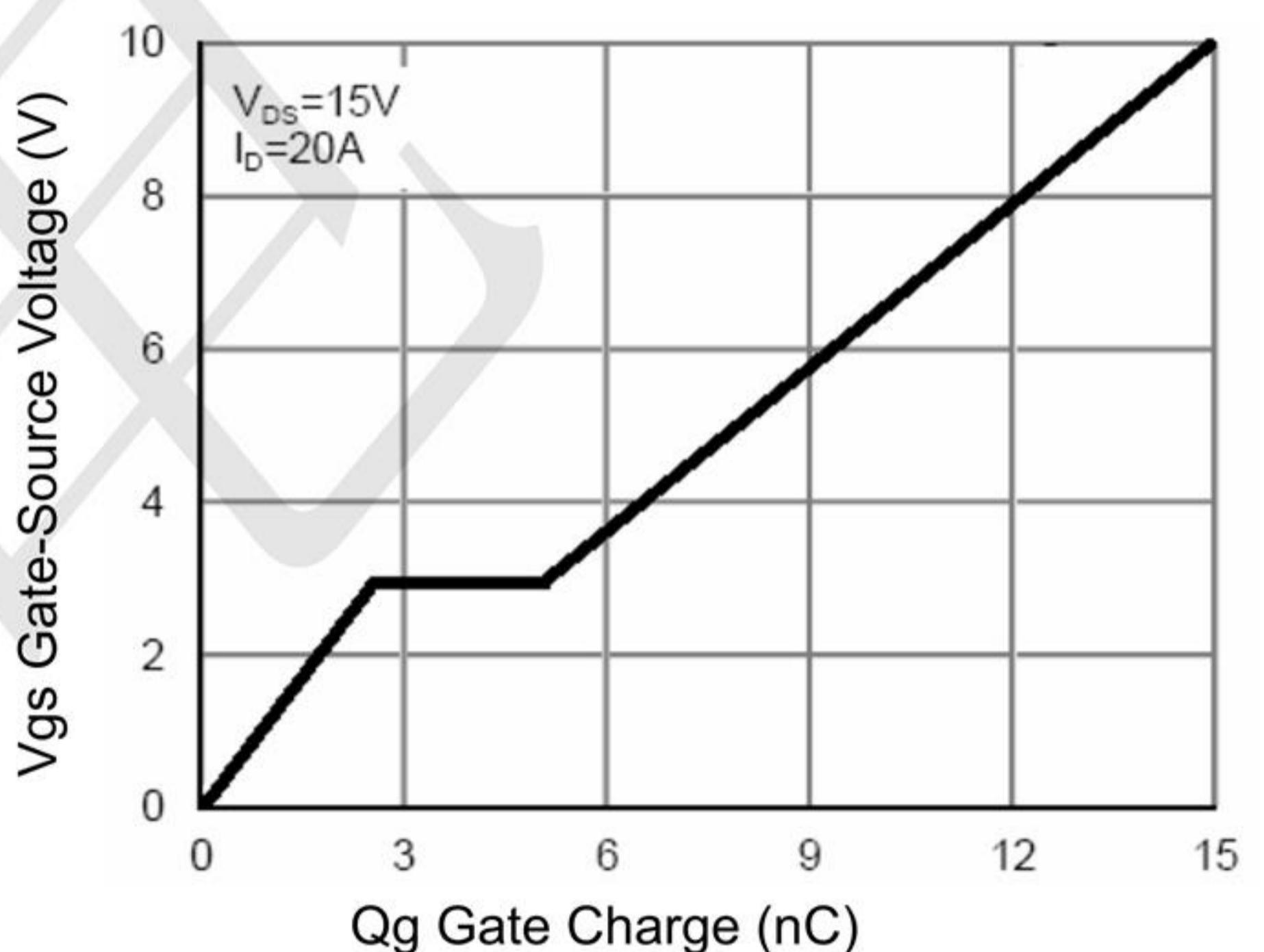


Figure 5 Gate Charge

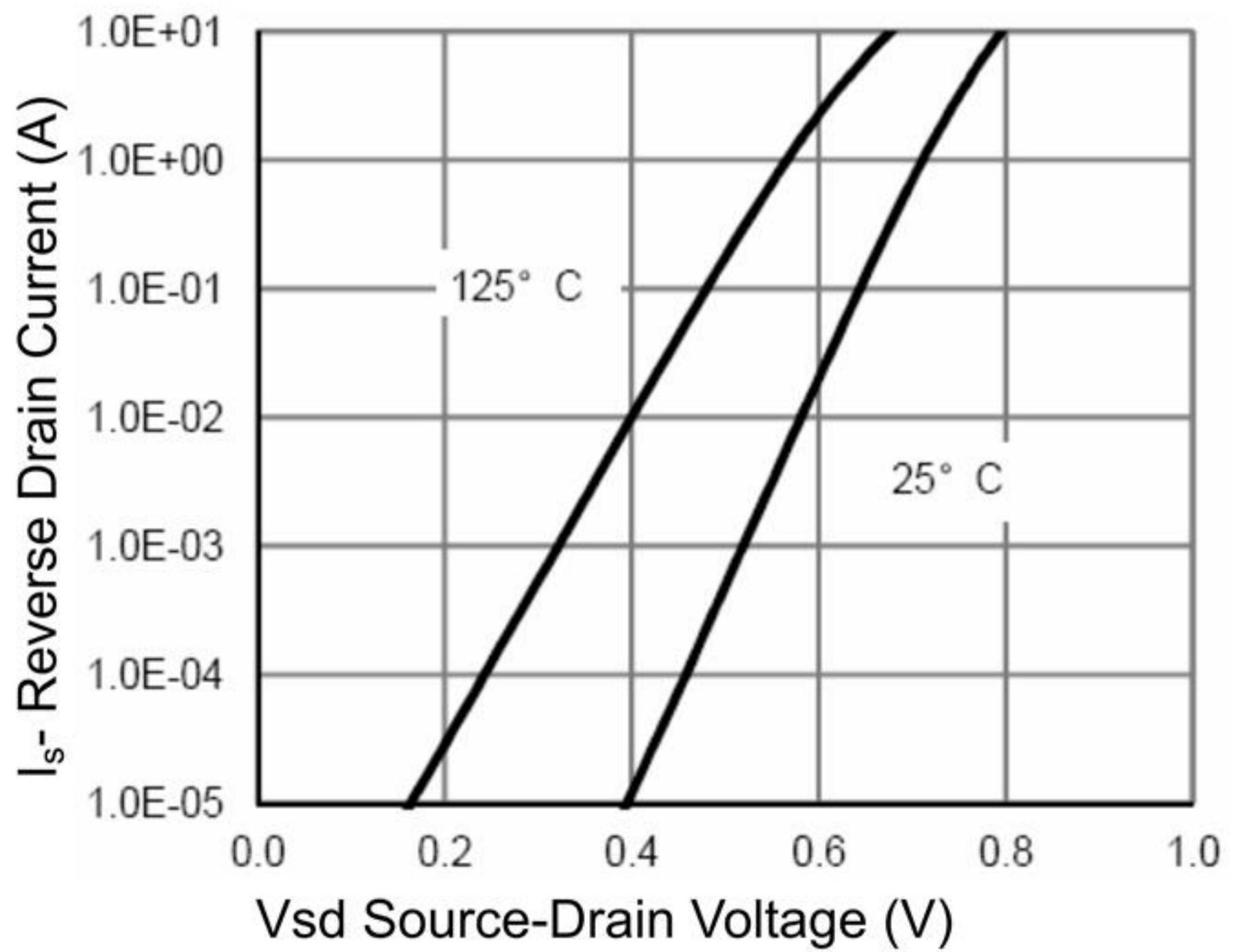


Figure 6 Source- Drain Diode Forward

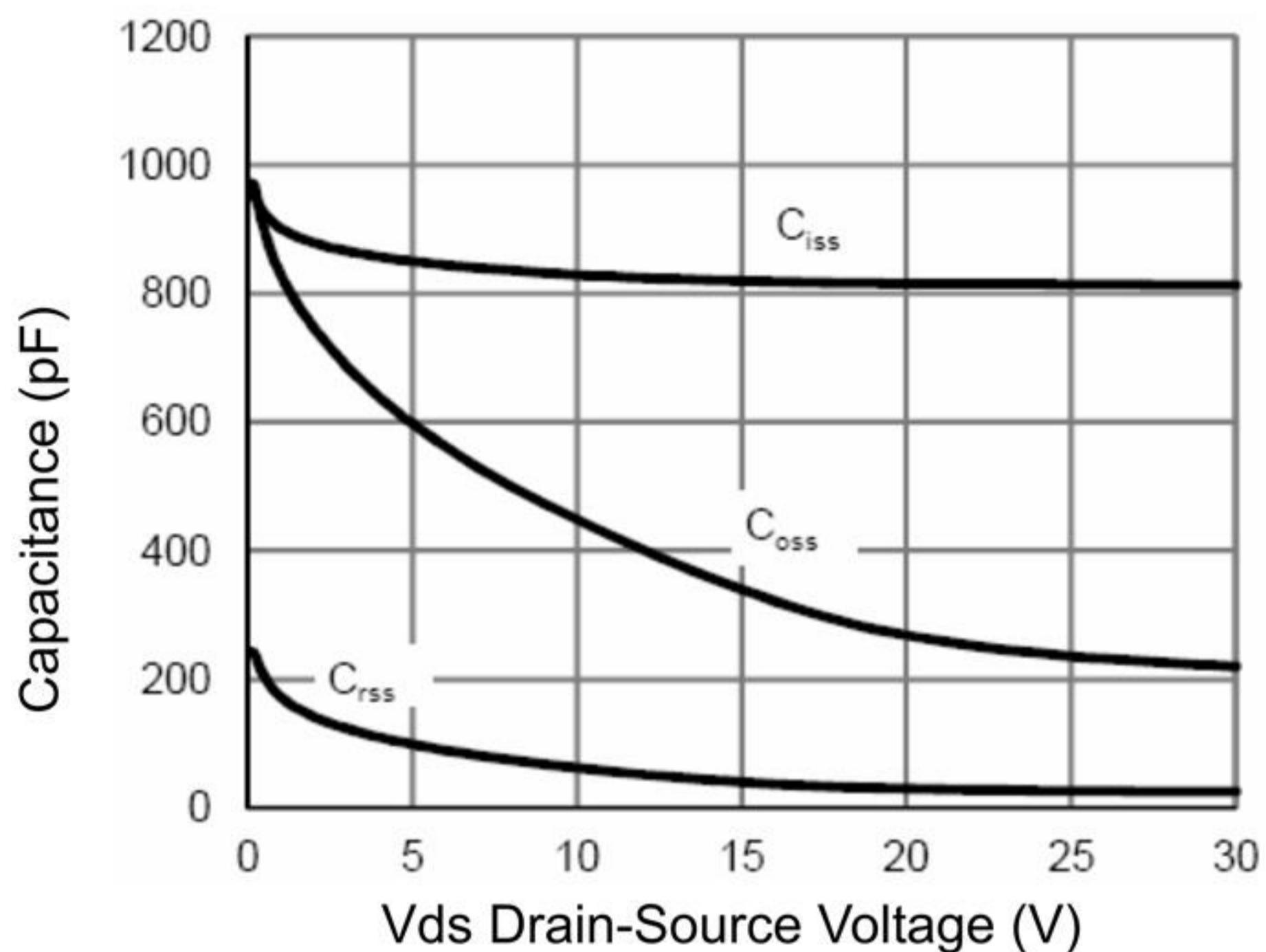


Figure 7 Capacitance vs Vds

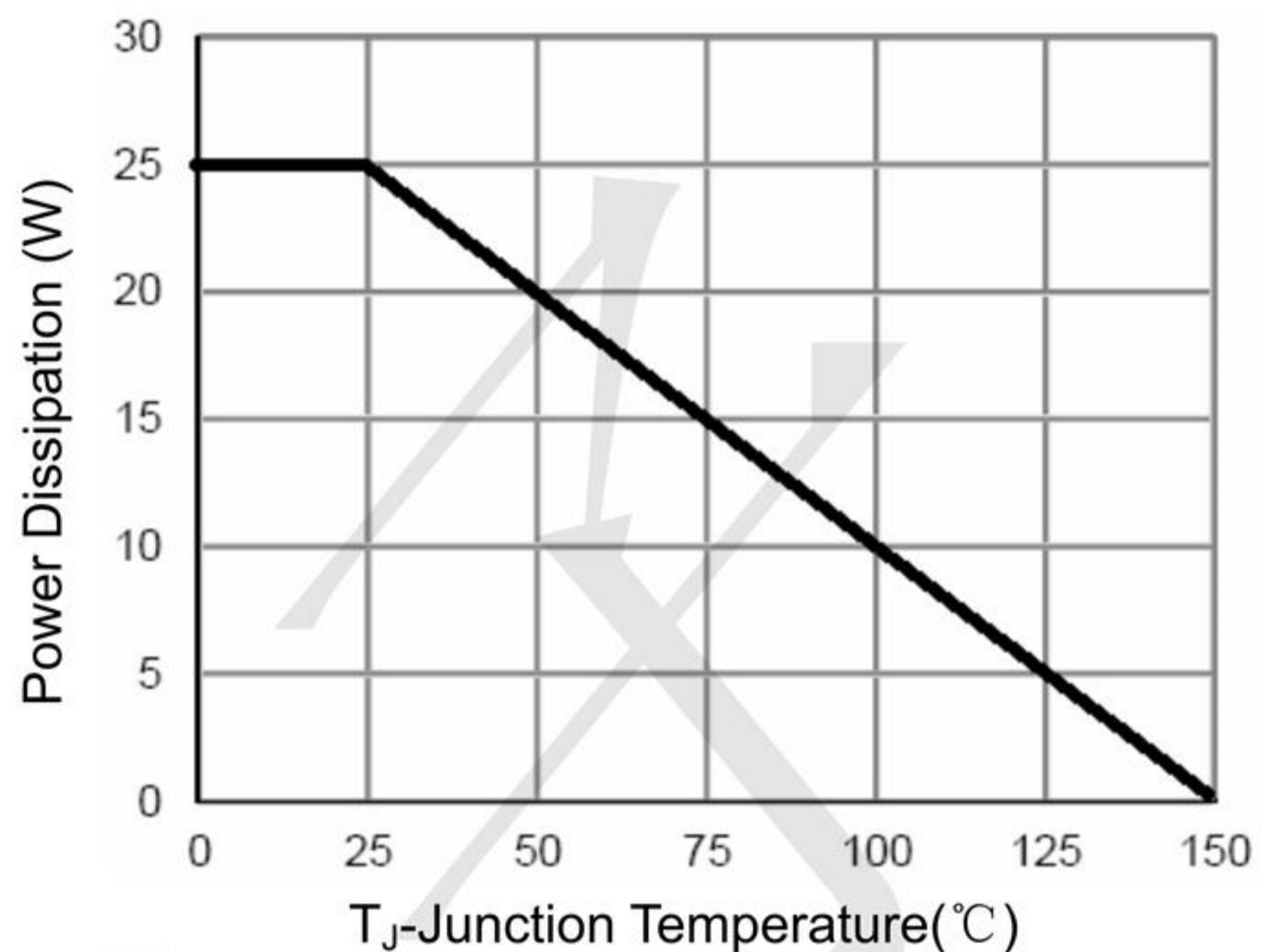


Figure 9 Power De-rating

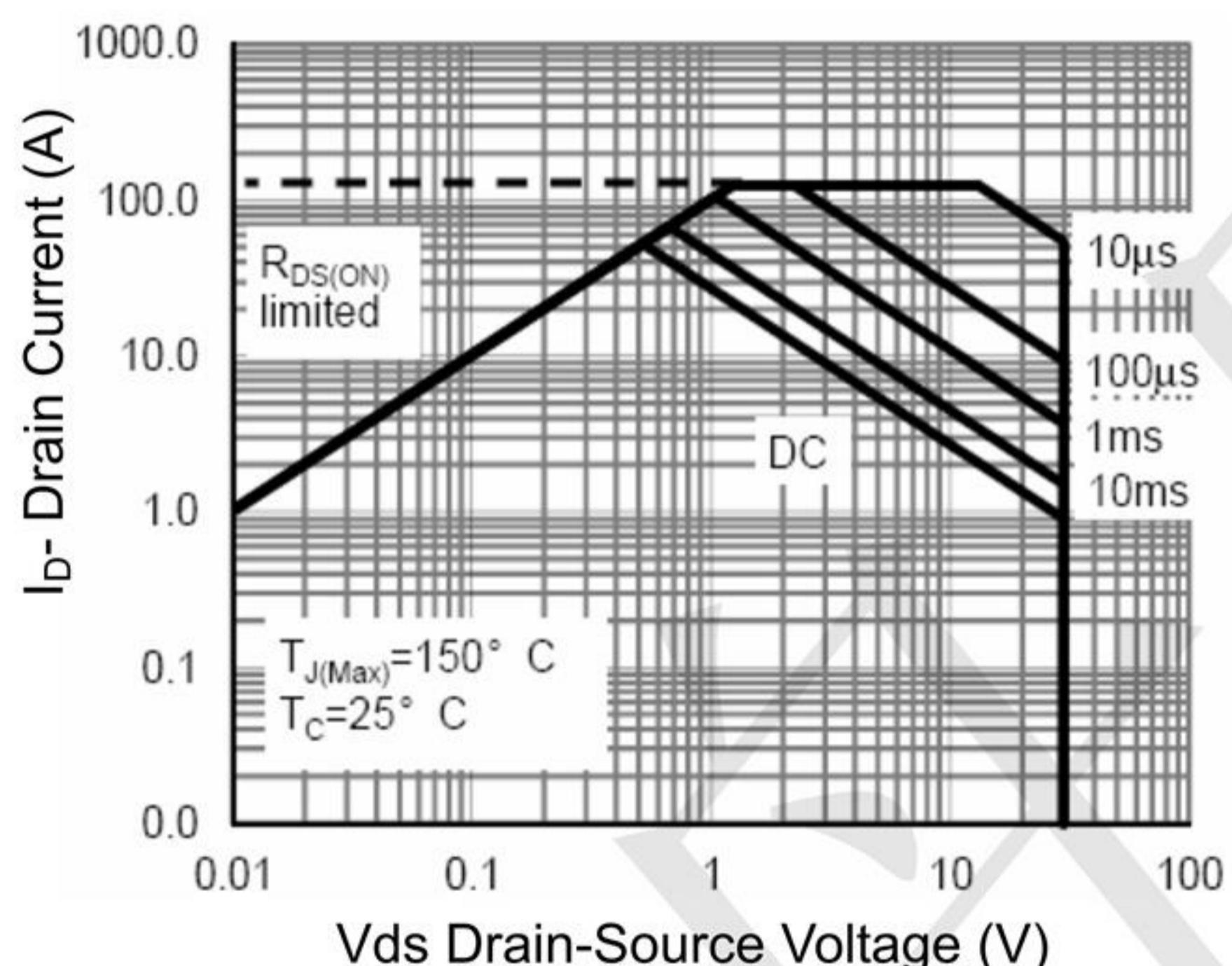


Figure 8 Safe Operation Area

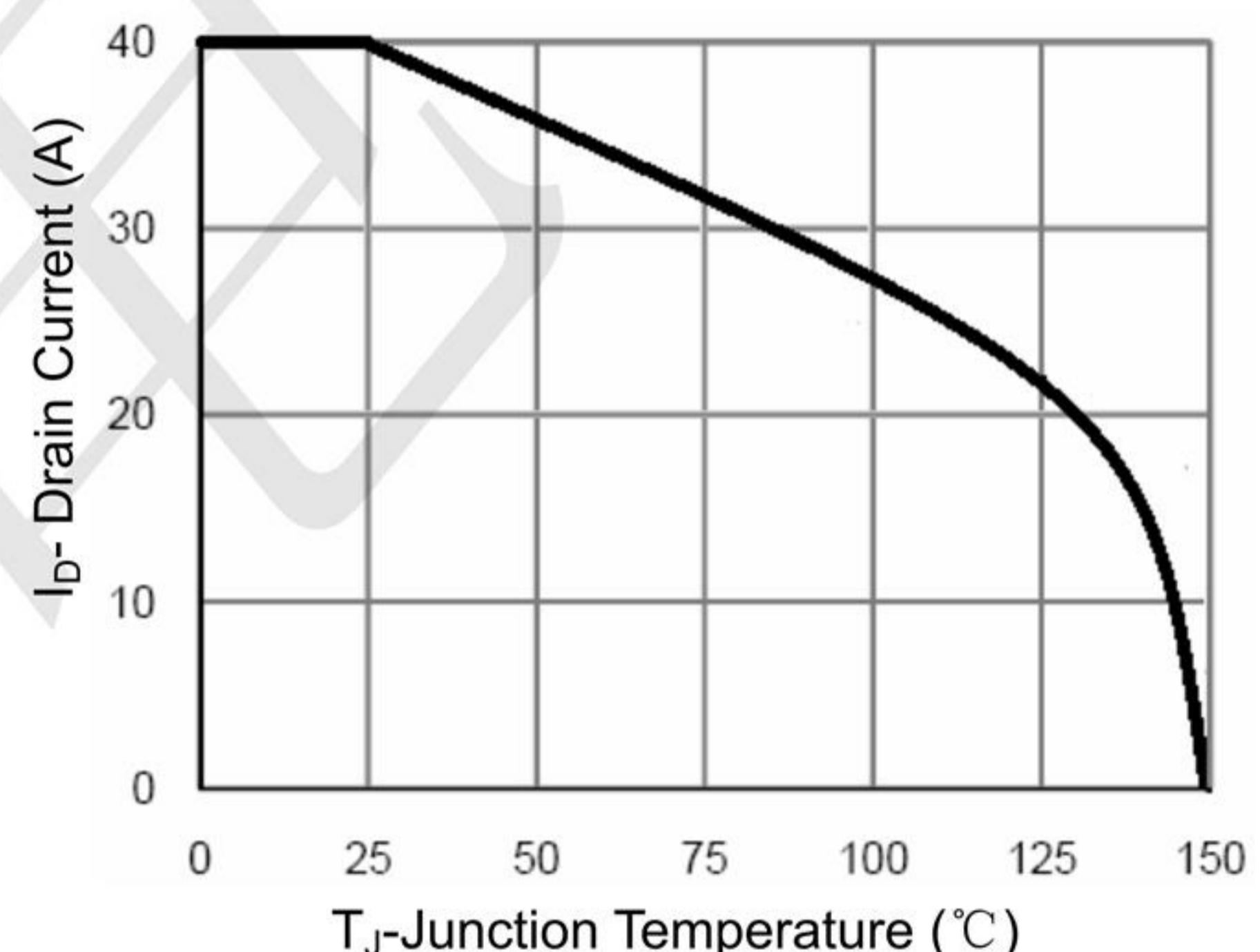


Figure 10 Current De-rating

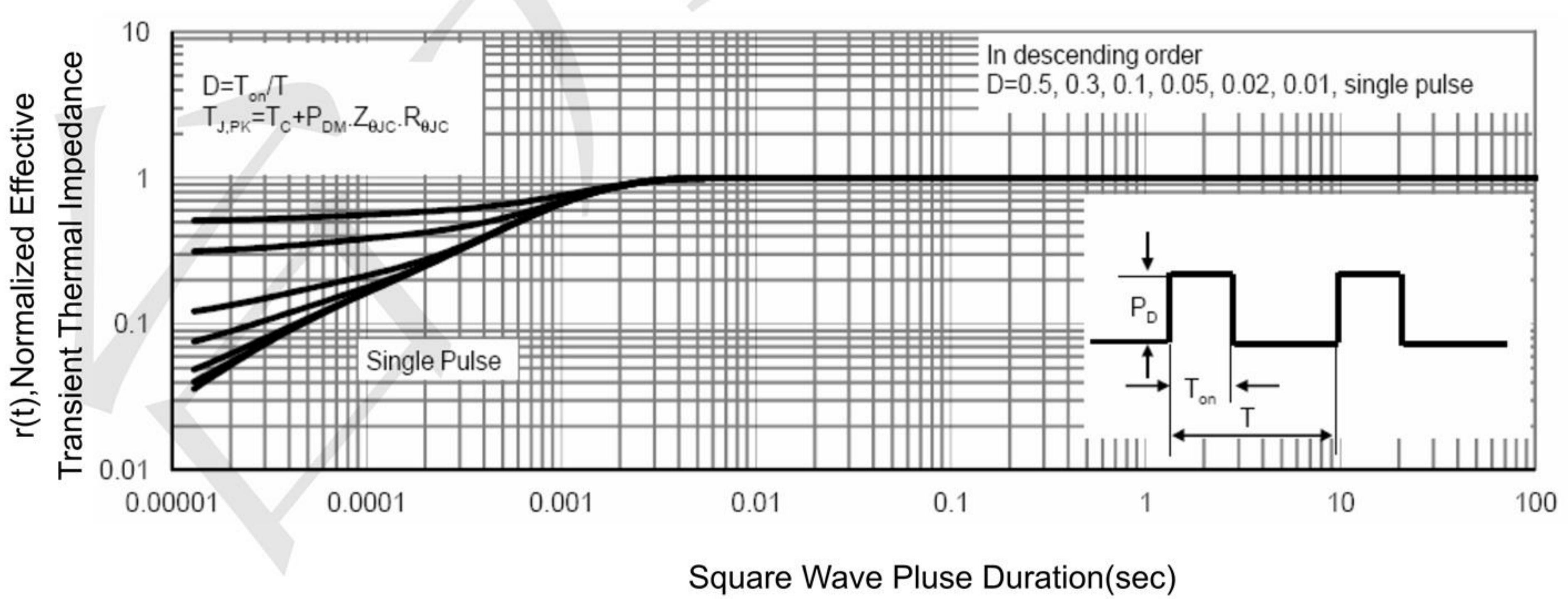


Figure 11 Normalized Maximum Transient Thermal Impedance



DFN3X3 Package Information

