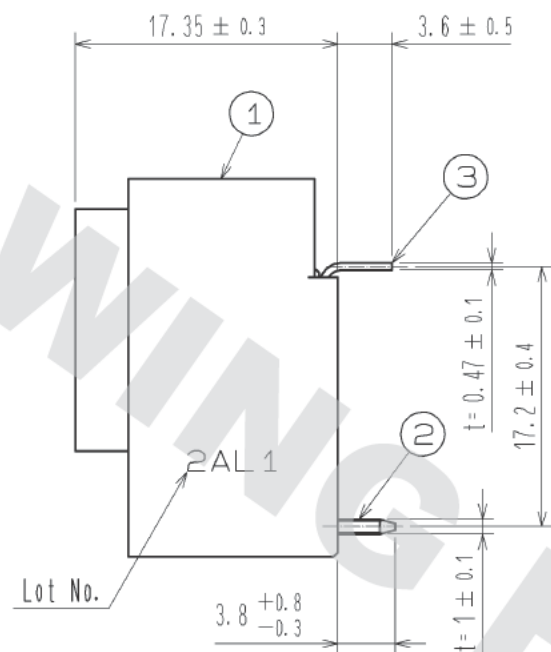
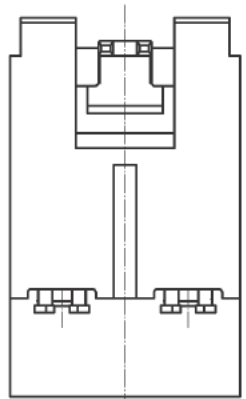
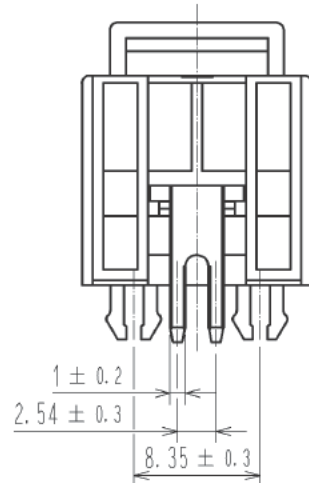
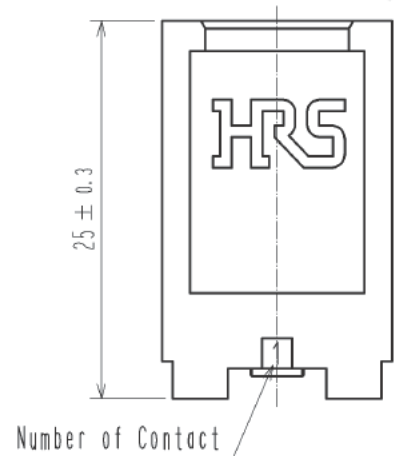
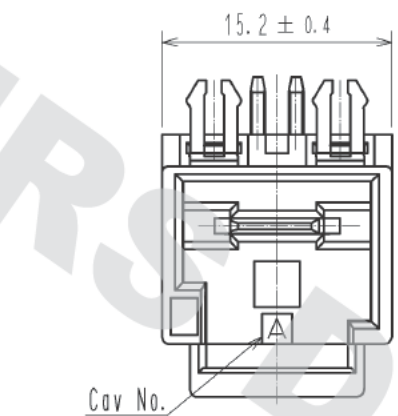
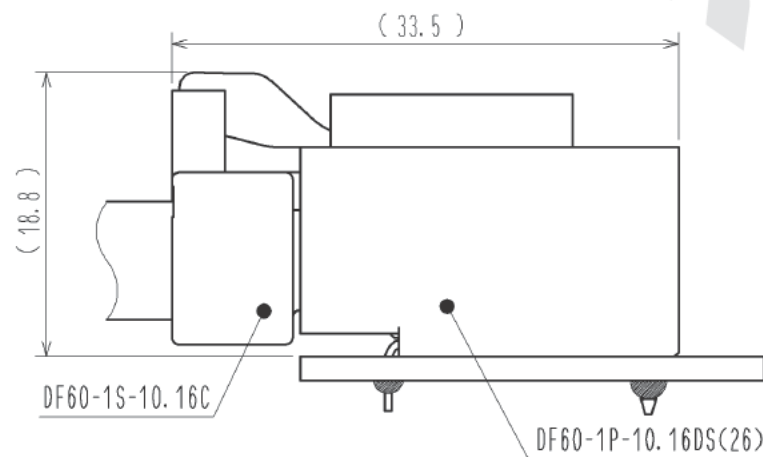


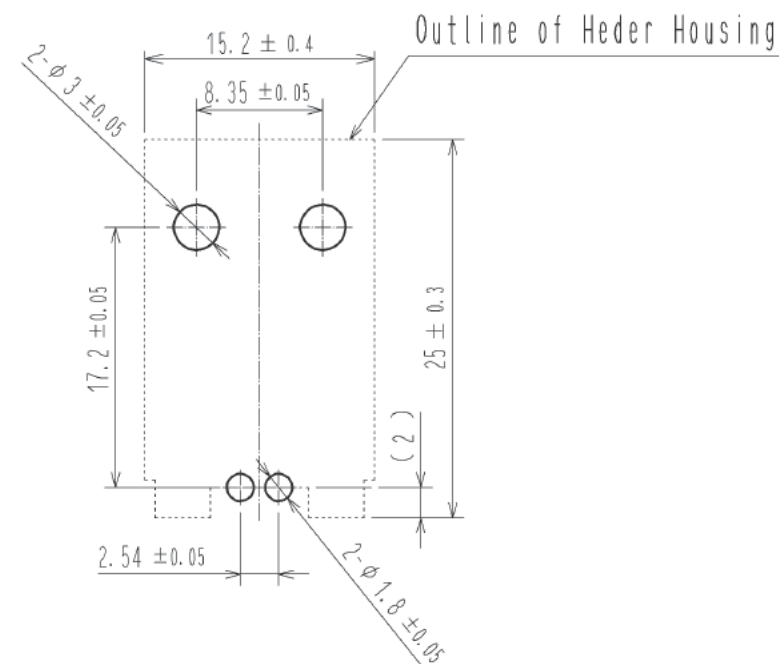
DRAWING FOR REFERENCE: This is subject to change without notice
05/31/2013



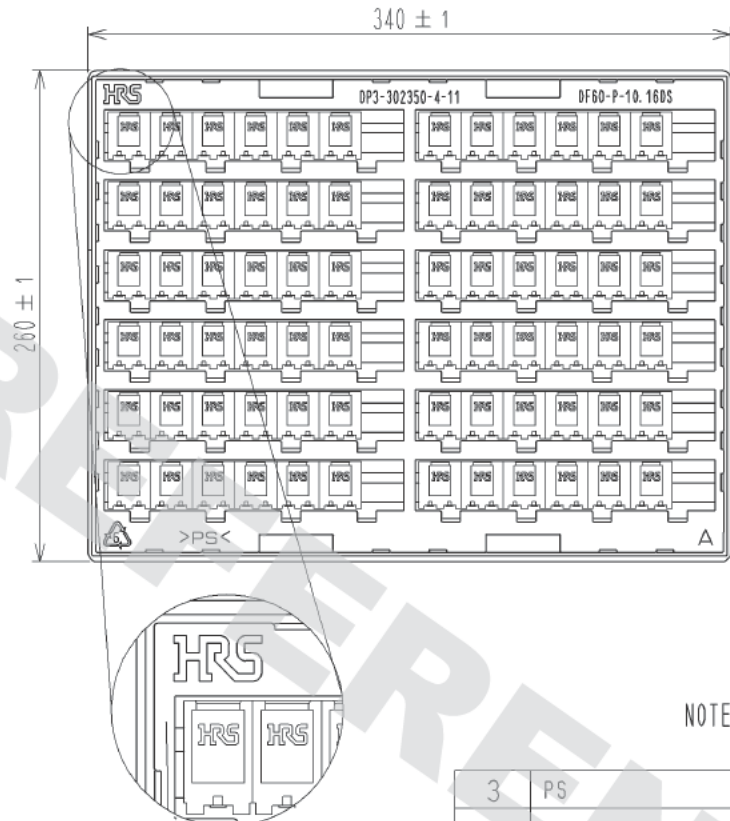
Application Diagram



Recommended PCB Layout
(Board thickness : t=1.6±0.1mm)



Packing Diagram(FREE)



NOTE 1. Per TRAY : 120 Pieces.

1	PBT	BLACK, UL94-V0			Under Plating : Ni Plating 1μm min			
NO.	MATERIAL	FINISH , REMARKS		NO.	MATERIAL	FINISH , REMARKS		
UNITS mm		SCALE 2 : 1	COUNT 	DESCRIPTION OF REVISIONS		DESIGNED	CHECKED	DATE
 HIROSE ELECTRIC CO., LTD.		APPROVED : KI. AKIYAMA	12.05.29	DRAWING NO.			EDC3-342143-01	
		CHECKED : SZ. ONO	12.05.29	PART NO.			DF60-1P-10.16DS(26)	
		DESIGNED : TH. YOSHIKAWA	12.05.28	CODE NO.			CL680-3015-1-26	
		DRAWN : TH. YOSHIKAWA	12.05.28				 1/1	