

Descriptions

The SGM3798YG/TR is an audio headset analog switch that is used to detect 3.5mm accessories and switch sleeve and ring by external controller. The ground signal is routed through a pair of low-impedance ground FETs ($75m\Omega$ typical), resulting minimal impact on audio cross-talk performance. The ground FETs of the device are designed to allow FM signal pass-through, making it possible to use the ground line of the headset as an FM antenna in mobile audio application. The SGM3798YG/TR is available in Green CSP-9 package.

Features

- Single supply range operating from 3.0V to 4.5V
- -118dB THD+N into 100kΩ load at 2Vrms
- -114dB THD+N into 32Ω load at 2Vrms
- Signal-to-Noise (SNR) Ratio 132dBA
- 100dB PSRR at 10kHz
- 145dB crosstalk & separation
- Pop/Click shunt circuit
- Audio Path Soft Turn-On/Off for Pop & Click Elimination

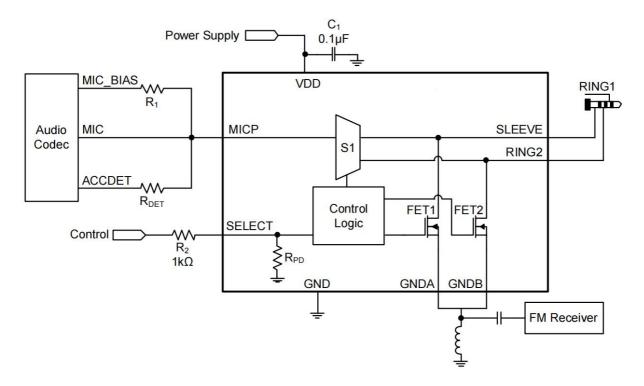
Applications

- Mobile Phones/Tablet PCs
- Notebook/Ultrabook Computers

Order Information

Part Number	Package		Quantity Per Reel	
SGM3798YG/TR	CSP-9	Tape and Reel	3000PCS	

Typical Application



Functional Diagram

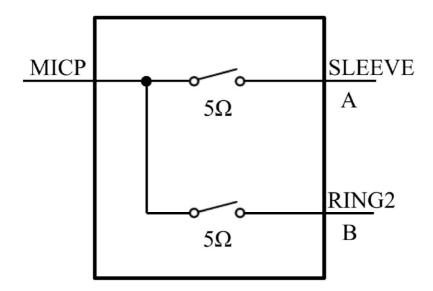
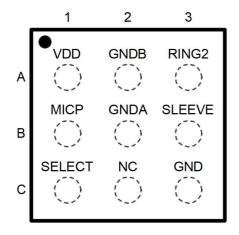


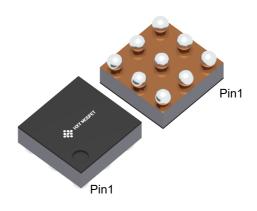
Fig.1 Functional Diagram



Pin Configuration







CSP-9

Pin Descriptions

PIN	NAME	I/O	FUN CTIO N	
A1	VDD	ı	Power Supply for the Chip.	
A2	GNDB	_	FET2 Ground Reference.	
А3	RING2	I/O	Connected to the RING2 Segment of the Jack. This pin will be routed to MICP or GNDB depending on the logic level of SELECT pin.	
B1	MICP	I/O	Microphone Signal Connection to Codec. Microphone bias should be fed into this pin.	
B2	GNDA	_	FET1 Ground Reference.	
В3	SLEEVE	I/O	Connected to the SLEEVE Segment of the Jack. This pin will be routed to MICP or GNDA depending on the logic level of SELECT pin.	
C1	SELECT	1	The Logic Signal Used to Control S1 Switch, FET1 and FET2.	
C2	NC	_	No Connection.	
СЗ	GND		Chip Ground Reference.	

Truth Table

Select	Function
0	MICP = A = SLEEVE, FET2 Turn On, FET1 Turn Off.
1	MICP = B = RING2, FET2 Turn Off, FET1 Turn On.

Absolute Maximum Ratings (1)

Parameter	Symbol	Value	Unit
Supply Voltage	Vcc	-0.3 ~6	V
Voltage Range on SELECT, MICP, RING2, SLEEVE	V _{IS}	-0.3 ~ VDD+0.3V	V
Storage Temperature Range	T _{STG}	-55 ~ 150	°C
Junction Temperature	TJ	150	°C
Lead Temperature (Soldering, 10 seconds)	TL	260	°C
Thermal Resistance	Reja	80	^o C/W

Recommend operating ratings (2)

Parameter	Symbol	Value	Unit
Supply Voltage	Vcc	2.6 ~ 5.0	V
Input/Output Voltage Range	V _{IN}	0.0 ~ 3.3	V

Note:

- "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation
 of the device at these or any other conditions beyond those indicated in the operational sections of this specification
 is not implied.
- 2. The input and output negative voltage ratings may be exceeded if the input and output diode current ratings are observed.



DC Electronics Characteristics

 $(V_{DD} = 2.6 \text{V to } 5.0 \text{V}$, typical values are at $V_{DD} = 3.3 \text{V}$, $T_A = +25 ^{\circ}\text{C}$, unless otherwise noted.)

Parameter	Symbol	Conditions	Min	Тур	Max	Units
Input Voltage Range	VDD		2.6		5.0	V
Quiescent Current	IQ	VDD = 4.5V, VMICP = 1.8V to VDD, SELECT = Low or SELECT = High (after detection)		4		μΑ
Input/Output Voltage Range	VIO		0		3.3	V
		VDD = 2.6V	1.1		VDD	
Input Logic High for SELECT	VI	VDD = 3.3V	1.1		VDD	V
	Н	VDD = 4.5V	1.3		VDD	
		VDD = 2.6V	0		0.7	
Input Logic Low for SELECT	VI	V _{DD} = 3.3V	0		8.0	V
	L	VDD = 4.5V	0		8.0	
Pull Down Resistor of Select Pin	RPD			560		kΩ
FET1 On Resistance	RF1	VDD = 2.6V, VGND = 0V,		75		
FET2 On Resistance	RF2	IGND = 10mA		75		mΩ
S1 On Resistance (Closed to A)	RS1A	VDD = 2.6V, VSLEEVE/RING2 = 0V to 2.6V,		5		
S1 On Resistance (Closed to B)	RS1B	IMIC = ±10mA		5		Ω

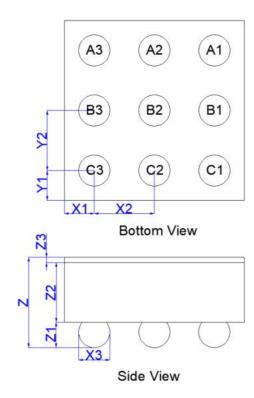
Note:

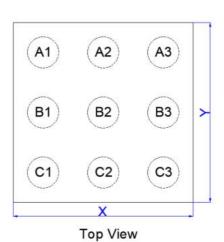
⁽¹⁾ Flatness is defined as the difference between maximum and minimum value of ON-resistance at the specified analog signal voltage points.

⁽²⁾ R_{ON} matching between channels is calculated by subtracting the channel with the lowest max Ron value from the channel with the highest max Ron value.

Package outline dimensions

CSP-9





Comple of	Dimensions In Millimeter				
Symbol	Min.	Тур.	Max.		
Х	1.145	1.17	1.195		
Y	1.145	1.17	1.195		
X1	-	0.185	-		
X2		0.400	-		
Х3	0.245	0.270	0.295		
Y1	-	0.185	-		
Y2	-	0.400	-		
Z	0.535	0.575	0.615		
Z1	0.170	0.195	0.22		
Z2	0.345	0.355	0.365		
Z3	0.02	0.025	0.03		



Attention

- Any and all HUA XUAN YANG ELECTRONICS products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your HUA XUAN YANG ELECTRONICS representative nearest you before using any HUA XUAN YANG ELECTRONICS products described or contained herein in such applications.
- HUA XUAN YANG ELECTRONICS assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all HUA XUAN YANG ELECTRONICS products described or contained herein.
- Specifications of any and all HUA XUAN YANG ELECTRONICS products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- HUA XUAN YANG ELECTRONICS CO.,LTD. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all HUA XUAN YANG ELECTRONICS products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of HUA XUAN YANG ELECTRONICS CO.,LTD.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production.

 HUA XUAN YANG ELECTRONICS believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the HUA XUAN YANG ELECTRONICS product that you intend to use.