

## **Description**

The HXY30N06DF uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

#### **General Features**

 $V_{DS} = 60V I_{D} = 15A$ 

 $R_{DS(ON)}$  < 40m $\Omega$  @  $V_{GS}$ =10V

### **Application**

Battery protection

Load switch

Uninterruptible power supply

## **Package Marking and Ordering Information**

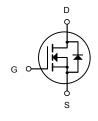
Product ID	Pack	Brand	Qty(PCS)
HXY30N06DF	DFN3X3-8L	HXY MOSFET	5000

## Absolute Maximum Ratings (T<sub>C</sub>=25 ℃unless otherwise noted)

Symbol	Parameter	Rating	Units
VDS	Drain-Source Voltage	60	V
Vgs	Gate-Source Voltage	±20	V
ID@T <sub>A</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	15	А
In@Ta=70°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	11	А
Ірм	Pulsed Drain Current <sup>2</sup>	46	A
EAS	Single Pulse Avalanche Energy <sup>3</sup>	25.5	mJ
las	Avalanche Current	20	А
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation⁴	34.7	W
Тѕтс	Storage Temperature Range	-55 to 175	°C
TJ	Operating Junction Temperature Range	-55 to 175	°C
R <sub>θ</sub> JA	Thermal Resistance Junction-Ambient <sup>1</sup>	62	°C/W



DFN3X3-8L



N-Channel MOSFET

# Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise noted)

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	60	-	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{DS}$ =60V, $V_{GS}$ = 0V,	-	-	1.0	μΑ
I <sub>GSS</sub>	Gate to Body Leakage Current	$V_{DS}$ =0V, $V_{GS}$ = ±20V	-	-	±100	nA
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	1.0	1.6	2.5	V
П	Static Drain-Source on-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =5A	-	28	40	0
$R_{DS(on)}$	note3	V <sub>GS</sub> =4.5V, I <sub>D</sub> =3A	-	36	50	mΩ
C <sub>iss</sub>	Input Capacitance	\\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	_	1148	-	pF
Coss	Output Capacitance	$V_{DS}$ =25V, $V_{GS}$ =0V, f=1.0MHz	-	58.5	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		-	49.4	-	pF
Qg	Total Gate Charge	V <sub>DS</sub> =30V, I <sub>D</sub> =2.5A, V <sub>GS</sub> =10V	-	20.3	-	nC
Q <sub>gs</sub>	Gate-Source Charge		-	3.7	-	nC
Q <sub>gd</sub>	Gate-Drain("Miller") Charge		-	5.3	-	nC
t <sub>d(on)</sub>	Turn-on Delay Time		-	7.6	-	ns
t <sub>r</sub>	Turn-on Rise Time	V <sub>DS</sub> =30V, I <sub>D</sub> =5A,	-	20	-	ns
$t_{d(off)}$	Turn-off Delay Time	R <sub>G</sub> =1.8Ω, V <sub>GS</sub> =10V	-	15	-	ns
t <sub>f</sub>	Turn-off Fall Time		-	24	-	ns
Is	Maximum Continuous Drain to Source Current	Diode Forward	_	-	5	Α
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		-	-	15	Α
\/.	Drain to Source Diode Forward	V <sub>GS</sub> =0V, I <sub>S</sub> =5A			1.2	V
$V_{SD}$	Voltage	VGS-UV, IS-DA	-	-		
trr	Body Diode Reverse Recovery Time		-	29	-	ns
Qrr	Body Diode Reverse Recovery Charge	I <sub>F</sub> =5A, dI/dt=100A/µs	-	43	-	nC

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

- 2. EAS condition : T<sub>J</sub>=25  $^{\circ}$ C,V<sub>DD</sub>=30V,V<sub>G</sub>=10V,L=0.5mH,Rg=25 $\Omega$ ,I<sub>AS</sub>=8.7A
- 3. Pulse Test: Pulse Width≤300µs, Duty Cycle≤0.5%



## **Typical Characteristics**

Figure1: Output Characteristics

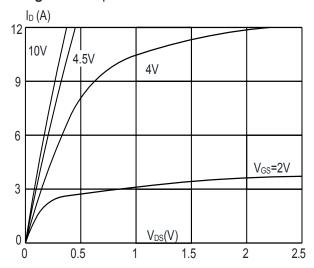


Figure 3:On-resistance vs. Drain Current

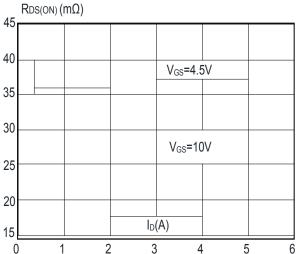


Figure 5: Gate Charge Characteristics

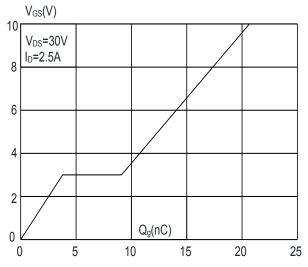


Figure 2: Typical Transfer Characteristics

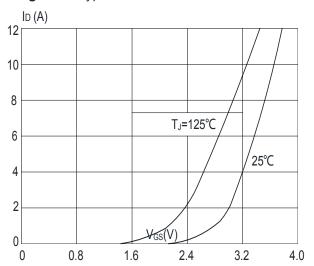


Figure 4: Body Diode Characteristics

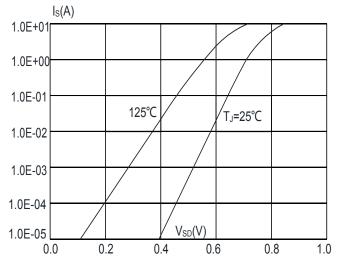
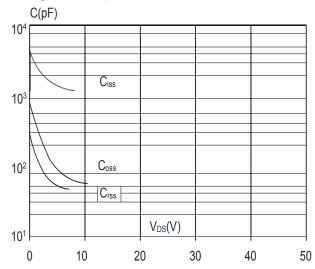
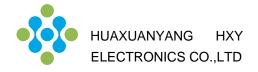


Figure 6: Capacitance Characteristics





**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature

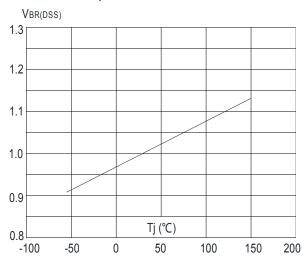


Figure 9: Maximum Safe Operating Area

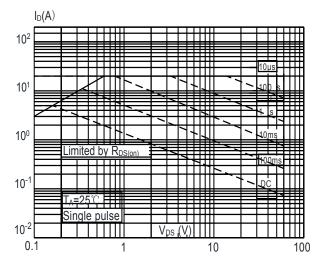
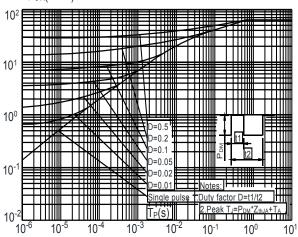
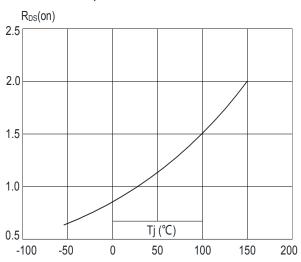


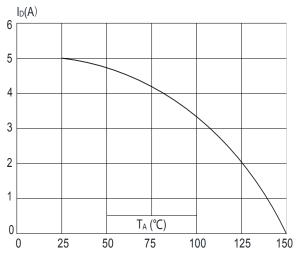
Figure.11: Maximum Effective
Transient Thermal Impedance, Junction-to-Ambient
Zth\_JA(°C/W)



**Figure 8:** Normalized on Resistance vs. Junction Temperature



**Figure 10:** Maximum Continuous Drain Current vs. Ambient Temperature



### **Test Circuit**

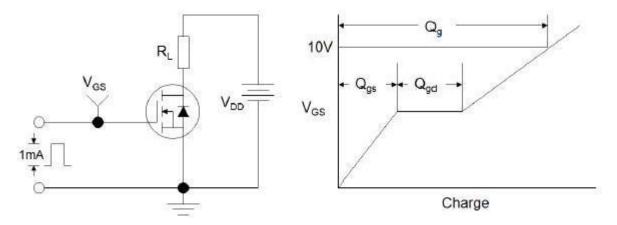


Figure1:Gate Charge Test Circuit & Waveform

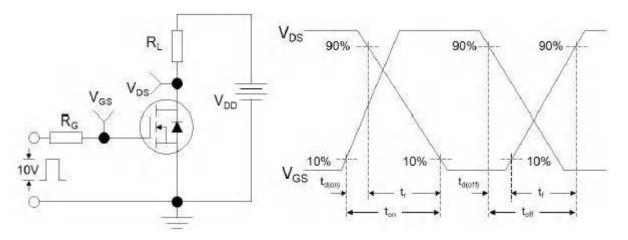


Figure 2: Resistive Switching Test Circuit & Waveforms

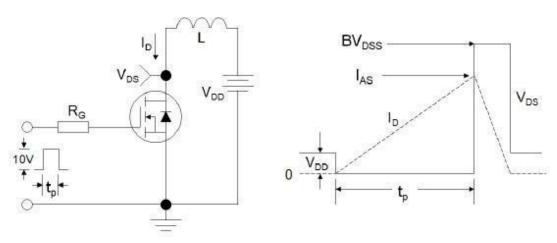
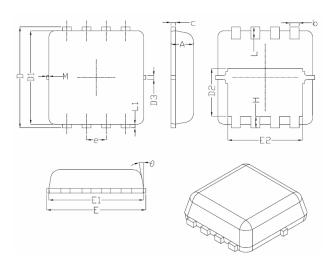


Figure 3:Unclamped Inductive Switching Test Circuit & Waveforms



# **DFN3X3-8L Package Information**



Symbol	Dimensions In Millimeters			
	Min.	Nom.	Max.	
A	0.70	0.75	0.80	
b	0.25	0.30	0.35	
С	0.10	0.15	0.25	
D	3.25	3.35	3.45	
D1	3.00	3.10	3.20	
D2	1.48	1.58	1.68	
D3	+	0.13	-	
E	3.20	3.30	3.40	
E1	3.00	3.15	3.20	
E2	2.39	2.49	2.59	
е	0.65BSC			
Н	0.30	0.39	0.50	
L	0.30	0.40	0.50	
L1	-	0.13	-	
М	*	*	0.15	
θ		10°	12 <sup>°</sup>	



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