RoHS



JLPI75B120RN2F7SN

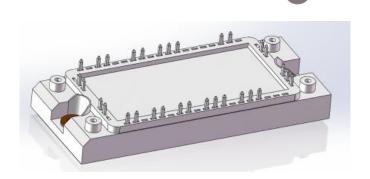
LN2 PACK module with NCE Gen.7 Trench/Fieldstop IGBT and Emitter Controlled diode and NTC

Features

- · Electrical features
- VCES = 1200 V
- IC nom = 75 A / ICRM = 150 A
- Low V_{CEsat}
- Overload operation up to 175°C
- · Mechanical features
- High power and thermal cycling capability
- Integrated NTC temperature sensor
- Copper base plate
- Al₂O₃ substrate with low thermal resistance

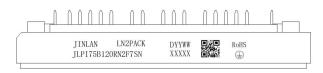
Typical Applications

- · Auxiliary inverters
- Motor drives
- Servo drives



LN2 Pack

MARKING DIAGRAM



JINLAN

= Company Name

JLPI75B120RN2F7SN

= Specific Device Code

YYWW

= Year and Work Week Code

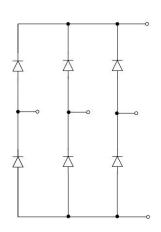
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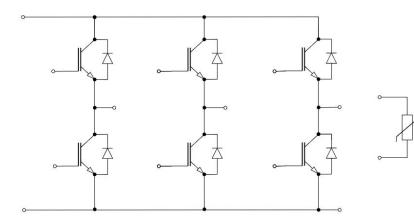
= Serial Number

QR code

= Custom Assembly Information

Description







Package Insulation coordination

Parameter	Symbol	Note or test condition	Values	Unit
Isolation test voltage	V _{ISOL}	RMS,f=50Hz,t=60s	2.5	kV
Internal isolation		basic insulation(class 1,IEC 61140)	Al ₂ O ₃	
Creepage distance	d _{creep}	terminal to heatsink	10.0	mm
Clearance	d _{clear}	terminal to heatsink	7.5	mm
Comparative tracking index (electrical)	СТІ		>200	
RTI Elec.	RTI	housing	140	°C

Package Characteristic values

		Values				
Parameter	Symbol	Note or test condition	Min.	Тур.	Max.	Unit
Stray Inductance	LCE			35		nH
Module Lead Resistance, Terminal to Chip	Raa'+cc'	T _C =25 [°] C, per switch		5.6		mΩ
Module Lead Resistance, Terminal to Chip	R _{CC'+EE'}	T _C =25℃, per switch		5.3		mΩ
Storage Temperature Range	T _{STG}		-40		125	$^{\circ}$
Mounting Torque, Screw M5	М	M5, Screw	3		6	N.m
Weight	G			180		g



IGBT, Inverter

Absolute Maximum Ratings (Tc = 25°C unless otherwise noted)

Symbol	Description	Value	Unit
V _{CES}	Collector-Emitter Voltage	1200	V
V _{GES}	Gate-Emitter Voltage ±30		V
	Collector Current @ T _C =25 ℃	150	Α
Ic	Collector Current @ Tc=80 ℃	75	Α
Ісм	Pulsed Collector Current, t _p =1S 225		Α

Characteristics (Tc = 25°C unless otherwise noted)

Symbol	Parameter	Test Cond	lition	Min	Тур	Max	Unit
			T _{vj} =25°C		1.65	2.50	
$V_{\text{CE(sat)}}$	Collector-Emitter Saturation Voltage	I _C =75A,V _{GE} =15V	T _{vj} = 175 °C		1.95		V
$V_{\text{GE}(th)} \\$	Gate Threshold Voltage	I _C =1mA,V ₀	_{CE} =V _{GE}	5.0	6.0	7.0	V
I _{CES}	Collector-Emitter Leakage Current	V _{GE} =0V,V _{CE}	=1200V			100	uA
I_{GES}	Gate-Emitter Leakage Current	V _{GE} =30V,\	/ _{CE} =0V			200	nA
R _{Gint}	Internal Gate Resistance	f=1Mi	·lz		0.5		Ω
C _{ies}	Input Capacitance	V _{CE} =25V,V	/ _{GE} =0V		7.7		nF
Coes	Out Capacitance	1	MHz		0.17		nF
Cres	Reverse Transfer				0.04		nF
\mathbf{Q}_{G}	Gate Charge	V _{CE} =600V, I _C =5	0A,V _{GE} =15V		0.36		μC
t _{d(on)}	Turn-On Delay Time				90		
t _r	Rise Time	$\begin{array}{l} V_{\text{CC}} = 600 \text{ V, } I_{\text{C}} = 75 \text{ A,} \\ R_{\text{Gon}} = 10\Omega, R_{\text{Goff}} = 15\Omega \\ V_{\text{GE}} = \pm 15V \\ \text{Inductive Load} \\ T_{\text{Vj}} = 25^{\circ}\text{C} \end{array}$			61		ns
$t_{\text{d(off)}} \\$	Turn−off Delay Time				304		115
t _f	Fall Time				45		
Eon	Turn-On Switching Loss per Pulse				4.17		- m.l
E _{off}	Turn Off Switching Loss per Pulse				2.1		mJ
$t_{\text{d(on)}}$	Turn-On Delay Time				TBD		
t _r	Rise Time	\/ - 000 \/	I -75 A		TBD		ns
$t_{\text{d(off)}} \\$	Turn-off Delay Time	$V_{CC} = 600 \text{ V}$ $R_{Gon} = 10\Omega, F$	$R_{Goff} = 15\Omega$		TBD		113
t_f	Fall Time	V _{GE} =± Inductive			TBD		
E _{on}	Turn-on Switching Loss per Pulse	T _{vj} = 17	′5°C		TBD		
E _{off}	Turn Off Switching Loss per Pulse				TBD		mJ
Isc	SC Data	t _P ≤10μs,V _G τ _{νj} ≤150℃,V V _{CEmax} =V _{CES}	_{CC} =800V,		330		А
R _{thJC}	Thermal resistance	Junction-to-Cas	e (per IGBT)		0.473		K/W
T _{vj op}		Temperature under sv	vitching conditions	-40		175 ¹⁾	$^{\circ}$

 $^{^{1)}}T_{\nu j\,op}$ > 150 $^{\circ}\!\mathrm{C}$ is only allowed for operation at overload conditions.



Diode, Inverter

Absolute Maximum Ratings (Tc = 25°C unless otherwise noted)

Symbol	Description	Value	Unit
V_{RRM}	Repetitive Peak Reverse Voltage	1200	V
I _F	Diode Continuous Forward Current	75	Α
I _{FM}	Diode Maximum Forward Current t _p =1ms	225	Α

Characteristics (Tc=25°C unless otherwise noted)

Symbol	Parameter	Test Co	ondition	Min	Тур	Max	Unit
.,	Diada Farrand Valtaria	I _F =75A	T _j =25°C		1.80	2.70	.,
V _F	Diode Forward Voltage	IF-73A	T _j =175°C		1.75		V
Qr	Recovered Charge				1.24		μC
I _{RM}	Peak Reverse Recovery Current	I _F =75A,I	R _q =25Ω,	-	27		Α
T _{rr}	Reverse Recovery Time	T _j =25°C			175		ns
E _{rec}	Reverse Recovery Energy				1.23		mJ
Qr	Recovered Charge	I _F =75A,R _g =25Ω,		I	TBD		μC
I _{RM}	Peak Reverse Recovery Current				TBD		Α
T _{rr}	Reverse Recovery Time	T _j =1	75°C		TBD		ns
E _{rec}	Reverse Recovery Energy			1	TBD	-	mJ
RthJC	Thermal resistance	Junction-to-Ca	ase (per diode)		0.79		K/W
T _{vj op}		Temperature under	switching conditions	-40		175 ²⁾	$^{\circ}$

Diode, Rectifier

Absolute Maximum Ratings (Tc = 25°C unless otherwise noted)

Symbol	Description	Value	Unit
V _{RRM}	Repetitive Peak Reverse Voltage	1600	V
I _F	Diode Continuous Forward Current	75	Α
I _{FM}	Diode Maximum Forward Current t _p =1ms	150	Α

Characteristics (Tc=25°C unless otherwise noted)

Symbol	Parameter	Test Condition	Min	Тур	Max	Unit
V _F	Diode Forward Voltage	I _F = 75A, T _j = 150 °C		0.85		V
I _R	Reverse Current	T _j =175 °C, V _R =1600V		1.0		mA
RthJC	Thermal resistance	Junction-to-Case (per diode)		0.54		K/W
T _{vj op}		Temperature under switching conditions	-40		175 ³⁾	$^{\circ}$ C

 $^{^{3)}}T_{\nu j\,op}$ > 150 $^{\circ}{\rm C}$ is only allowed for operation at overload conditions.

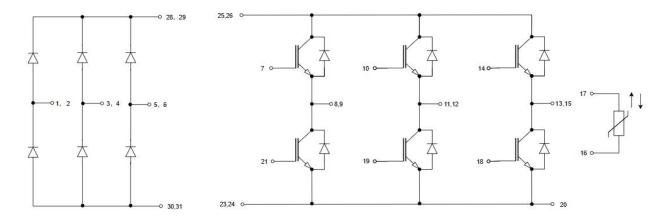


NTC Characteristics (Tc = 25°C unless otherwise noted)

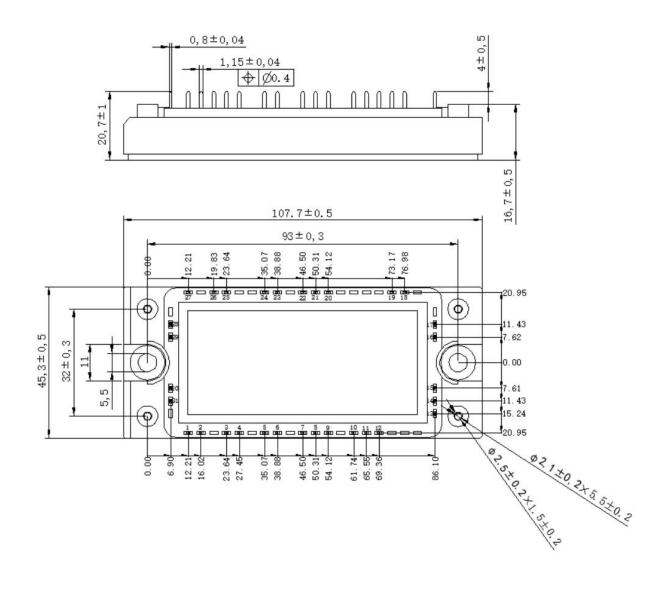
Symbol	Parameter	Test Condition	Min	Тур	Max	Unit
R ₂₅	Rated Resistance			5.0		kΩ
ΔR/R	Deviation of R100	Tc=100 ℃,R100=493.3Ω	-5		5	%
P ₂₅	Power Dissipation				20.0	mW
B _{25/50}	B-value	R ₂ =R ₂₅ exp[B _{25/50} (1/T ₂ - 1/(298.15K))]		3375		К
B _{25/80}	B-value	R ₂ =R ₂₅ exp[B _{25/80} (1/T ₂ - 1/(298.15K))]		3411		K
B _{25/100}	B-value	R ₂ =R ₂₅ exp[B _{25/100} (1/T ₂ - 1/(298.15K))]		3433		К



CIRCUIT DIAGRAM



PACKAGE DIMENSION





REVISION HISTORY

Document version	Date of release	Description of changes
Rev.00	2025-01-16	Preview



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