

# **Description**

The NVMFS5C673NLAFT1G uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

#### **General Features**

V<sub>DS</sub> = 60V I<sub>D</sub> =65A

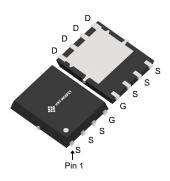
 $R_{DS(ON)} < 11 m\Omega V_{GS} = 10 V$ 

### **Application**

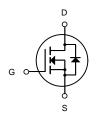
Battery protection

Load switch

Uninterruptible power supply



DFN5X6-8L



N-Channel MOSFET

### **Package Marking and Ordering Information**

Product ID	Pack	Brand	Qty(PCS)
NVMFS5C673NLAFT1G	DFN5X6-8L	HXY MOSFET	5000

## Absolute Maximum Ratings (T<sub>c</sub>=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units	
V <sub>D</sub> s	Drain-Source Voltage	60	V	
Vgs	Gate-Source Voltage ±20			
I <sub>D</sub> @T <sub>C</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V	А		
I <sub>D</sub> @T <sub>C</sub> =70°C	Continuous Drain Current, V <sub>GS</sub> @ 10V	А		
Ідм	Pulsed Drain Current <sup>2</sup>	180	А	
EAS	Single Pulse Avalanche Energy <sup>3</sup>	56	mJ	
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation <sup>4</sup>	89	W	
Тѕтс	Storage Temperature Range	-55 to 150	°C	
TJ	Operating Junction Temperature Range	-55 to 150	°C	
Reja	Thermal Resistance Junction-Ambient <sup>1</sup> 62		°C/W	

N-Channel Enhancement Mode MOSFET

## Electrical Characteristics (T<sub>J</sub>=25°C unless otherwise specified)

Symbol	Parameter Test Condition		Min.	Тур.	Max.	Units	
Off Charac	teristic						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250µA	60	_	-	V	
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =60V, V <sub>GS</sub> =0V,	-	-	1.0	μA	
I <sub>GSS</sub>	Gate to Body Leakage Current	V <sub>DS</sub> =0V, V <sub>GS</sub> = ±20V	-	-	±100	nA	
On Charac	teristics						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	1.0	1.6	2.5	V	
	Static Drain-Source on-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	-	8	11	mΩ	
$R_{DS(on)}$		V <sub>GS</sub> =4.5V, I <sub>D</sub> =10A	-	14	20		
Dynamic C	Characteristics						
C <sub>iss</sub>	Input Capacitance	)/ OF)/ )/ O)/	_	930	-	pF	
Coss	Output Capacitance	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V,	-	370	-	pF	
C <sub>rss</sub>	Reverse Transfer Capacitance	f=1.0MHz	-	20	-	pF	
Qg	Total Gate Charge	V <sub>DS</sub> =30V, I <sub>D</sub> =20A, V <sub>GS</sub> =10V	-	19	-	nC	
Q <sub>gs</sub>	Gate-Source Charge		-	4.8	-	nC	
$Q_{gd}$	Gate-Drain("Miller") Charge	VGS-10V	-	4.5	-	nC	
Switching	Characteristics						
$t_{d(on)}$	Turn-on Delay Time	V <sub>DD</sub> =30V, I <sub>D</sub> =20A, R <sub>G</sub> =1.6Ω, V <sub>GS</sub> =10V	_	4.9	-	ns	
t <sub>r</sub>	Turn-on Rise Time		-	31	-	ns	
t <sub>d(off)</sub>	Turn-off Delay Time		-	23	-	ns	
t <sub>f</sub>	Turn-off Fall Time		-	8.7	-	ns	
Drain-Sou	rce Diode Characteristics and Maximu	um Ratings					
Is	Maximum Continuous Drain to Source Diode Forward Current		-	-	65	А	
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		-	-	240	Α	
$V_{\text{SD}}$	Drain to Source Diode Forward Voltage	V <sub>GS</sub> =0V, I <sub>S</sub> =30A	-	-	1.4	V	
t <sub>rr</sub>	Body Diode Reverse Recovery Time	T 0500	-	34	-	ns	
Qrr	Body Diode Reverse Recovery Charge	T <sub>J</sub> =25°C, I <sub>F</sub> =20A,dI/dt=100A/μs	-	14	-	nC	

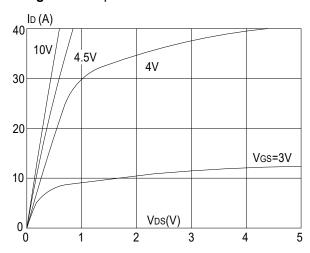
Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

- 2. EAS condition:  $T_J$ =25°C,  $V_{DD}$ =30V,  $V_G$ =10V,  $R_G$ =25 $\Omega$ , L=0.5mH,  $I_{AS}$ =12A
- 3. Pulse Test: Pulse Width≤300µs, Duty Cycle≤0.5%



### **Typical Performance Characteristics**

Figure1: Output Characteristics



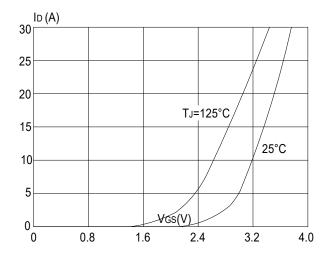


Figure 2: Typical Transfer Characteristics

Figure 3:On-resistance vs. Drain Current

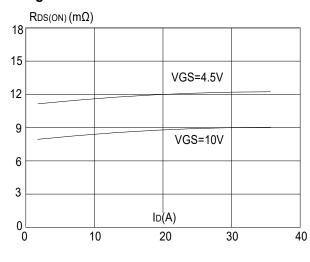


Figure 4: Body Diode Characteristics

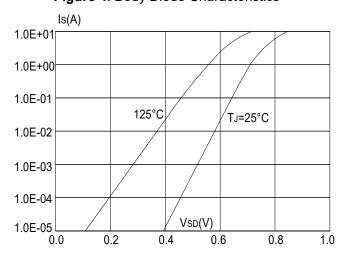


Figure 5: Gate Charge Characteristics

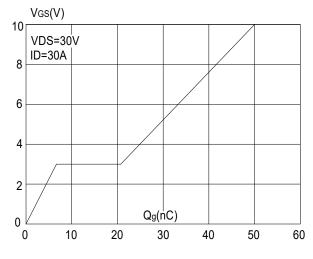
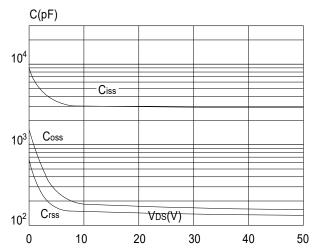


Figure 6: Capacitance Characteristics





**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature

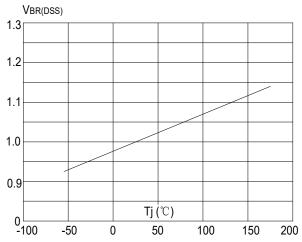
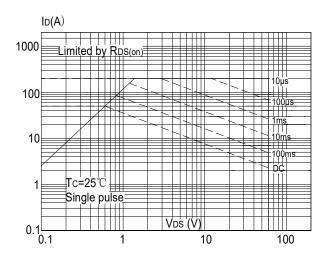
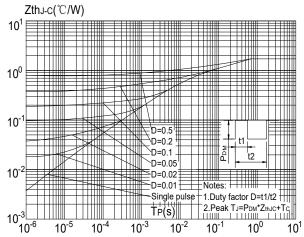


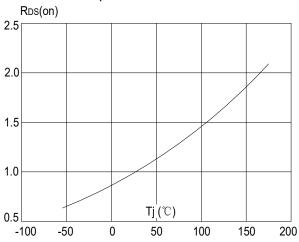
Figure 9: Maximum Safe Operating Area



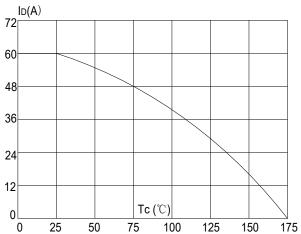
**Figure.11:** Maximum Effective Transient Thermal Impedance, Junction-to-Case



**Figure 8:** Normalized on Resistance vs. Junction Temperature

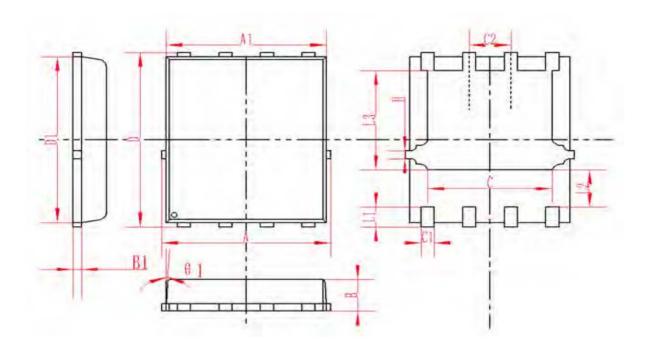


**Figure 10:** Maximum Continuous Drain Current vs. Case Temperature



### N-Channel Enhancement Mode MOSFET

# **DFN5X6-8L Package Information**



SYMBOL	MM		INCH			
	MIN	NOM	MAX	MIN	NOM	MAX
А	4.95	5	5.05	0.195	0.197	0.199
A1	4.82	4.9	4.98	0.190	0.193	0.196
D	5.98	6	6.02	0.235	0.236	0.237
D1	5.67	5.75	5.83	0.223	0.226	0.230
В	0.9	0.95	1	0.035	0.037	0.039
B1	0.254REF		0.010REF			
С	3.95	4	4.05	0.156	0.157	0.159
C1	0.35	0.4	0.45	0.014	0.016	0.018
C2	1.27TYP		0.5TYP			
θ1	8°	10°	12°	8°	10°	12°
L1	0.63	0.64	0.65	0.025	0.025	0.026
L2	1.2	1.3	1.4	0.047	0.051	0.055
L3	3.415	3.42	3.425	0.134	0.135	0.135
Н	0.24	0.25	0.26	0.009	0.010	0.010

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