

Description

The SX80P04NF uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

General Features

$V_{DS} = -40V$ $I_D = -80A$

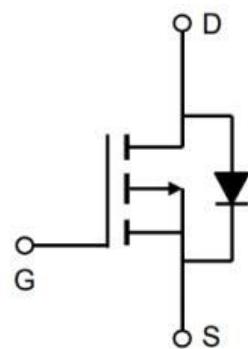
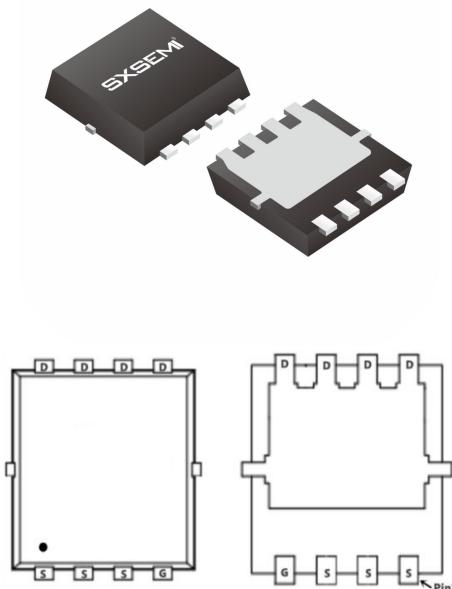
$R_{DS(ON)} < -10m\Omega$ @ $V_{GS} = -10V$

Application

Battery protection

Load switch

Uninterruptible power supply

PDFN5*6-8L**Absolute Maximum Ratings (TC=25°C unless otherwise noted)**

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-40	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D @ T_c = 25^\circ C$	Continuous Drain Current, $V_{GS} @ -10V^1$	-80	A
$I_D @ T_c = 100^\circ C$	Continuous Drain Current, $V_{GS} @ -10V^1$	-56	A
I_{DM}	Pulsed Drain Current ²	-280	A
EAS	Single Pulse Avalanche Energy ³	500	mJ
I_{AS}	Avalanche Current	-50	A
$P_D @ T_c = 25^\circ C$	Total Power Dissipation ⁴	52.1	W
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹	25	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	2.4	°C/W

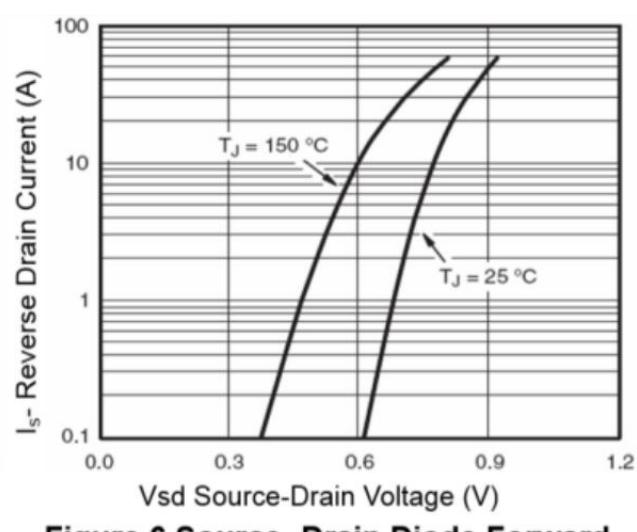
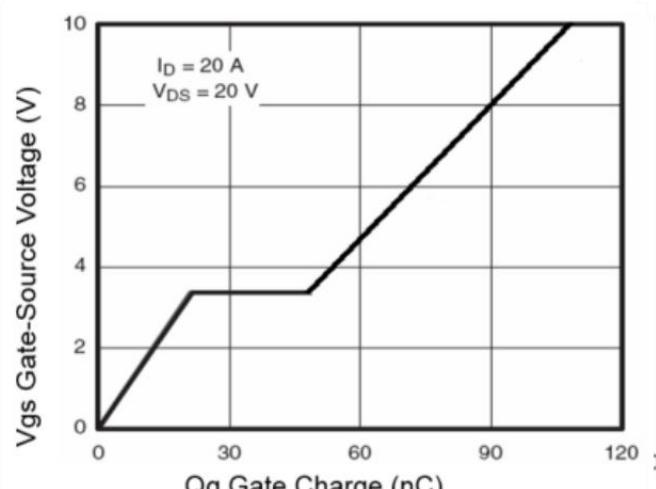
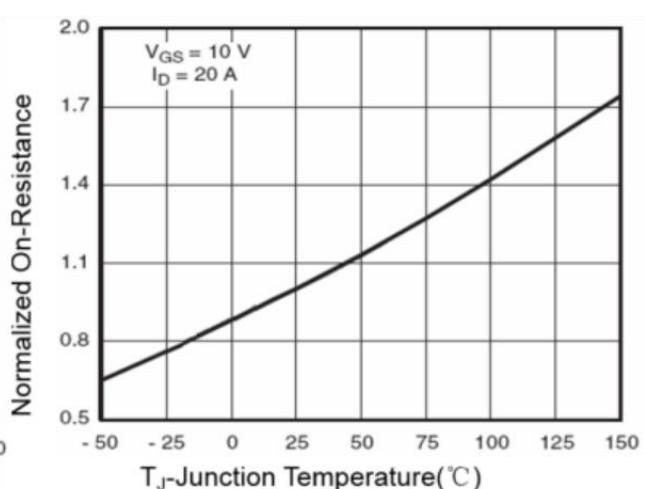
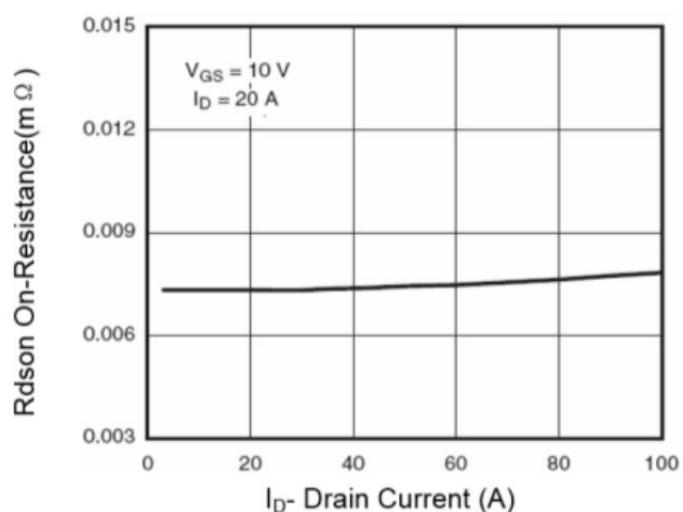
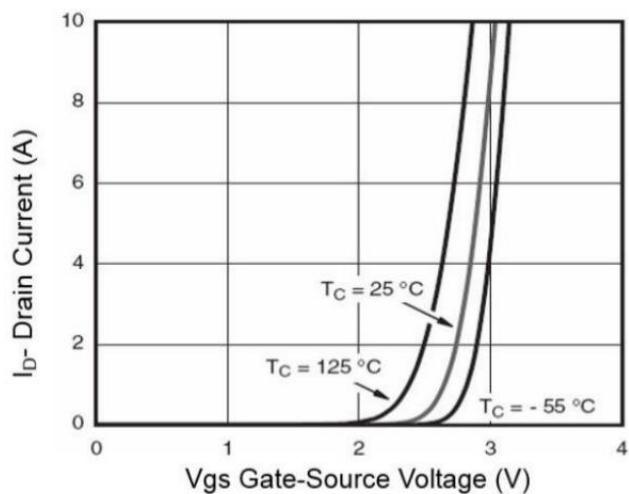
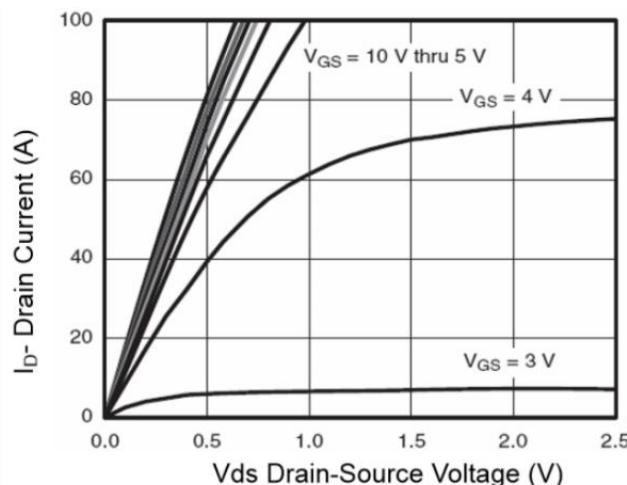
Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=-250\mu\text{A}$	-40	-44	---	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to 25°C , $I_D=-1\text{mA}$	---	-0.023	---	$\text{V}/^\circ\text{C}$
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance ²	$V_{\text{GS}}=-10\text{V}$, $I_D=-12\text{A}$	---	7.0	10	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}$, $I_D=-12\text{A}$	---	9.0	15	
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_D=-250\mu\text{A}$	-1.2	-1.8	-2.5	V
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=-40\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	1	uA
		$V_{\text{DS}}=-40\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=55^\circ\text{C}$	---	---	5	
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{\text{DS}}=-15\text{V}$, $I_D=-12\text{A}$	---	20	---	S
R_g	Gate Resistance	$V_{\text{DS}}=0\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	7	14	Ω
Q_g	Total Gate Charge (-4.5V)	$V_{\text{DS}}=-20\text{V}$, $V_{\text{GS}}=-10\text{V}$, $I_D=-12\text{A}$	---	27.9	---	nC
Q_{gs}	Gate-Source Charge		---	7.7	---	
Q_{gd}	Gate-Drain Charge		---	7.5	---	
$T_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DD}}=-20\text{V}$, $V_{\text{GS}}=-10\text{V}$, $R_g=3.0\Omega$, $I_D=-12\text{A}$	---	40	---	ns
T_r	Rise Time		---	35.2	---	
$T_{\text{d(off)}}$	Turn-Off Delay Time		---	100	---	
T_f	Fall Time		---	9.6	---	
C_{iss}	Input Capacitance	$V_{\text{DS}}=-20\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	6500	---	pF
C_{oss}	Output Capacitance		---	790	---	
C_{rss}	Reverse Transfer Capacitance		---	605	---	
I_s	Continuous Source Current ^{1,5}	$V_G=V_D=0\text{V}$, Force Current	---	---	-70	A
V_{SD}	Diode Forward Voltage ²	$V_{\text{GS}}=0\text{V}$, $I_s=-1\text{A}$, $T_J=25^\circ\text{C}$	---	---	-1.2	V

Note :

- 1、The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2、The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- 3、The EAS data shows Max. rating . The test condition is $V_{\text{DD}}=-32\text{V}$, $V_{\text{GS}}=-10\text{V}$, $L=0.1\text{mH}$, $I_{\text{AS}}=-50\text{A}$
- 4、The power dissipation is limited by 150°C junction temperature
- 5、The data is theoretically the same as ID and IDM , in real applications , should be limited by total power dissipation.

Typical Characteristics



Typical Characteristics

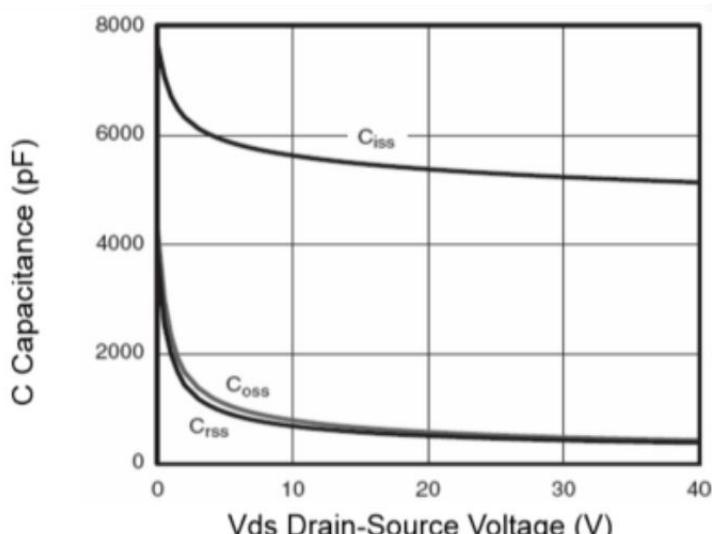


Figure 7 Capacitance vs Vds

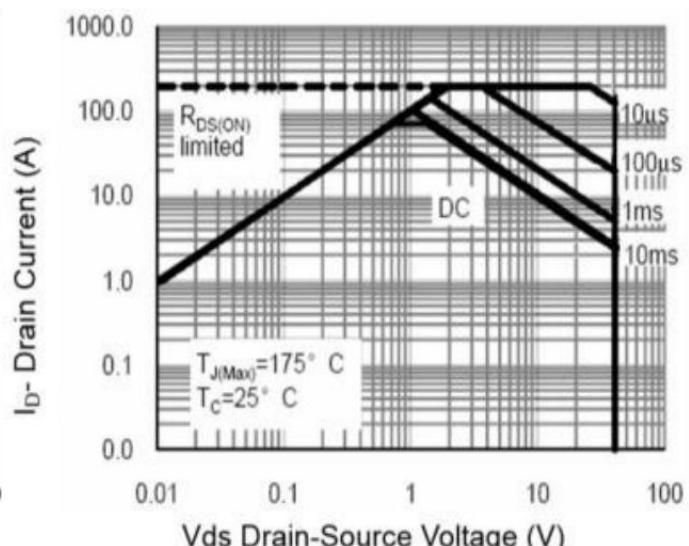


Figure 8 Safe Operation Area

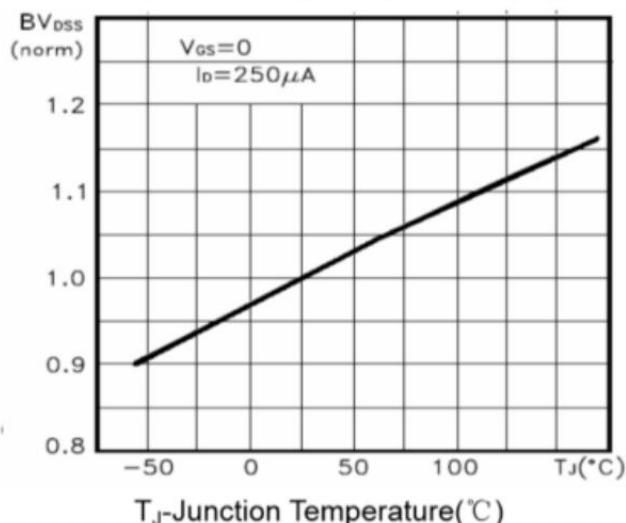


Figure 9 BV_{DSS} vs Junction Temperature

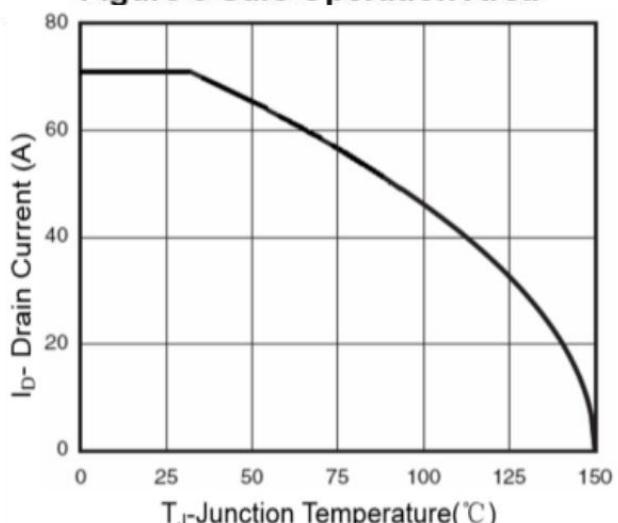


Figure 10 I_D Current Derating vs Junction Temperature

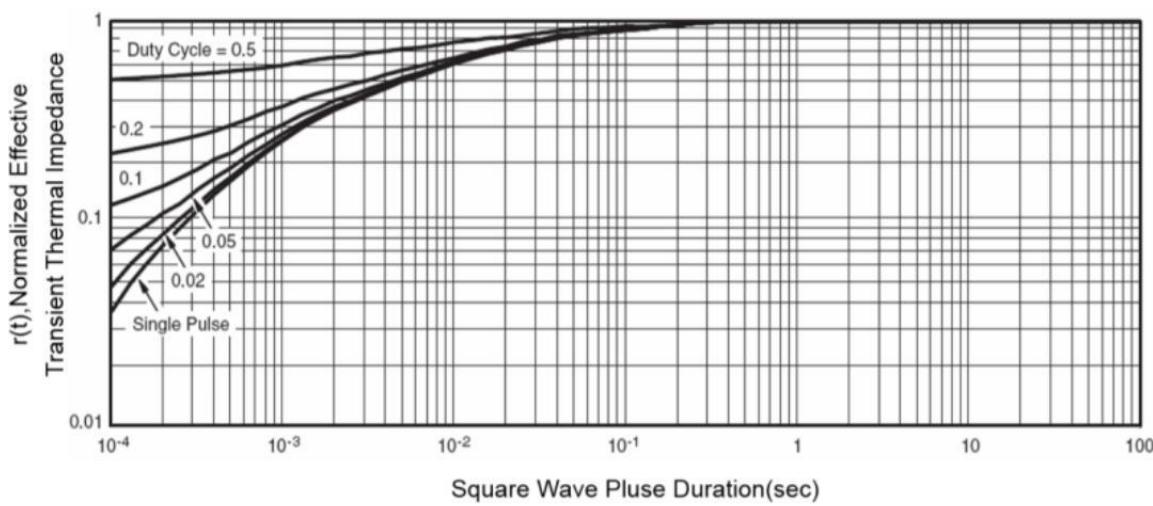
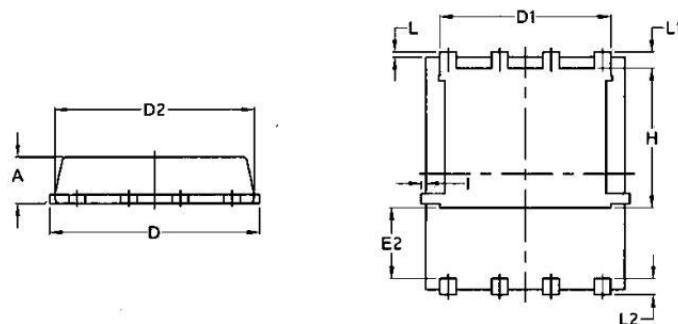
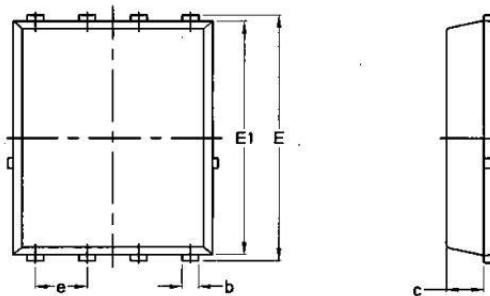


Figure 11 Normalized Maximum Transient Thermal Impedance

Package Mechanical Data- PDFN5*6-8L-JQ Single



Symbol	Common			
	mm		Inch	
	Mim	Max	Min	Max
A	1.03	1.17	0.0406	0.0461
b	0.34	0.48	0.0134	0.0189
c	0.824	0.0970	0.0324	0.082
D	4.80	5.40	0.1890	0.2126
D1	4.11	4.31	0.1618	0.1697
D2	4.80	5.00	0.1890	0.1969
E	5.95	6.15	0.2343	0.2421
E1	5.65	5.85	0.2224	0.2303
E2	1.60	/	0.0630	/
e	1.27 BSC		0.05 BSC	
L	0.05	0.25	0.0020	0.0098
L1	0.38	0.50	0.0150	0.0197
L2	0.38	0.50	0.0150	0.0197
H	3.30	3.50	0.1299	0.1378
I	/	0.18	/	0.0070

Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
TAPING	PDFN5*6-8L		5000