

Features

- Low On-resistance, $R_{on}=1.5\Omega$ when VCC =5V
- 1.8V Logic Compatible Control Pin
- High Off-Isolation: -100dB @ 100KHz
- COM+/- Overrides VCC to Achieve True Isolation Even When Supply Is Dead
- Low Channel-to-Channel Crosstalk: -97dB @ 100KHz
- High Bandwidth (-3dB @700MHz) Suitable For USB2.0 High-Speed Routing
- Low Quiescent Current (<2uA) With Very Wide Supply Range (1.5V ~ 5.5V)

Applications

- Mobile Phones, Tablets and Notebooks
- Portable media players or Personal media players

General Description

This is a high-speed dual channel analog switch, which has First disconnect, then connect, and bidirectional signal switching function. It can be used Create a dual 2:1 multiplexer or a 1:2 dual de multiplexer.

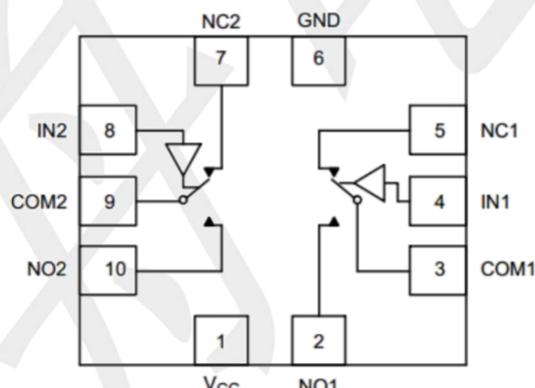
It provides extremely low on resistance, very low THD, and channels

Interstitial interference and high shutdown isolation. These characteristics make

It is suitable for audio signal transmission and switching applications.

The control logic supports 1.5V-5.5V CMOS logic levels. This logic interface can directly interface with various CPU and microcontrollers without increasing the power output current (ICC), thereby reducing Power consumption.

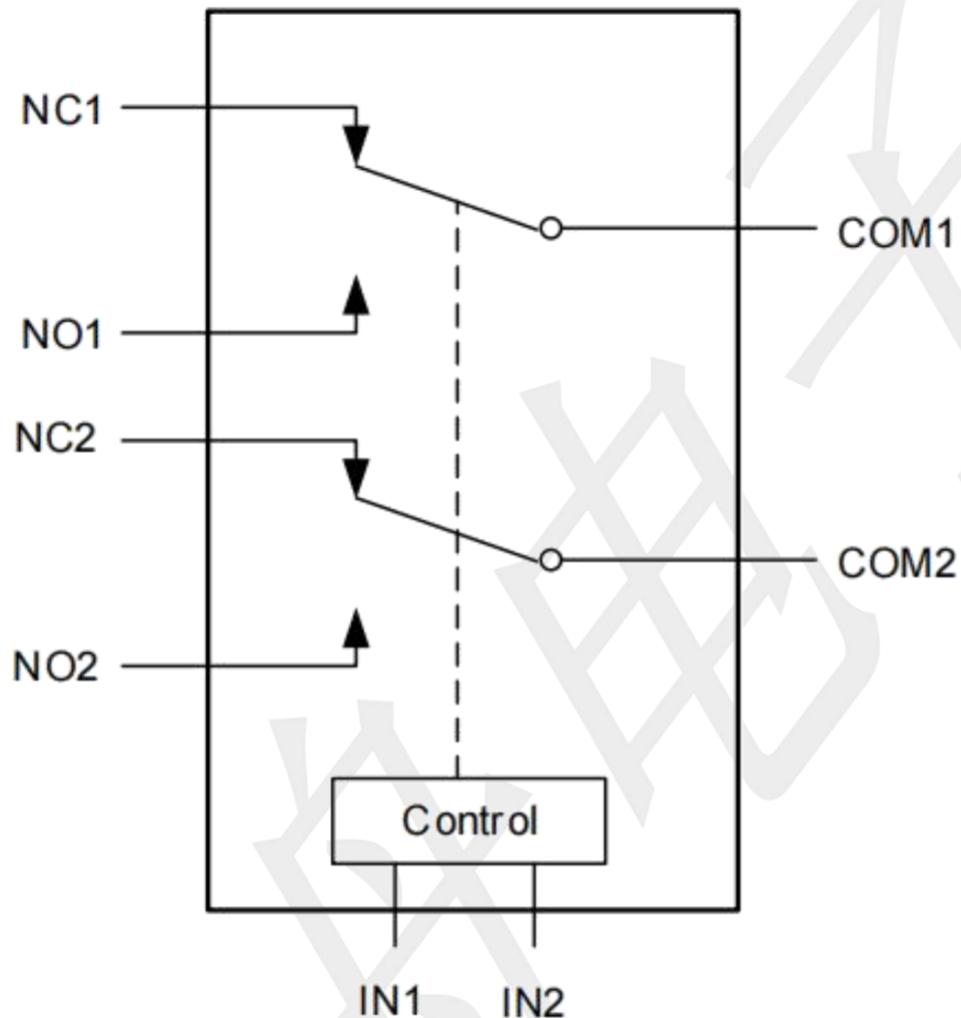
PIN CONFIGURATIONS (TOP VIEW)



PIN DESCRIPTION

PIN NO.	PIN NAME	DESCRIPTION
1	VCC	Supply voltage
2	NO1	Analog/Digital Signal Ports (Normally open)
3	COM1	Common Signal Ports
4	IN1	Logic Input Control
5	NC1	Analog/Digital Signal Ports (Normally closed)
6	GND	Ground
7	NC2	Analog/Digital Signal Ports (Normally closed)
8	IN2	Logic Input Control
9	COM2	Common Signal Ports
10	NO2	Analog/Digital Signal Ports (Normally open)

BLOCK DIAGRAM



Function Table

IN1 (Pin 4)	IN2 (Pin 8)	Function
0	X	COM1 = NC1
1	X	COM1 = NO1
X	0	COM2 = NC2
X	1	COM2 = NO2

Absolute Maximum Ratings

(Unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	VCC	-0.3 ~ +6.5	V
Input Voltage	VIN	-0.6 ~ +6.5	V
Continuous Current Through NO, NC, COM		±100	mA
Peak Current Through NO, NC, COM (pulsed at 1ms 50% duty cycle)		±200	mA
Storage Temperature Range	TSTG	-55 ~ +150	°C
Operating Junction Temperature	TJ	150	°C
Lead Temperature (Soldering, 10 seconds)	TL	260	°C
Power Dissipation	PD	250	mW

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

Recommend operating ratings

(Unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage Operating	Vcc	1.5 ~ 5.5	V
Control Input Voltage	VIN	-0.6 ~ 5.5	V
Input Signal Voltage	VCOM	-0.6 ~ 5.5	V
Operating Temperature	TA	-40 ~ +85	°C
Junction to Ambient	RθJA	360	°C/W

DC Electrical Characteristics (TA =25°C, VC=+3.3V,unless otherwise specified)

PARAMETER	SYMBOL	TEST Conditions	MIN	TYP	MAX	UNIT
High-Level Input Voltage	VIH	VCC=3.3V ~ 5.5V	1.6	--	--	V
		VCC=1.5V ~ 3.3V	1.4	--	--	V
Low-Level Input Voltage	VIL	VCC=3.3V ~ 5.5V	--	--	0.6	V
		VCC=1.5V ~ 3.3V	--	--	0.4	V
Supply quiescent current	I _{CC}	I _A =0, V _{SEL} =0 or V _{SEL} =VCC	--	--	1.0	uA
Increase in ICC per input	I _{CCIT}	I _A =0, VCC=4.5V V _{SEL} >1.8 or V _{SEL} <0.5	--	--	1.0	uA
Off state leakage from COMx to NCx (or NOx)	I _{COMx}	V _{COM} = 5.5V , V _{NC} (or NO) = 0V	--	--	±2.0	uA
On-Resistance	R _{ON1}	V _A =0 ~ 0.5V, I _A =30mA	--	3.6	3.9	Ω
	R _{ON2}	V _A =0.5 ~ 2.0V, I _A =30mA	--	3.0	3.5	Ω
	R _{ON3}	V _A =2.0 ~ 4.0V, I _A =30mA	--	2.5	3.5	Ω
	R _{ON4}	V _A =4.0 ~ 5.5V, I _A =30mA	--	1.5	1.8	Ω
On-Resistance Flatness	R _{FLAT1}	V _A =0 ~ 0.5V, I _A =30mA	--	1.6	--	Ω
	R _{FLAT2}	V _A =0.5 ~ 2.0V, I _A =30mA	--	0.7	--	Ω
	R _{FLAT3}	V _A =2.0 ~ 4.0V, I _A =30mA	--	0.5	--	Ω
	R _{FLAT4}	V _A =4.0 ~ 5.5V, I _A =30mA	--	0.3	--	Ω
On-Resistance Matching Between Channels	Δ R _{ON}	V _A =0~5.5V, I _A =30mA	--	0.1	0.2	Ω

AC Electronics Characteristics (Ta=25°C, VCC=+3.3V, unless otherwise noted)

PARAMETER	SYMBOL	TEST Conditions	MIN	TYP	MAX	UNIT
Turn-On Time	T _{ON}	V _A =1.5V, C _L =35pF, R _L =50Ω	--	200	--	ns
Turn-Off Time	T _{OFF}	V _A =1.5V, C _L =35pF, R _L =50Ω	--	200	--	ns
Break-Before-Make time	T _{BBA}	V _A =1.5V, C _L =35pF, R _L =50Ω	--	500	--	ns
-3dB Bandwidth	BW	R _L =50Ω, C _L =5pF	--	550	--	MHz
		R _L =50Ω, C _L =0pF	--	700	--	MHz
Off isolation	OIRR	F=1KHz, R _L =50Ω	--	-81	--	dB
		F=10KHz, R _L =50Ω	--	-80	--	dB
Crosstalk	Xtalk	F=1KHz, R _L =50Ω	--	-83	--	dB
		F=10KHz, R _L =50Ω	--	-82	--	dB
Total Harmonic Distortion	THD	F=20Hz to 20KHz V _A =600mVp-p @R _L =32Ω	--	-80	--	dB

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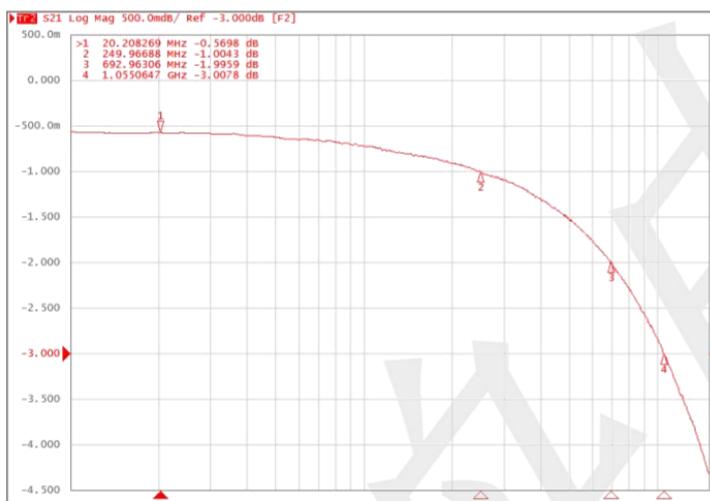
Dual channel SPDT bidirectional analog switch

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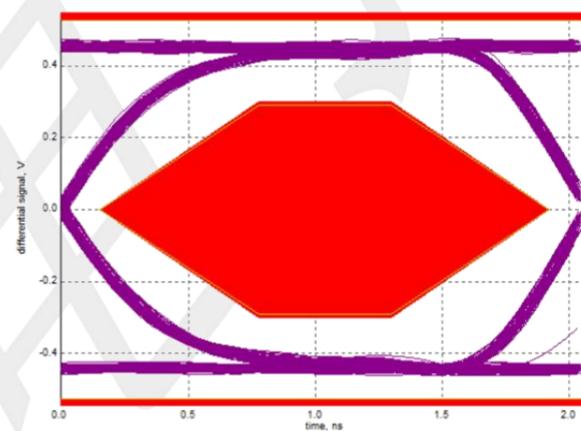
Capacitance (Ta=25°C, VCC=+3.3V, unless otherwise noted)

PARAMETER	SYMBOL	TEST Conditions	MIN	TYP	MAX	UNIT
Off capacitance	C _{OFF}	F=100KHz,	--	5.0	--	pF
On capacitance	C _{ON}	F=100KHz,	--	7.0	--	pF

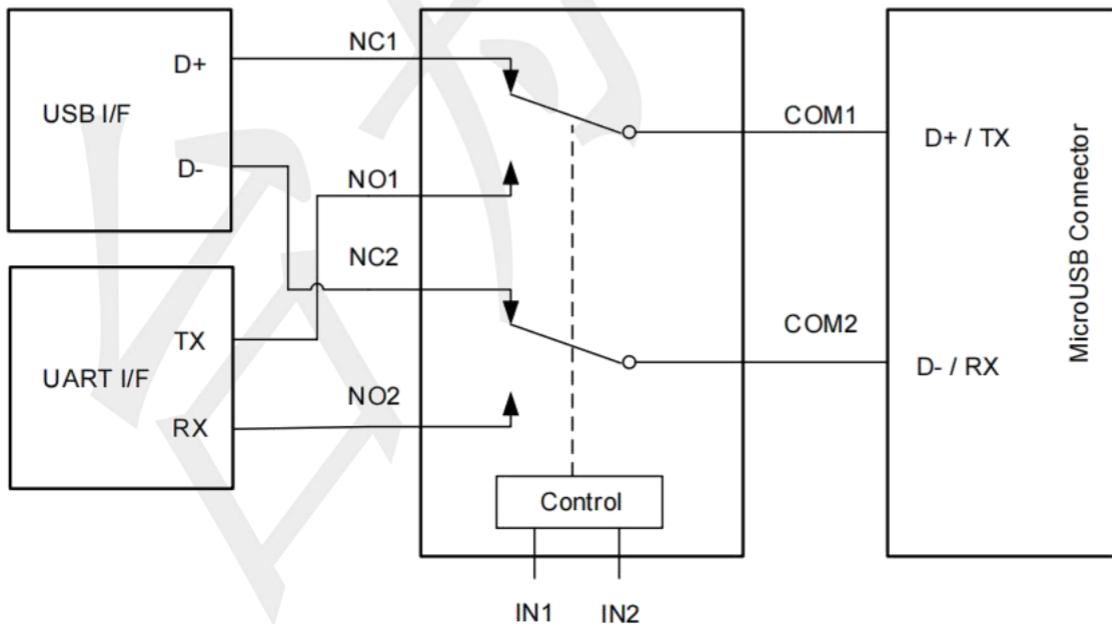
Typical Characteristics (Ta=25°C, VCC=3.3V, unless otherwise noted)



Bandwidth



Eye Diagram (480Mbps)

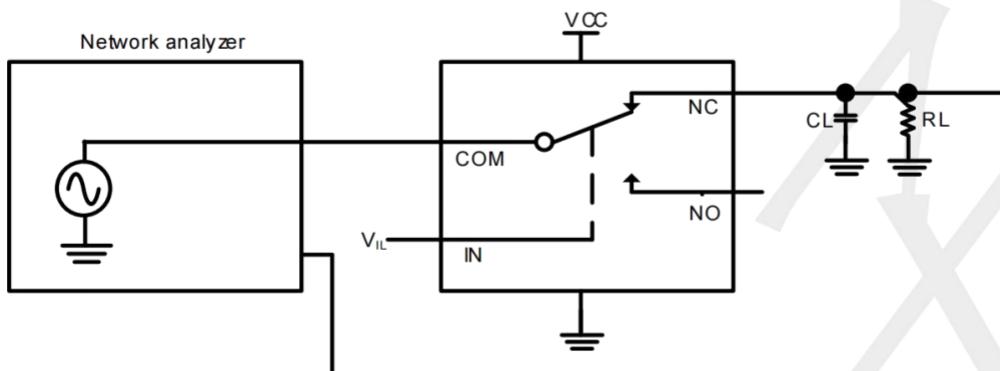


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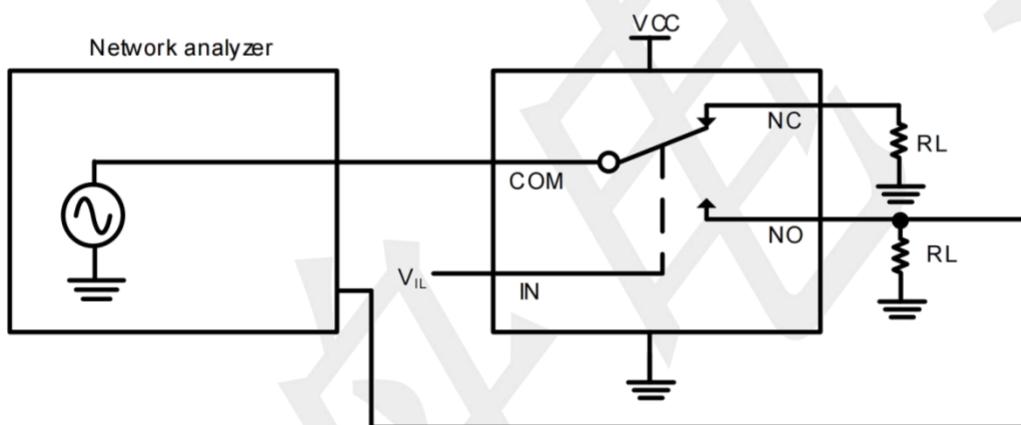
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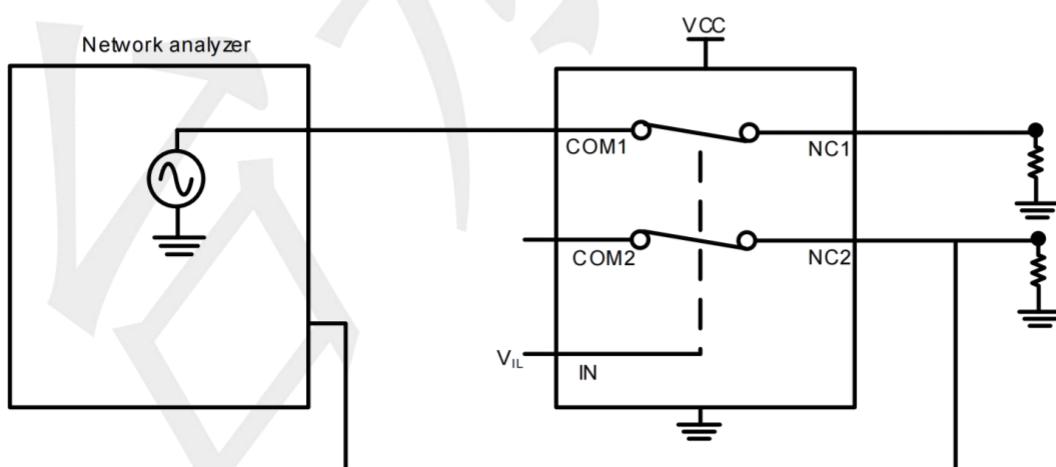
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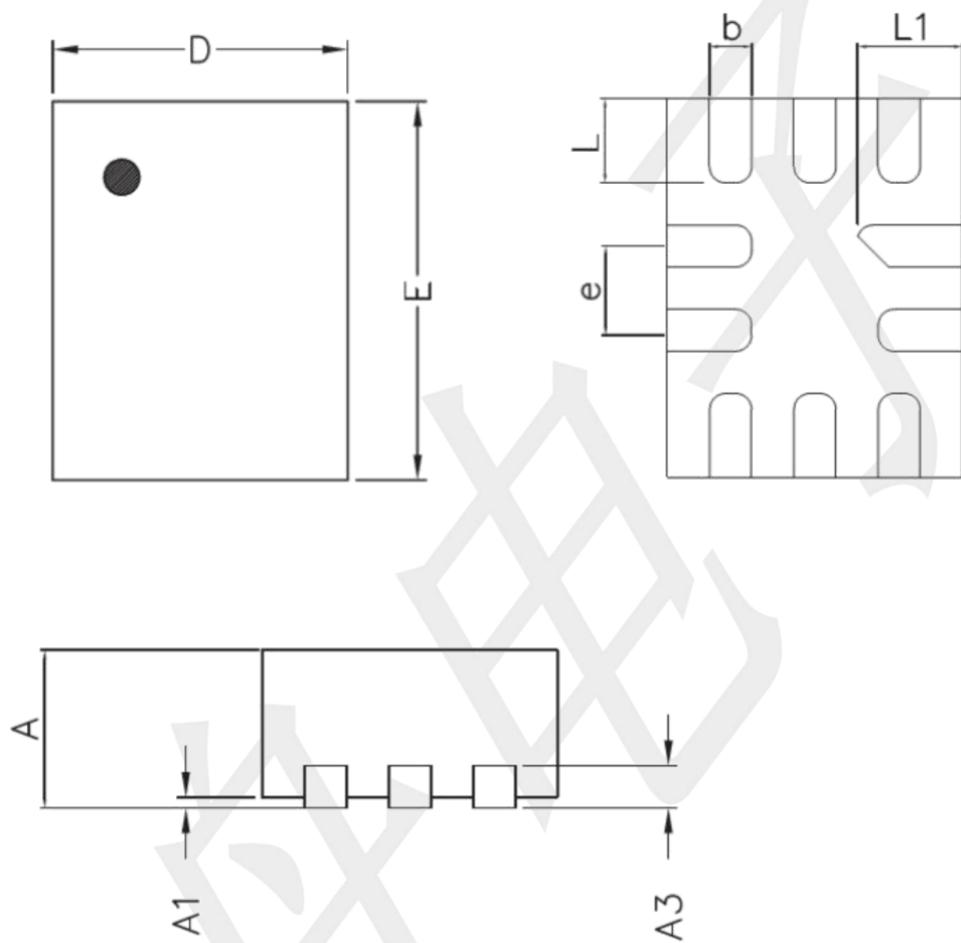
Off isolation



Crosstalk

Package information

QFN1418-10L (Unit: mm)



Symbol	Dimension in Millimeters	
	Min.	Max.
A	0.450	0.550
A1	0.000	0.050
A3	0.152 Ref.	
D	1.350	1.450
E	1.750	1.850
b	0.150	0.250
e	0.400 Typ.	
L	0.350	0.450
L1	0.450	0.550