

Features

- 650-Volt Schottky Rectifier
- Zero Reverse Recovery Current
- Zero Forward Recovery Voltage
- High-Frequency Operation
- Temperature-Independent Switching Behavior
- Extremely Fast Switching
- Positive Temperature Coefficient on V_F

Benefits

- Replace Bipolar with Unipolar Rectifiers
- Essentially No Switching Losses
- Higher Efficiency
- Reduction of Heat Sink Requirements
- Parallel Devices Without Thermal Runaway

Applications

- Switch Mode Power Supplies
- Power Factor Correction
- Motor Drives



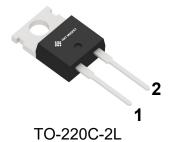


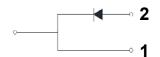


Part Number	Package	Qty(PCS)
IDH06G65C6	TO-220C-2L	50

Maximum Ratings (T_C = 25 °C unless otherwise specified)

Symbol	Parameter	Value	Unit	Test Conditions
V _{RRM}	Repetitive Peak Reverse Voltage	650	V	
V _{RSM}	Surge Peak Reverse Voltage	650	V	
I _F	Continuous Forward Current	22.3 10.4 6	А	T _c =25°C T _c =135°C T _c =158.5°C
I _{FRM}	Repetitive Peak Forward Surge Current	33	А	T _c =25°C, t _p = 10 ms, Half Sine Wave
I _{FSM}	Non-Repetitive Peak Forward Surge Current	66	А	T_c =25°C, t_p = 10 ms, Half Sine Wave
P _{tot}	Power Dissipation	88 38	W	T _c =25°C T _c =110°C
T_{J} , T_{stg}	Operating Junction and Storage Temperature	-55 to +175	°C	
	TO-220 Mounting Torque	1	Nm	M3 Screw







Electrical Characteristics

Symbol	Parameter	Min.	Тур.	Max.	Unit	Test Conditions
V _{DC}	DC Blocking Voltage	650			V	
V _F	Forward Voltage		1.36 1.64	1.7 2	V	I _F = 6 A T _J =25°C I _F = 6 A T _J =175°C
I _R	Reverse Current		0.12 0.91	50 100	μΑ	V _R = 650 V T _J =25°C V _R = 650 V T _J =175°C
Q _c	Total Capacitive Charge		22		nC	V _R = 400 V T _J = 25°C
С	Total Capacitance		440 42 41		pF	V _R = 0 V, T _J = 25°C, f = 1 MHz V _R = 200 V, T _J = 25°C, f = 1 MHz V _R = 400 V, T _J = 25°C, f = 1 MHz
E _c	Capacitance Stored Energy		5		μJ	V _R = 400 V

Thermal Characteristics

Symbol	Parameter	Тур.	Unit
R _{eJC}	Thermal Resistance from Junction to Case	1.7	°C/W

Typical Performance

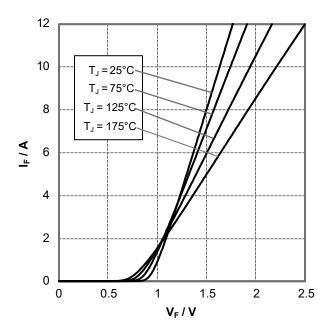


Figure 1. Forward Characteristics

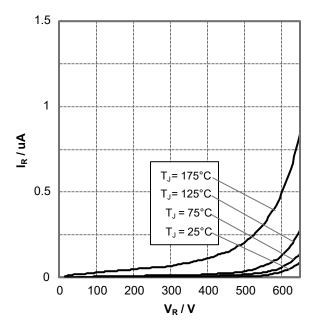
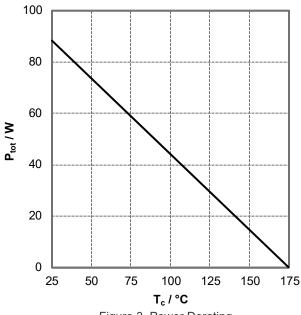
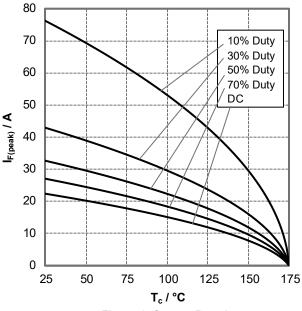
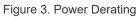


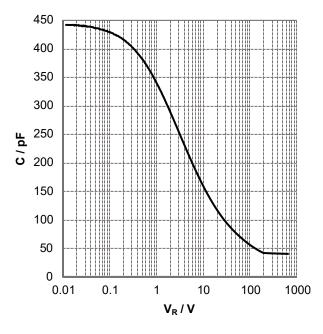
Figure 2. Reverse Characteristics











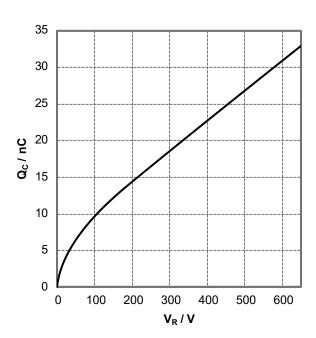


Figure 5. Capacitance vs. Reverse Voltage

Figure 6. Total Capacitance Charge vs. Reverse Voltage

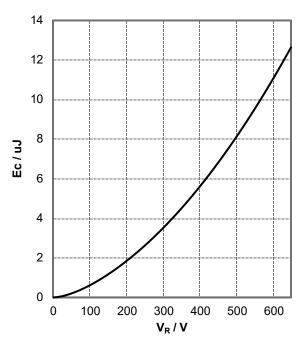


Figure 7. Capacitance Stored Energy

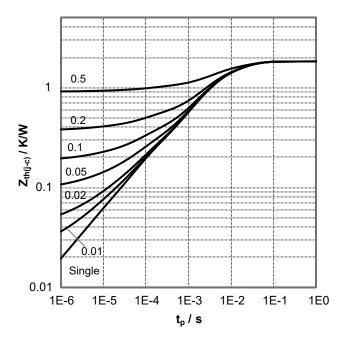
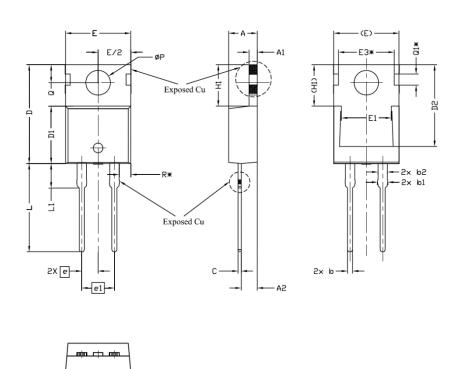


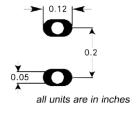
Figure 8. Transient Thermal Impedance

Package Information TO-220C-2L

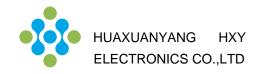


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SYMBOL	MIN.	NOM.	MAX.	NOTES
Α	4,24	4,44	4,64	
A1	1.15	1.27	1.40	
A2	2.30	2.48	2.70	
ь	0.70	0.80	0.90	
b1	1.20	1.55	1.75	
b2	1.20	1.45	1.70	
С	0.40	0.50	0.60	
D	14.70	15.37	16.00	4
D1	8.82	8.92	9.02	
D2	12.43	12.73	12.83	5
E	9.96	10.16	10.36	4,5
E1	6,86	7,77	8.89	5
E3*		8.70REF.		
e	2,54BSC			
e1	5.08BSC			
H1	6.30	6.45	6.60	5,6
L	13,47	13.72	13.97	
L1	3.60	3.80	4.00	
ØP	3,75	3.84	3.93	
Q	2,60	2,80	3,00	
Q1*		1.73REF.		
R*		1.82REF.		

Recommended Solder Pad Layout



TO-220C-2L



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