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MT29F1G01AAADDH4-ITX

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Data Sheet



1Gb x1 (M68A) SPI NAND Flash Memory Data Sheet

Data Sheet Rev L; Design Revision D

File Type: PDF

Updated: 02/2016

Embedded Memory Solutions
NAND Flash
Serial NAND
SLC NAND

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Specs

Orderable Parts for: MT29F1G01AAADDH4-ITX

SEE ALL SLC NAND PARTS

	Status	Media	FBGA Code	SPD Data	Chipset Validation	PLP	Start Date	Alternative Part
MT29F1G01AAADDH4-ITX:D	Production	N/A	NQ433	N/A	N/A	Yes	05/04/2011	N/A

Detailed Specifications

Density	1Gb	Status	Production	RoHS	Yes	Width	x1
Voltage	3.3V	Package	VFBGA	Pin Count	63-ball	MT/s	
I/O	Common	Op Temp	-40C to +85C				

Sim Models & Software

Sim Models

Verilog



Verilog: Serial NAND 1Gb M68A (TAR)

NAND Flash
SLC NAND

m68a Serial NAND model rev 3.00 Initialize memory array feature added to model

File Type: TAR

Updated: 03/30/2015

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IBIS



IBIS: Serial NAND 1Gb (Rev D) M68A (ZIP)

NAND Flash
Serial NAND

Rev. 2.1

File Type: ZIP

Updated: 04/11/2011

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HSpice



HSpice: Serial NAND 1Gb (Rev D) M68A (ZIP)

NAND Flash
Serial NAND

Rev. 2.0

File Type: ZIP

Updated: 07/16/2010

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Software

Software Support Pack

NAND FLASH lockunlock bug fix (ZIP)

MLC NAND
NAND Flash
SLC NAND

Reset NAND before write protect check.

File Type: ZIP

Updated: 03/31/2016

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Software Support Pack

YAFFS mark bad block issue (ZIP)

MLC NAND
NAND Flash
SLC NAND

If the bitflips num over mtd->bitflip_threshold the mtd_read_oob will return -EUCLEAN and tags->ecc_result > YAFFS_ECC_RESULT_NO_ERROR. Then we will call yaffs_handle_chunk_error.

File Type: ZIP

Updated: 03/31/2016

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Software Support Pack



NANDcode Flash Translation Layer (ZIP)

NAND Flash
SLC NAND

NANDcode is a "C" FTL (Flash Translation Layer) software library for ARM platform.

File Type: ZIP

Updated: 08/08/2014

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Software Driver

MT29F, SLC, 60/70 Series (1Gb, 2Gb, 4Gb, 8Gb, 16Gb, 32Gb) General Low-Level Driver for Parallel NAND Flash memory (ZIP)

NAND Flash
SLC NAND

M60A, M61A, M62B, M68A, M68M, M70M, M71M, M72A, M73A, M79M. Download the technical note for this driver [here](#).

File Type: ZIP

Updated: 04/05/2013

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Software Driver



MT29F, SLC, 60 Series (1Gb, 2Gb, 4Gb) General Low-Level Driver for SPI NAND Flash memory (ZIP)

NAND Flash
Serial NAND
SLC NAND

M68A, M69A, M60A

File Type: ZIP

Updated: 12/28/2011

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RoHS Certificates

RoHS Certificates

RoHS Certificate of Compliance (PDF)

MT29F1G01AAADDH4-ITX

Part-specific certification of how this product meets the requirements of the current DIRECTIVE 2002/95/EC, a.k.a. Restriction of Hazardous Substances (RoHS) Directive.

File Type: (PDF)

Updated: 11/2016

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RoHS Certificates

China RoHS Certificate (PDF)

MT29F1G01AAADDH4-ITX

Part-specific certification as required by China's Management Methods for Controlling Pollution by Electronic Information Products.

File Type: (PDF)

Updated: 11/2016

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Documentation & Support

[See All SLC NAND Documentation](#)

Technical Notes

Technical Notes

**TN-13-49: SLC NAND Device Refresh (PDF)**NAND Flash
SLC NAND

(TN-13-49) This technical note discusses the adverse effects of high temperature and PROGRAM/ERASE (P/E) cycles on flash memory data retention and how the REFRESH operation can be used to mitigate these adverse effects.

File Type: PDF

Updated: 10/31/2016

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Technical Notes

**TN-29-58: ONFI NV-DDR2 Design Guide (PDF)**NAND Flash
SLC NAND

(TN-29-58) This technical note explains the device features that enable NV-DDR2 and provides guidelines for system designs to enable I/O transfer rates of up to 400 MT/s using the NV-DDR2 interface.

File Type: PDF

Updated: 09/29/2016

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Technical Notes

TN-29-76: Using the Block Lock Feature on MT29F NAND Flash Memory Devices (PDF)NAND Flash
SLC NAND

(TN-29-76) This technical note describes the block lock feature on Micron's MT29F NAND Flash memory devices.

File Type: PDF

Updated: 08/16/2016

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Technical Notes

**TN-29-51: Migrating from 50-Series to 60-Series SLC NAND (PDF)**Embedded Memory Solutions
NAND Flash
SLC NAND

(TN-29-51) This technical note supplements the product change notification (PCN) covering the transition from Micron® 50-series (50nm) to 60-series (34nm) single-level cell (SLC) NAND Flash devices.

File Type: PDF

Updated: 08/16/2016

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Technical Notes

TN-26-03: How to Read, Program, and Manage Small Page NAND OTP Memory Area (PDF)NAND Flash
SLC NAND

(TN-26-03) This technical note explains how to read, program, and manage small page NAND memory.

File Type: PDF

Updated: 08/16/2016

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Parts with the same Data Sheet (2)

MT29F1G01AAADDH4-ITX (Current)		MT29F1G01AAADDH4	MT29F1G01AAADDH4-IT
Status	Production	Production	Production
Density	1Gb	1Gb	1Gb
RoHS	Yes	Yes	Yes
Width	x1	x1	x1
Voltage	3.3V	3.3V	3.3V
Package	VFBGA	VFBGA	VFBGA
Pin Count	63-ball	63-ball	63-ball
MT/s			
I/O	Common	Common	Common
Op Temp	-40C to +85C	0C to +70C	-40C to +85C

Where to Buy

Orderable Parts

	Status	Media	FBGA Code	SPD Data	Chipset Validation	PLP	Start Date	Alternative Part
MT29F1G01AAADDH4-ITX:D	Production	N/A	NQ433	N/A	N/A	Yes	05/04/2011	N/A

Contact Your Sales Rep

- OR - Check with Distributors

CONTACT A REP

Your Region: Americas

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NAND Flash
Managed NAND
NOR Flash
Hybrid Memory Cube
Multichip Packages
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