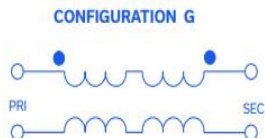


THE BIG DEAL

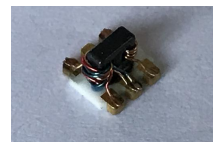
- Wideband, 1500 to 6700 MHz
- Balanced transmission line
- Low insertion loss 0.8 dB at 4 GHz
- Flat insertion loss, 0.6 dB typ.
- Good input return loss, 15 dB typ.
- Aqueous washable

Applications

- PCS
- Wideband push-pull amplifiers
- Cellular



JY-TCM2-672AX+



50Ω 1500 to 6700 MHz

Maximum Ratings

Operating Temperature	-40°C to 85°C
Storage Temperature	-55°C to 100°C
RF Power Input*	0.4W
DC Current	30mA

Permanent damage may occur if any of these limits are exceeded.

Pid Connections

PRIMARY DOT	6
PRIMARY	2
SECONDARY DOT	4
SECONDARY	3
GND	2,5
NOT USED	1

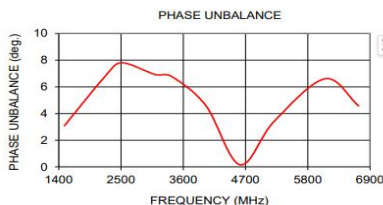
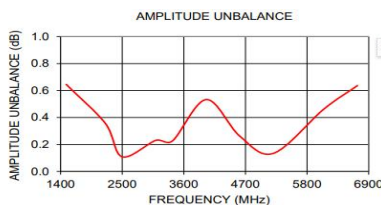
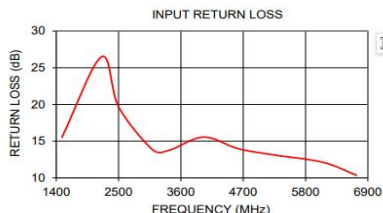
Electrical Specifications at 25°C

Parameter	Frequency (MHz)	Min.	Typ.	Max.	Unit
Impedance Ratio(secondary/primary)	-	2			Ohm
Frequency Range	-	1500	-	6700	Mhz
Insertion Loss	1500-4000 4000-6700	- -	0.8 2.1	1.5 3.2	dB
Amplitude Unbalance	1500-3800 3800-6700	- -	0.8 1.8	- -	dB
Phase Unbalance	1500-3800 3800-6700	- -	6 12	- -	Degree
Return Loss	1500-6700	-	15	-	dB

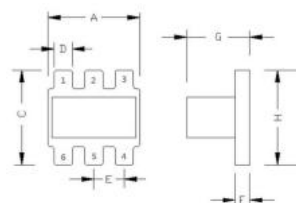
Typical Performance Data

(TEST CONDITIONS: INPUT POWER = 0dBm @Temperature = +25°C)

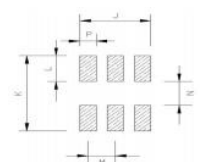
FREQUENCY (MHz)	INSERTION LOSS (dB)	INPUT R. LOSS (dB)	AMPLITUDE UNBALANCE (dB)	PHASE UNBALANCE (Deg.)
1500	0.48	15.53	0.65	3.09
2200	0.44	26.48	0.36	6.07
2500	0.52	19.67	0.11	7.81
3100	0.75	13.84	0.23	6.93
3400	0.76	13.79	0.23	6.76
4000	0.79	15.56	0.53	4.59
4600	0.96	14.00	0.26	0.16
5200	1.11	13.17	0.13	3.39
6100	1.56	12.13	0.46	6.60
6700	2.05	10.36	0.64	4.57



Outline Drawing



PCB Land Pattern

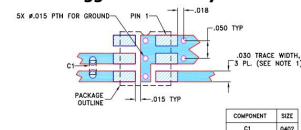


Suggested Layout,Tolerance to be within ±0.02

Outline Dimensions: Unit (mm)

A	3.81	C	3.81	D	0.76
E	1.27	F	0.61	G	2.61
H	3.81	J	3.30	K	4.83
L	1.65	N	1.53	M	1.27
P	0.76	wt	0.15g		

Suggested PCB Layout



NOTES:
1. TRACE WIDTH PARAMETERS ARE SHOWN FOR ROGERS RO4350B WITH DIELECTRIC THICKNESS 0.137±0.001", COPPER: 1/2 OZ. EACH SIDE.
2. FOR OTHER MATERIALS, TRACE WIDTH MAY VARY, TO BE ADVISED.
3. FOR COMPONENT FOOT PRINTS SHOWN FOR REFERENCE.
FOR COMPONENT VALUES REFER TO TB-742+.
4. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.
5. DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER).
6. DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK.