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MT29F8G08ABABAWP-ITX

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Data Sheets (1)

[Data Sheet](#)

M61A SLC datasheet: 8Gb Asynchronous/Synchronous NAND

M61A SLC datasheet

China
NAND Flash
SLC NAND
Taiwan
USA

File Type: PDF

Updated: 04/2014

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Specs

Orderable Parts for: MT29F8G08ABABAWP-ITX

[SEE ALL SLC NAND PARTS](#)

	Status	Media	FBGA Code	SPD Data	Chipset Validation	PLP	Start Date	Alternative Part
MT29F8G08ABABAWP-ITX:B	Production	N/A	N/A	N/A	N/A	Yes	12/31/2011	N/A

Detailed Specifications

Density	8Gb	Status	Production	RoHS	Yes	Width	x8
Voltage	3.3V	Package	TSOP	Pin Count	48-pin	MT/s	
I/O	Common	Op Temp	-40C to +85C				

Sim Models & Software

Sim Models

IBIS



IBIS: NAND 8Gb SLC (Rev B) M61A (ZIP)

NAND Flash
SLC NAND

Rev. 2.2

File Type: ZIP

Updated: 03/12/2015

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Verilog



Verilog: NAND 8Gb, SLC (rev B) M61A (ZIP)

NAND Flash
SLC NAND

8Gb SLC NAND Verilog Model

File Type: ZIP

Updated: 02/06/2015

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HSpice



HSpice: NAND 8Gb SLC (Rev B) M61A (ZIP)

NAND Flash
SLC NAND

Rev. 2.0

File Type: ZIP

Updated: 11/29/2010

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Software

Software Support Pack

NAND FLASH lockunlock bug fix (ZIP)

MLC NAND
NAND Flash
SLC NAND

Reset NAND before write protect check.

File Type: ZIP

Updated: 03/31/2016

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Software Support Pack

YAFFS mark bad block issue (ZIP)

MLC NAND
NAND Flash
SLC NAND

If the bitflips num over mtd->bitflip_threshold the mtd_read_oob will return -EUCLEAN and tags->ecc_result > YAFFS_ECC_RESULT_NO_ERROR. Then we will call yaffs_handle_chunk_error.

File Type: ZIP

Updated: 03/31/2016

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Software Support Pack



NANDcode Flash Translation Layer (ZIP)

NAND Flash
SLC NAND

NANDcode is a "C" FTL (Flash Translation Layer) software library for ARM platform.

File Type: ZIP

Updated: 08/08/2014

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Software Driver

MT29F, SLC, 60/70 Series (1Gb, 2Gb, 4Gb, 8Gb, 16Gb, 32Gb) General Low-Level Driver for Parallel NAND Flash memory (ZIP)

NAND Flash
SLC NAND

M60A, M61A, M62B, M68A, M68M, M70M, M71M, M72A, M73A, M79M. Download the technical note for this driver [here](#).

File Type: ZIP

Updated: 04/05/2013

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Software Driver



MT29F, SLC, 60 Series (1Gb, 2Gb, 4Gb) General Low-Level Driver for SPI NAND Flash memory (ZIP)

NAND Flash
Serial NAND
SLC NAND

M68A, M69A, M60A

File Type: ZIP

Updated: 12/28/2011

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RoHS Certificates

RoHS Certificates

RoHS Certificate of Compliance (PDF)

MT29F8G08ABABAWP-ITX

Part-specific certification of how this product meets the requirements of the current DIRECTIVE 2002/95/EC, a.k.a. Restriction of Hazardous Substances (RoHS) Directive.

File Type: (PDF)

Updated: 11/2016

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RoHS Certificates

China RoHS Certificate (PDF)

MT29F8G08ABABAWP-ITX

Part-specific certification as required by China's Management Methods for Controlling Pollution by Electronic Information Products.

File Type: (PDF)

Updated: 11/2016

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Documentation & Support

[See All SLC NAND Documentation](#)

Technical Notes

Technical Notes

**TN-13-49: SLC NAND Device Refresh (PDF)**NAND Flash
SLC NAND

(TN-13-49) This technical note discusses the adverse effects of high temperature and PROGRAM/ERASE (P/E) cycles on flash memory data retention and how the REFRESH operation can be used to mitigate these adverse effects.

File Type: PDF

Updated: 10/31/2016

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Technical Notes

**TN-29-58: ONFI NV-DDR2 Design Guide (PDF)**NAND Flash
SLC NAND

(TN-29-58) This technical note explains the device features that enable NV-DDR2 and provides guidelines for system designs to enable I/O transfer rates of up to 400 MT/s using the NV-DDR2 interface.

File Type: PDF

Updated: 09/29/2016

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Technical Notes

TN-29-76: Using the Block Lock Feature on MT29F NAND Flash Memory Devices (PDF)NAND Flash
SLC NAND

(TN-29-76) This technical note describes the block lock feature on Micron's MT29F NAND Flash memory devices.

File Type: PDF

Updated: 08/16/2016

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Technical Notes

**TN-29-51: Migrating from 50-Series to 60-Series SLC NAND (PDF)**Embedded Memory Solutions
NAND Flash
SLC NAND

(TN-29-51) This technical note supplements the product change notification (PCN) covering the transition from Micron® 50-series (50nm) to 60-series (34nm) single-level cell (SLC) NAND Flash devices.

File Type: PDF

Updated: 08/16/2016

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Technical Notes

TN-26-03: How to Read, Program, and Manage Small Page NAND OTP Memory Area (PDF)NAND Flash
SLC NAND

(TN-26-03) This technical note explains how to read, program, and manage small page NAND memory.

File Type: PDF

Updated: 08/16/2016

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Parts with the same Data Sheet (4)

MT29F8G08ABABAWP-ITX (Current)	MT29F8G08ABABAM61A3W C1	MT29F8G08ABABAWC-IT	MT29F8G08ABABAWP
Status Production	Contact Factory	Obsolete	Production
Density 8Gb	8Gb	8Gb	8Gb
RoHS Yes	Yes	Yes	Yes
Width x8	x8	x8	x8
Voltage 3.3V	3.3V	3.3V	3.3V
Package TSOP	Wafer	TSOP	TSOP
Pin Count 48-pin	n/a	48-pin	48-pin
MT/s			
I/O Common	Common	Common	Common
Op Temp -40C to +85C	0C to +70C	-40C to +85C	0C to +70C

Where to Buy

Orderable Parts

	Status	Media	FBGA Code	SPD Data	Chipset Validation	PLP	Start Date	Alternative Part
MT29F8G08ABABAWP-ITX:B	Production	N/A	N/A	N/A	N/A	Yes	12/31/2011	N/A

Contact Your Sales Rep

- OR - Check with Distributors

CONTACT A REP

Your Region: Americas

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NAND Flash
Managed NAND
NOR Flash
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