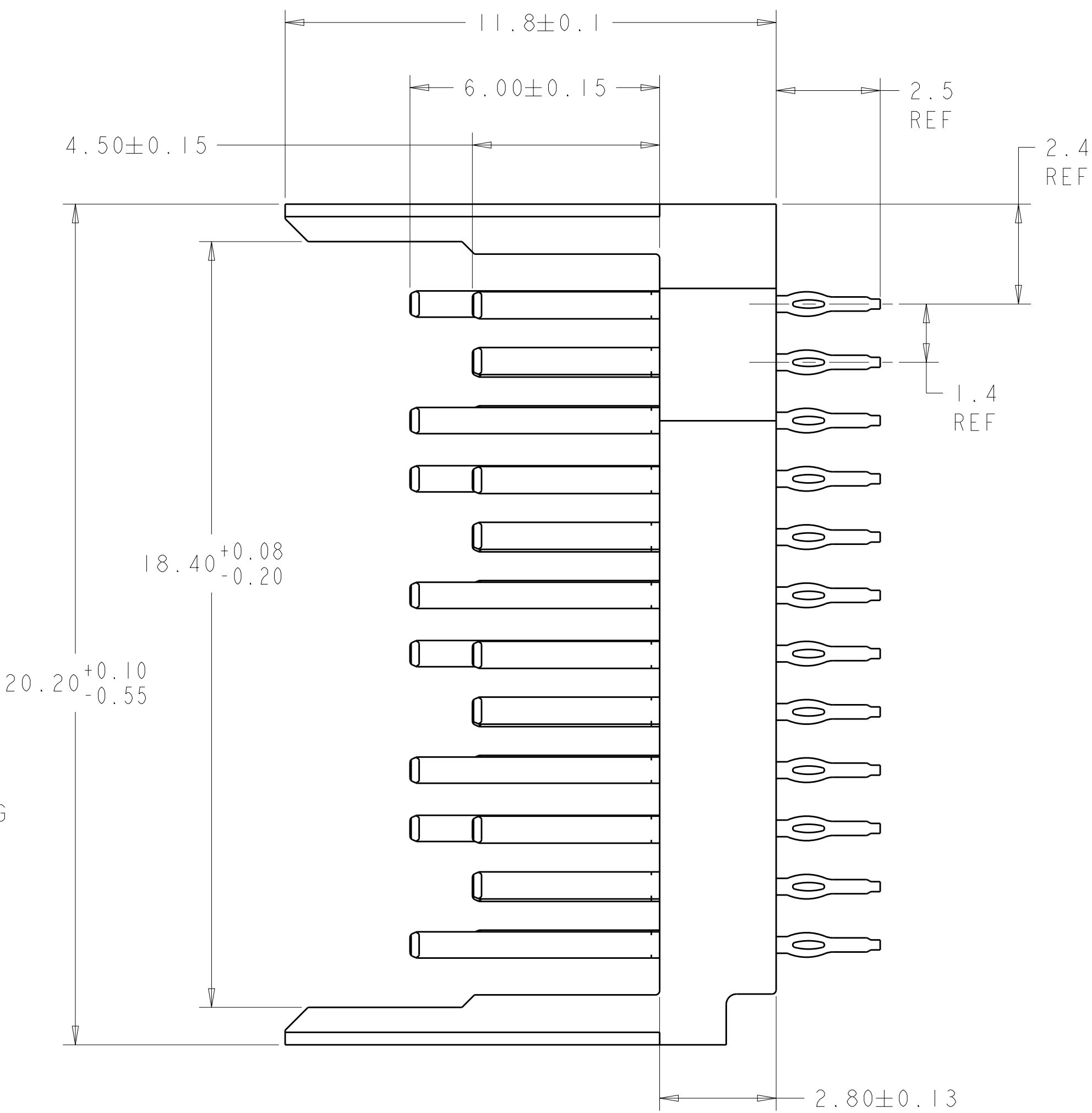
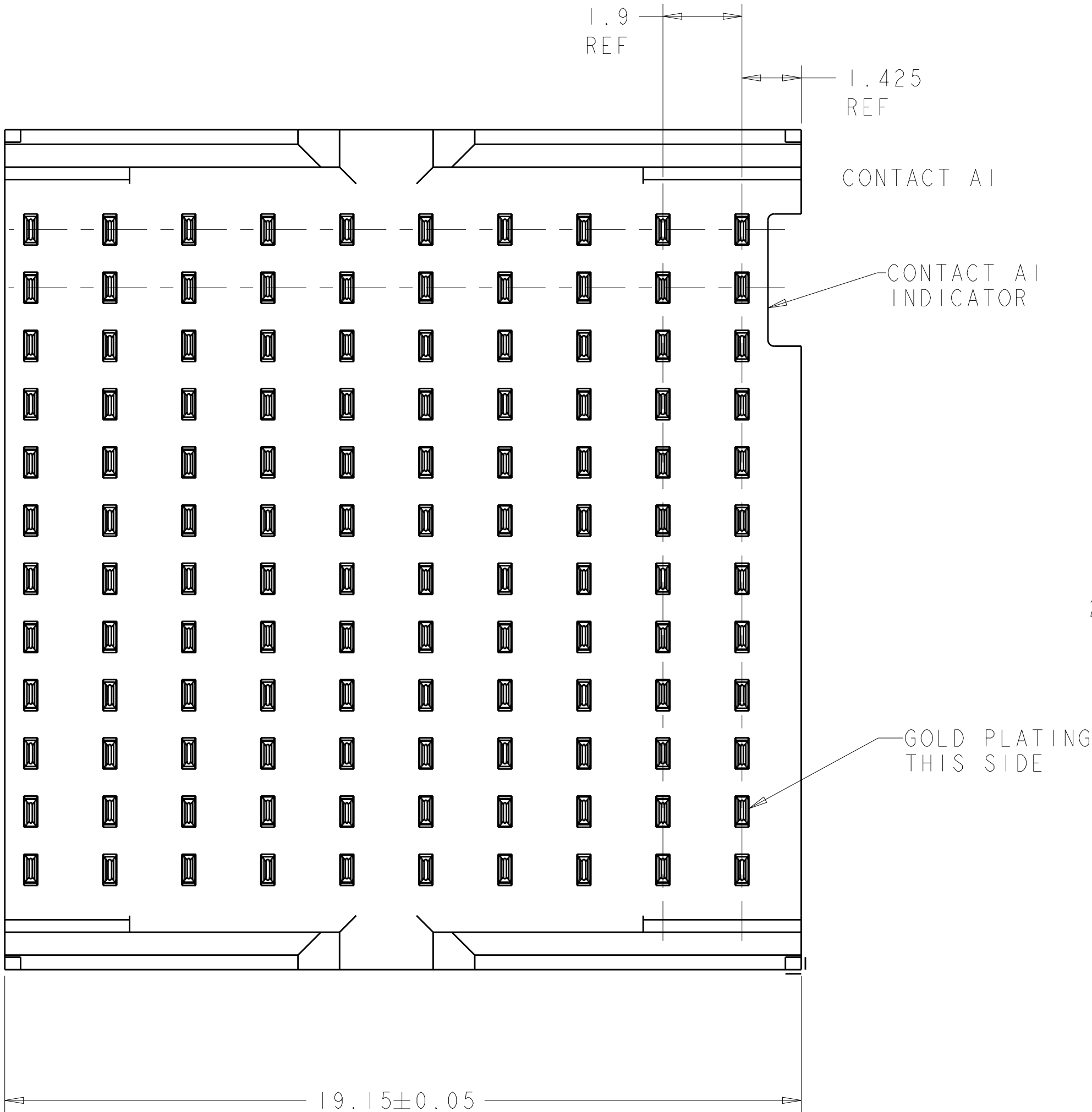
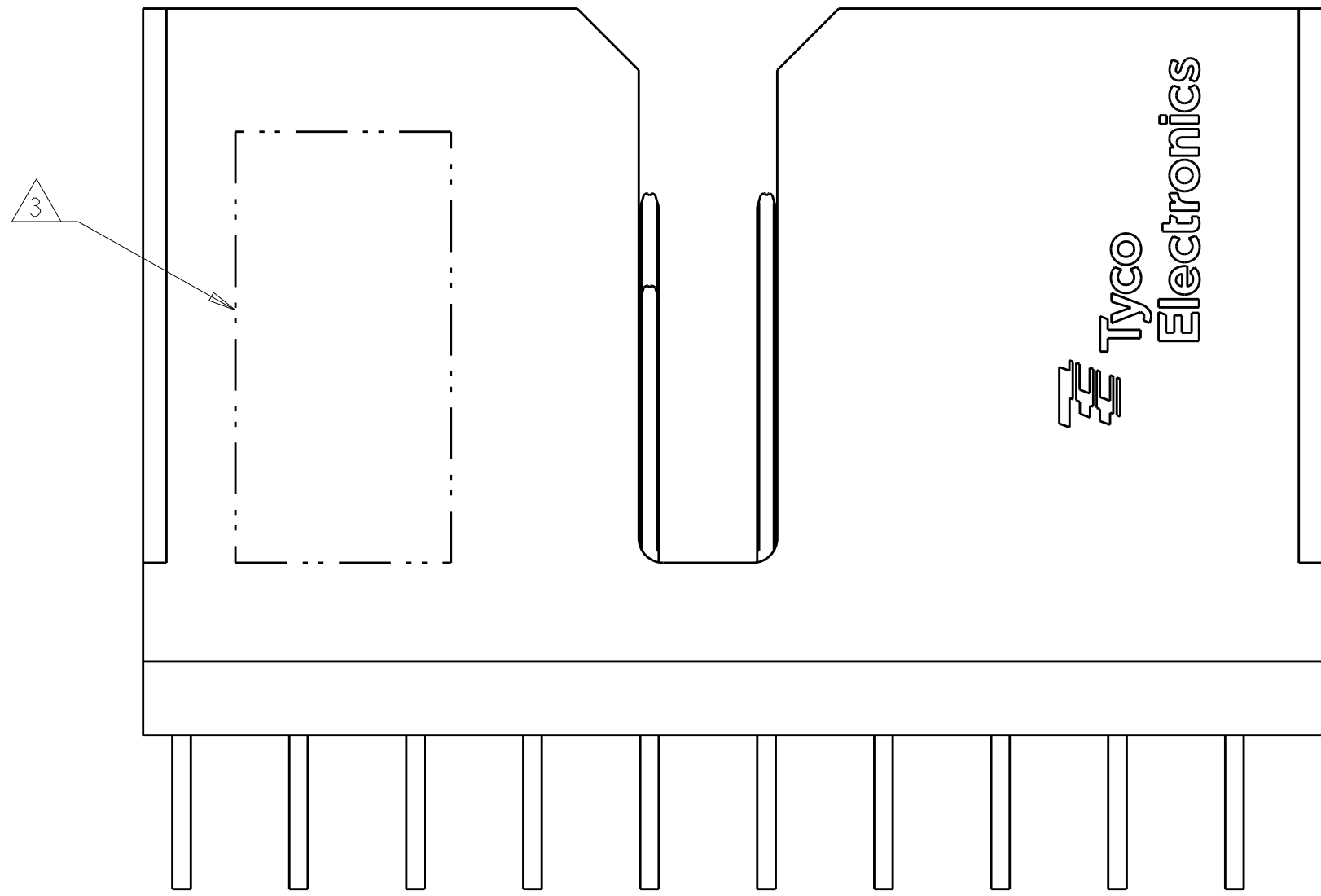


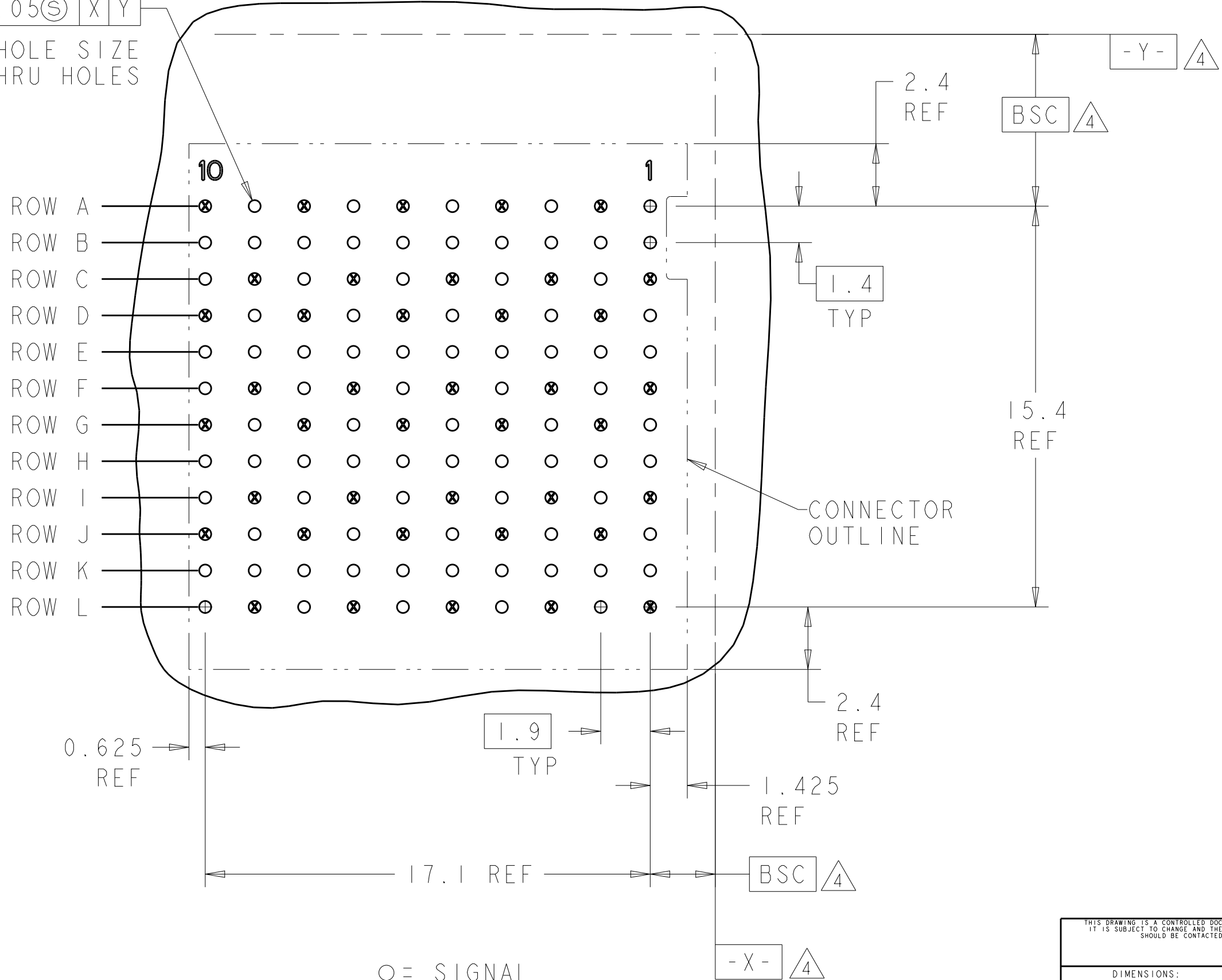
LOC	DIST	REVISIONS					
GP	00	P	LTR	DESCRIPTION	DATE	DWN	APVD
			A	RELEASED	22FEB2010	WS	RP
			B	REV PER ECO-12-000894	24JAN2012	WS	RP
			C	REV PER ECO-12-018914	03DEC2012	WS	RP
			D	REV PER ECO-12-020324	16 JAN2013	WS	RP



- 1 MATERIAL:
CONTACT - PHOS BRONZE
HOUSING - LCP, UL 94V-0, COLOR - BLACK
- 2 FINISH: CONTACT
MATING AREA - 0.76µm MIN GOLD OVER 1.27µm MIN NICKEL
COMPLIANT PIN AREA - 1.25µm MIN BRIGHT TIN-LEAD OVER 1.27µm MIN NICKEL
- 3 CONNECTOR MARKED WITH PART NUMBER, DATE CODE AND STANDARD IN APPROXIMATE AREA SHOWN
- 4 DATUMS AND BASIC DIMENSIONS ESTABLISHED BY CUSTOMER.
5. CONTACT AREA LUBRICATED WITH BELLCORE APPROVED LUBRICANT. TECHNICAL REFERENCE GR-1217-CORE, ISSUE 1, NOVEMBER 1995.
- 6 REFERENCE APPLICATION SPEC 114-13202 FOR RECOMMENDED PLATED THRU HOLE DIAMETER AND PLATING THICKNESSES.
- 7 FINISH: CONTACT
MATING AREA - 0.76µm MIN GOLD OVER 1.27µm MIN NICKEL
COMPLIANT PIN AREA - 1.25µm MIN MATTE TIN OVER 1.27µm MIN NICKEL
- 8 FINISH: CONTACT MATING AREA 0.076µm MIN GOLD OVER 0.68µm MIN PALLADIUM-NICKEL OVER 1.27µm NICKEL. COMPLIANT PIN AREA 0.5µm MIN MATTE TIN OVER 1.27µm MIN NICKEL.



6 $\varnothing 0.05 \text{ (S)}$ X Y
FINISHED HOLE SIZE
AND PLATED THRU HOLES



○ = SIGNAL
⊗ = GROUND
RECOMMENDED PC BOARD
LAYOUT BACKPLANE,
COMPONENT SIDE SHOWN
SCALE 6:1

8	2065917-3
7	2065917-2
2	2065917-1
FINISH	PART NUMBER

1ST FORMERLY IT IS CONSIDERED TO BE THE PROPERTY OF TYCO ELECTRONICS CORPORATION. IT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE USER OF THIS DRAWING SHOULD BE CONTACTED FOR THE LATEST REVISION.		DWN W. SOURBEER 22FEB2010	Tyco Electronics Harrisburg, PA 17105-3608	
DIMENSIONS: mm		CHK R. PATTERSON 22FEB2010	NAME	
TOLERANCES UNLESS OTHERWISE SPECIFIED: 0 PLC ± 1 PLC ± 2 PLC ± 3 PLC ± 4 PLC ± ANGLES ±		APVD R. PATTERSON 22FEB2010	PRODUCT SPEC	
MATERIAL		FINISH	APPLICATION SPEC	
SEE TABLE		WEIGHT	-	
CUSTOMER DRAWING		SCALE	10:1	
SHEET 1 OF 1		REV D		CISCO