

Discription

The ESD8D15C protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. Excellent clamping capability, low leakage, low capacitance, and fast response time provide best in class protection on designs that are exposed to ESD.

It gives designer the flexibility to protect one bi-directional line in applications where arrays are not practical.



DFN1006-2L

Features

- ★ Low Leakage
- ★ Response Time is Typically < 1 ns
- ★ IEC61000-4-2 Level 4 ESD Protection
- ★ These are Pb-Free Devices
- ★ We declare that the material of product compliance with RoHS requirements.



Circuit Diagram

Ordering information

Product ID	Pack	Qty(PCS)	
ESD8D15C	DFN1006-2L	10000	

Absolute Ratings (T_{amb}=25°C)

Symbol	Parameter	Value	Units
P_{PP}	Peak Pulse Power (t _p = 8/20 μ s)	240	W
T _L	Maximum lead temperature for soldering during 10s	260	°C
T _{stg}	Storage Temperature Range	-55 to +150	°C
T _{op}	Operating Temperature Range	-55 to +150	°C
T _j	Maximum junction temperature	150	°C
	IEC61000-4-2 (ESD) air discharge contact discharge	±20 ±20	KV



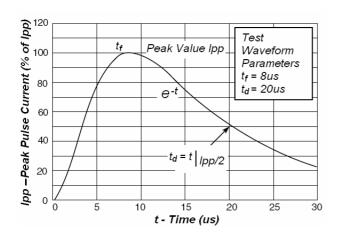
Electrical Characteristics

	V _{RWM} (V)	I _{R1} (μΑ) @ V _{RWM}	V _{BR} (\ (No	/) @ 뉴 te 2)	Ι _Τ	V _C (V) @ I _{PP} = 4 A (Note 3)	V _C (V) @ MAX I _{PP} (Note 3)	I _{PP} (A) (Note 3)	P _{PK} (W) (Note 3)	C (pF)
Device	Max	Max	Min	Max	mA	Max	Max	Max	Max	Тур
ESD8D15C	15	0.3	16	19.5	1.0	25	35	6.5	240	20

100

Other voltage available upon request.

- 3. Surge current waveform per Figure 1.



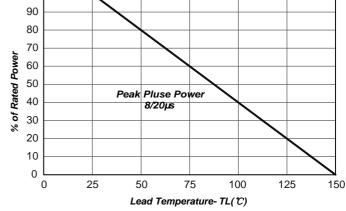
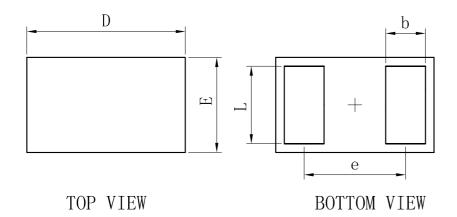


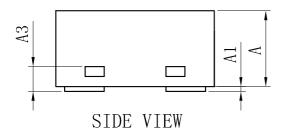
Fig1. Pulse Waveform

Fig2.Power Derating Curve

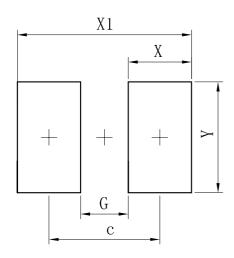
Outline and Dimensions



DFN1006-2L					
Dim	Min	Max			
D	0. 95	1.00	1.05		
Е	0. 55	0.60	0.65		
е	_	0.64	_		
L	0.44	0.49	0. 54		
b	0. 20	0. 25	0.30		
A	0. 43	0.48	0. 53		
A1	0	. 1	0.05		
A3	0. 127REF.				
All Dimensions in mm					



Soldering Footprint



Dimensions	(mm)
С	0.70
G	0.30
X	0.40
X1	1. 10
Y	0.70



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