

Description

The SX260N12TLG1 uses advanced technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 10V. This device is suitable for use as a Battery protection or in other Switching application.

General Features

$V_{DS} = 120V$ (Type: **135V**) $I_D = 260A$

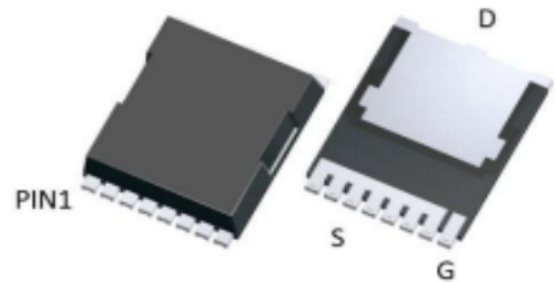
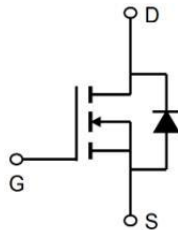
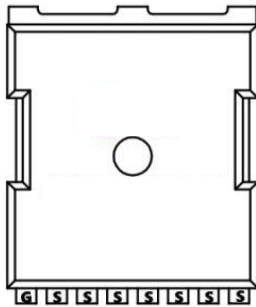
$R_{DS(ON)} < 4.2m\Omega$ @ $V_{GS}=10V$

Application

BMS

UPS

Power Management Switches



Absolute Maximum Ratings ($T_c=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	120	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D @ T_c=25^\circ C$	Continuous Drain Current, V_{GS} @ 10V	240	A
$I_D @ T_c=100^\circ C$	Continuous Drain Current, V_{GS} @ 10V	180	A
I_{DM}	Pulsed Drain Current	360	A
E_{AS}	Single Pulse Avalanche Energy	530	mJ
I_{AS}	Avalanche Current	45	A
$P_D @ T_c=25^\circ C$	Total Power Dissipation ⁴	240	W
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ C$
$R_{\theta JA}$	Thermal Resistance Junction-Ambient	0.75	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction-Case	35	$^\circ C/W$

Electrical Characteristics (T_c=25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Type	Max.	Unit
VDSS	Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = 250μA	120	135	-	V
IGSS	Gate-body Leakage current	V _{DS} = 0V, V _{GS} = ±20V	-	-	±100	nA
IDSS	Zero Gate Voltage Drain Current T _J =25°C	V _{DS} = 120V, V _{GS} = 0V	-	-	1	μA
IDSS	Zero Gate Voltage Drain Current T _J =100°C		-	-	100	
VGS(th)	Gate-Threshold Voltage	V _{DS} = V _{GS} , I _D = 250μA	2.0	2.9	4.0	V
RDS(on)	Drain-Source on-Resistance ²	V _{GS} = 10V, I _D = 20A	-	3.7	4.2	mΩ
RDS(on)	Drain-Source on-Resistance ²	V _{GS} = 6V, I _D = 20A		4.3	5.8	mΩ
Ciss	Input Capacitance	V _{GS} = 0V, V _{DS} = 60V, f = 250kHz	-	5240	-	pF
Coss	Output Capacitance		-	739	-	
Crss	Reverse Transfer Capacitance		-	12	-	
R _g	Gate Resistance	V _{GS} = 0V, V _{DS} = 0V, f = 1MHz	-	1.7	-	Ω
Q _g	Total Gate Charge	V _{DD} = 60V, I _D = 45A, V _{GS} = 0 to 10V	-	19	-	nC
Q _{gs}	Gate-Source Charge		-	11	-	
Q _{gd}	Gate-Drain Charge		-	75	-	
td(on)	Turn-on Delay Time	V _{DD} = 60V, V _{GS} = 10V, I _D = 45A, R _G = 10Ω	-	59	-	ns
t _r	Rise Time		-	41	-	
td(off)	Turn-off Delay Time		-	96	-	
t _f	Fall Time		-	33	-	
VSD	Diode Forward Voltage ²	I _F = 20A, V _{GS} = 0V	-	0.8	1.2	V
IS	Continuous Source Current ^{1,5}	V _G = V _D = 0V, Force Current	-	-	200	A
trr	Body Diode Reverse Recovery Time	V _R = 60V I _F = 35A, dI/dt = 100A/μs	-	70	-	ns
Q _{rr}	Body Diode Reverse Recovery Charge		-	200	-	nC

Notes:

- 1、The data tested by surface mounted on a 1 inch² FR-4 board with 20Z copper.
- 2、The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%
- 3、The EAS data shows Max. rating. The test condition is V_{DD} = 50V, V_{GS} = 10V, L = 0.5mH, I_{AS} = 45A
- 4、The power dissipation is limited by 150°C junction temperature
- 5、The data is theoretically the same as I_D and I_{DM}, in real applications, should be limited by total power dissipation.

Typical Characteristics

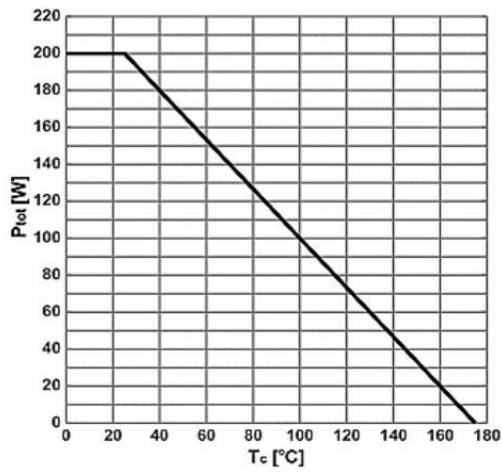


Figure 1. Power dissipation

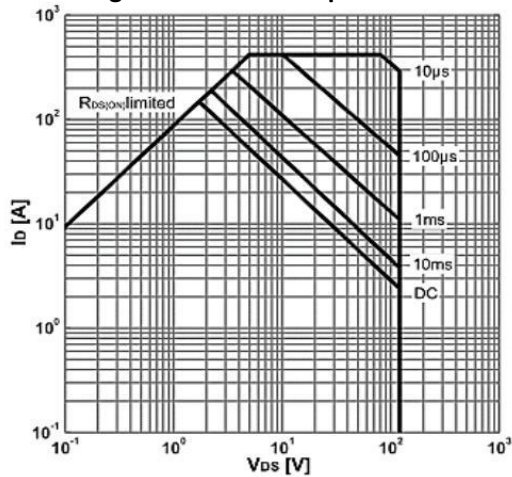


Figure 3. Safe operating area

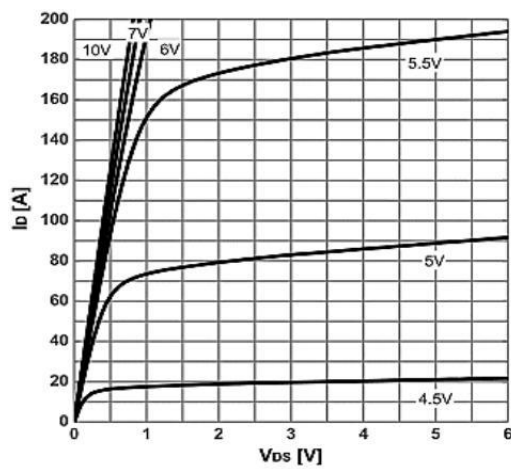


Figure 5. Typ. output characteristics(T_J 25°C)

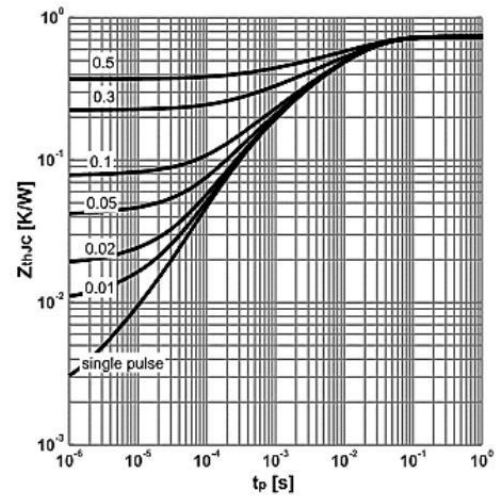


Figure 2. Max. transient thermal impedance

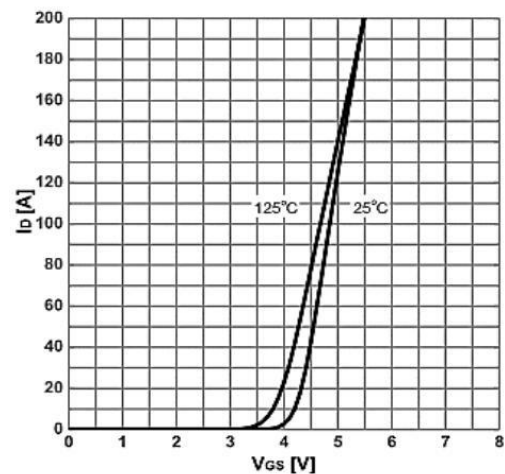


Figure 4. Type. transfer characteristics

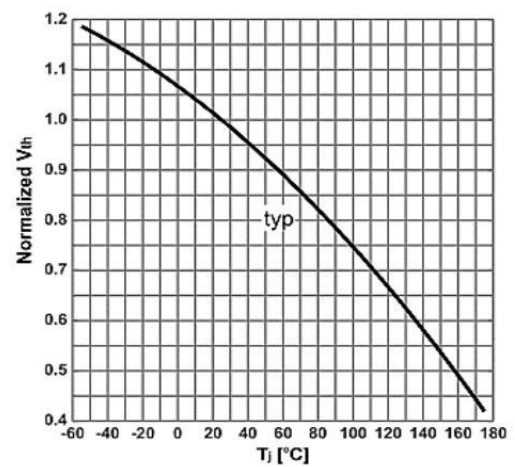


Figure 6. Typ. output characteristics(T_J 125°C)

Typical Characteristics

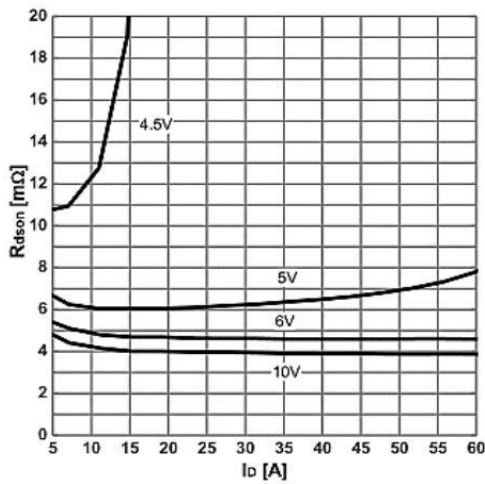


Figure 7. On-state resistance vs. Drain current

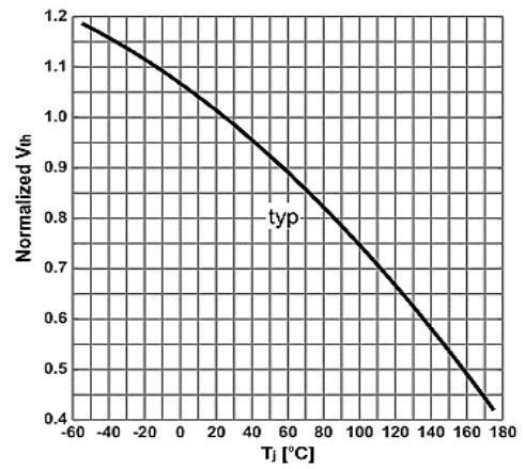


Figure 6. Gate threshold voltage vs. Junction Temperature

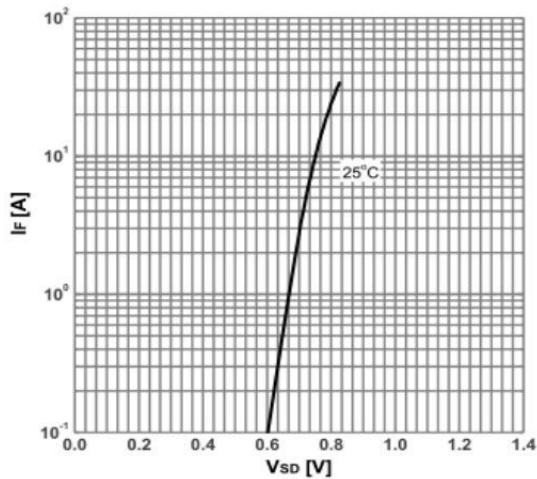


Figure 9. Forward characteristics of reverse diode

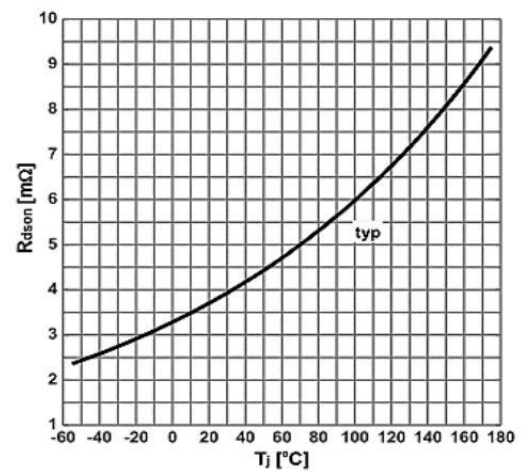


Figure 8. On-state resistance vs. Junction temperature

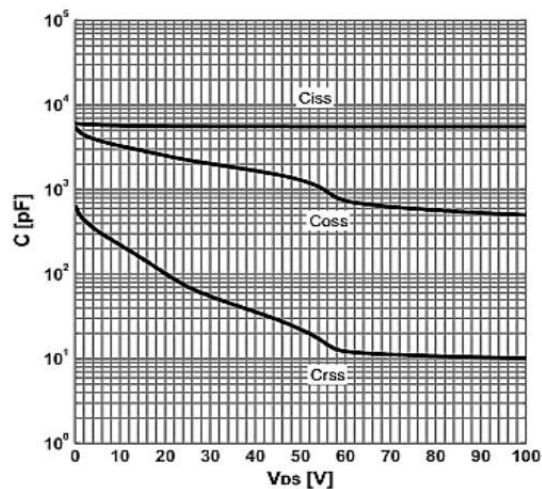


Figure9 Typ. capacitances

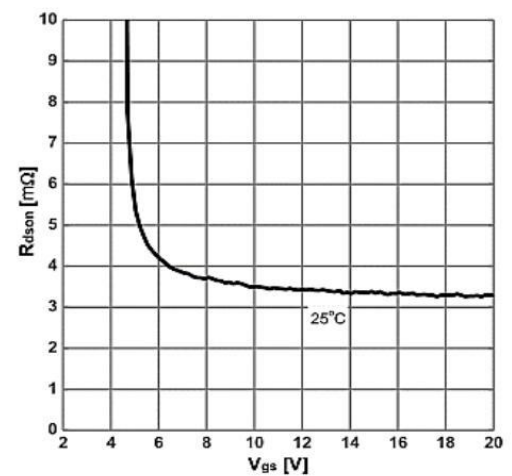


Figure11. On-state resistance vs. Vgs characteristics

Typical Characteristics

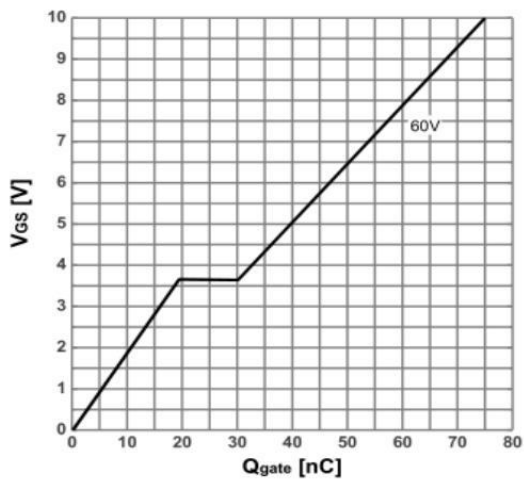
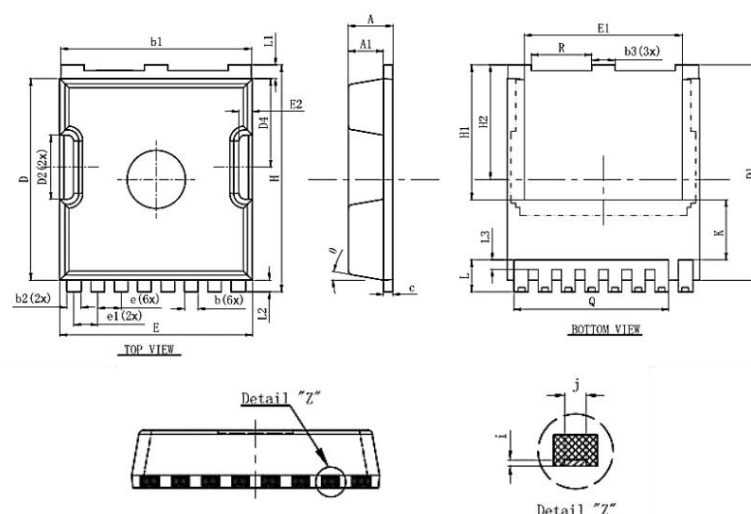


Figure 13: Typ. gate charge

Package Mechanical Data-TOLLA-8-XZ Single



Symbol	Dimensions In Millimeters		
	Min.	Nom	Max.
A	2.2	2.3	2.4
A1	1.7	1.8	1.9
b	0.6	0.7	0.8
b1	9.7	9.8	9.9
b2	0.65	0.75	0.85
b3	1.1	1.2	1.3
C	0.4	0.5	0.6
D	10.3	10.4	10.5
D1	11.0	11.1	11.2
D2	3.2	3.3	3.4
D4	4.47	4.57	4.67
E	9.8	9.9	10.0
E1	8.0	8.1	8.2
E2	0.5	0.6	0.7
e	1.200 (BSC)		
e1	1.225 (BSC)		
H	11.6	11.7	11.8
H1	6.95BSC		
H2	5.9BSC		
i	0.1REF		
j	0.350REF		
K	3.100REF		
L	1.55	1.65	1.75
L1	0.6	0.7	0.8
L2	0.5	0.6	0.7
L3	0.4	0.5	0.6
Q	7.95REF		
R	3.0	3.1	3.2
θ	10°REG		

Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
TAPING	TOLLA-8L		2000