

## Description

The SX2311MI uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a Battery protection or in other Switching application.

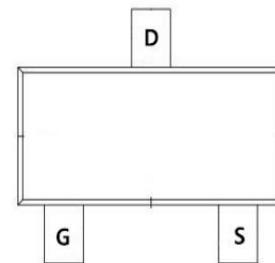
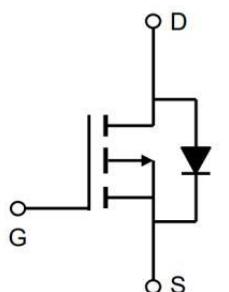
## General Features

$V_{DS} = -12V$   $I_D = -7.0A$

$R_{DS(ON)} < 24m\Omega$  @  $V_{GS}=4.5V$

## Application

electronic cigarette  
Load switch



## Absolute Maximum Ratings ( $T_c=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Rating	Units
VDSS	Drain-Source Voltage	-12	V
VGSS	Gate-Source Voltage	$\pm 12$	V
$I_D$ @ $T_c=25^\circ C$	Continuous Drain Current, $V_{GS}$ @ 10V <sup>1</sup>	-7.0	A
$I_D$ @ $T_c=100^\circ C$	Continuous Drain Current, $V_{GS}$ @ 10V <sup>1</sup>	-3.6	A
IDM	Pulsed Drain Current <sup>note1</sup>	-22	A
$P_D$ @ $T_c=25^\circ C$	Power Dissipation	1.6	W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	125	°C/W
TJ, TSTG	Operating and Storage Temperature Range	-55 to +150	°C

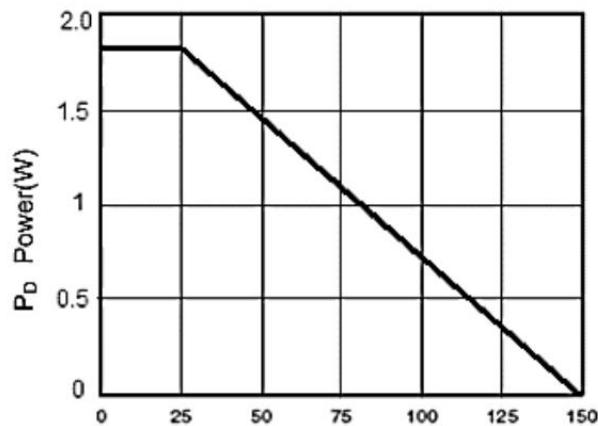
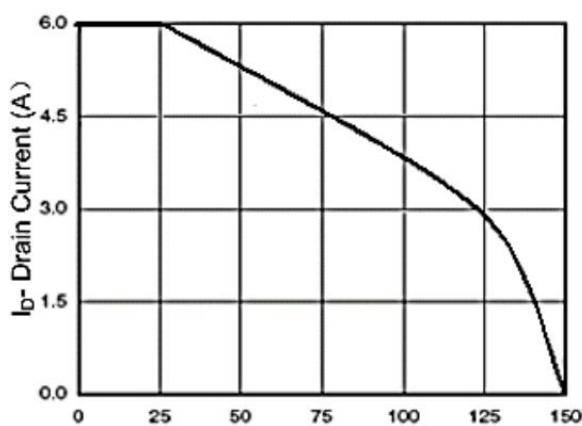
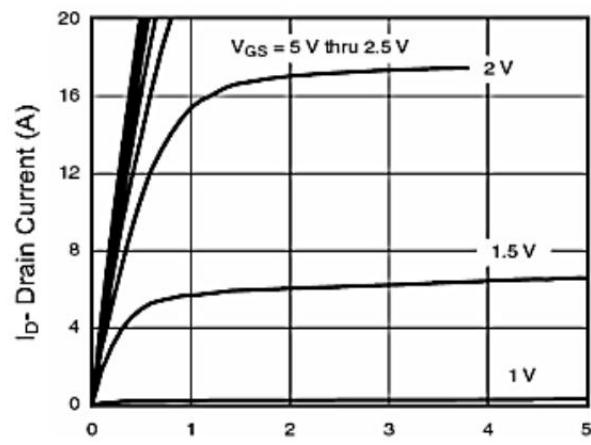
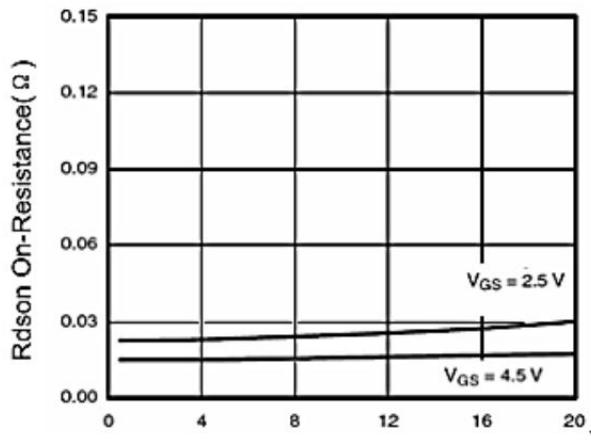
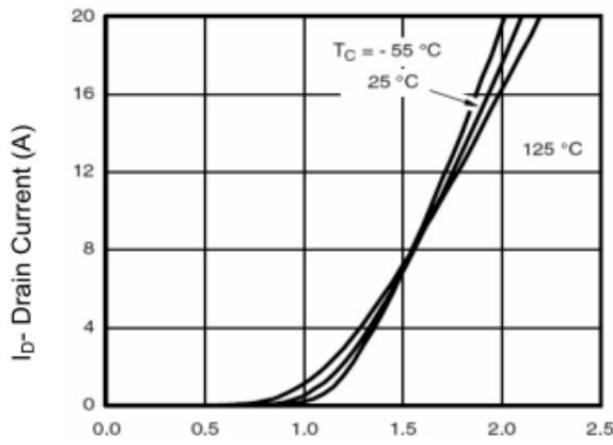
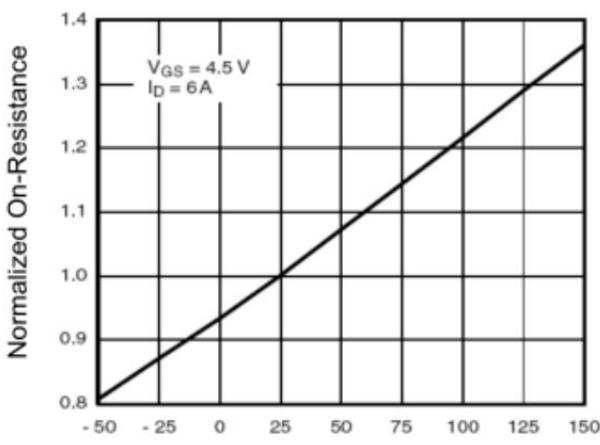
**Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)**

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
V(BR)DSS	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D=-250\mu\text{A}$	-12	-18	-	V
IDSS	Zero Gate Voltage Drain Current	$V_{DS} = -12\text{V}$ , $V_{GS} = 0\text{V}$ ,	-	-	-1	$\mu\text{A}$
IGSS	Gate to Body Leakage Current	$V_{DS} = 0\text{V}$ , $V_{GS} = \pm 12\text{V}$	-	-	$\pm 100$	nA
VGS(th)	Gate Threshold Voltage	$V_{DS}=V_{GS}$ , $I_D=-250\mu\text{A}$	-0.5	-0.65	-1.0	V
RDS(on)	Static Drain-Source on-Resistance note2	$V_{GS}=-4.5\text{V}$ , $I_D=-5.2\text{A}$	-	19	24	$\text{m}\Omega$
RDS(on)	Static Drain-Source on-Resistance note2	$V_{GS}=-2.5\text{V}$ , $I_D=-4.2\text{A}$		28	35	$\text{m}\Omega$
Ciss	Input Capacitance	$V_{DS}=-6\text{V}$ , $V_{GS}=0\text{V}$ $f=1.0\text{MHz}$	-	1100	-	pF
Coss	Output Capacitance		-	390	-	pF
Crss	Reverse Transfer Capacitance		-	300	-	pF
Qg	Total Gate Charge	$V_{DS}=-4\text{V}$ , $I_D=-4.1\text{A}$ , $V_{GS} = -4.5\text{V}$	-	11.5	-	nC
Qgs	Gate-Source Charge		-	1.5	-	nC
Qgd	Gate-Drain("Miller") Charge		-	3.2	-	nC
td(on)	Turn-on Delay Time	$V_{DD} = -4\text{V}$ , $I_D=-3.3\text{A}$ , $R_G=1.0\Omega$ , $V_{GEN}=-4.5\text{V}$ , $R_L=1.2\Omega$	-	25	-	ns
tr	Turn-on Rise Time		-	45	-	ns
td(off)	Turn-off Delay Time		-	72	-	ns
tf	Turn-off Fall Time		-	60	-	ns
IS	Maximum Continuous Drain to Source Diode Forward Current		-	-	-6.0	A
ISM	Maximum Pulsed Drain to Source Diode Forward Current		-	-	-16	A
VSD	Drain to Source Diode Forward Voltage	$V_{GS}=0\text{V}$ , $I_S = -4.1\text{A}$	-	-	-1.2	V
trr	Reverse Recovery Time	$V_{GS}=0\text{V}$ , $I_S = -4.1\text{A}$ , $di/dt=100\text{A}/\mu\text{s}$	-	20	-	ns
Qrr	Reverse Recovery Charge		-	9	-	nC

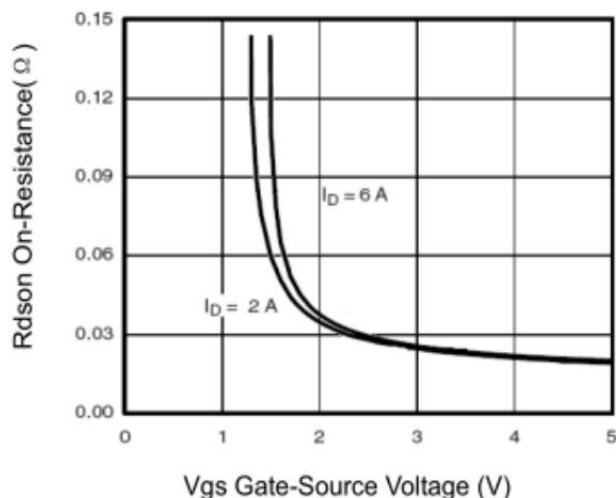
**Note :**

- 1、The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2、The data tested by pulsed , pulse width  $\leq 300\mu\text{s}$  , duty cycle  $\leq 2\%$
- 3、The power dissipation is limited by  $150^\circ\text{C}$  junction temperature
- 4、The data is theoretically the same as  $I_D$  and  $I_{DM}$  , in real applications , should be limited by total power dissipation.

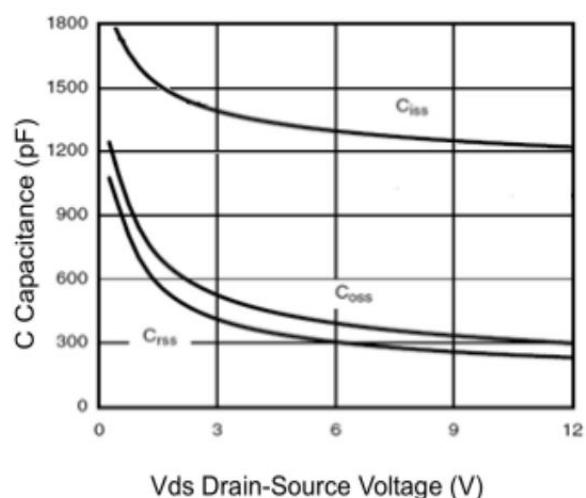
### Typical Characteristics

T<sub>j</sub>-Junction Temperature(°C)**Figure 1 Power Dissipation**T<sub>j</sub>-Junction Temperature(°C)**Figure 2 Drain Current**V<sub>DS</sub>- Drain-Source Voltage (V)**Figure 3 Output Characteristics**I<sub>D</sub>- Drain Current (A)**Figure 4 Drain-Source On-Resistance**V<sub>GS</sub>- Gate-Source Voltage (V)**Figure 5 Transfer Characteristics**T<sub>j</sub>-Junction Temperature(°C)**Figure 6 Drain-Source On-Resistance**

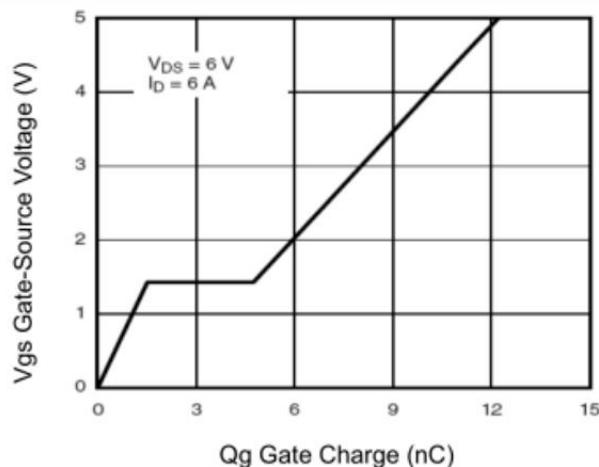
### Typical Characteristics



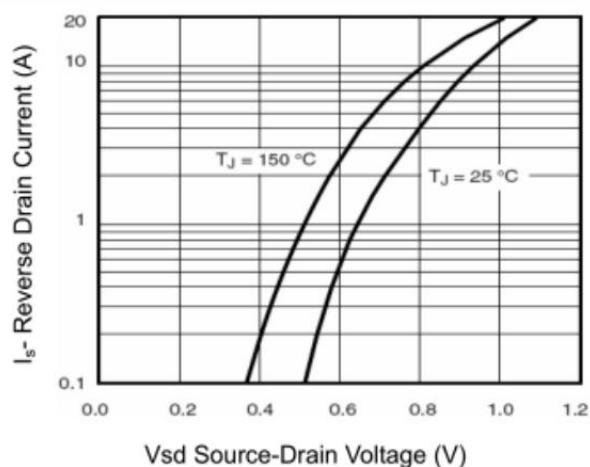
Vgs Gate-Source Voltage (V)

**Figure 7 Rdson vs Vgs**

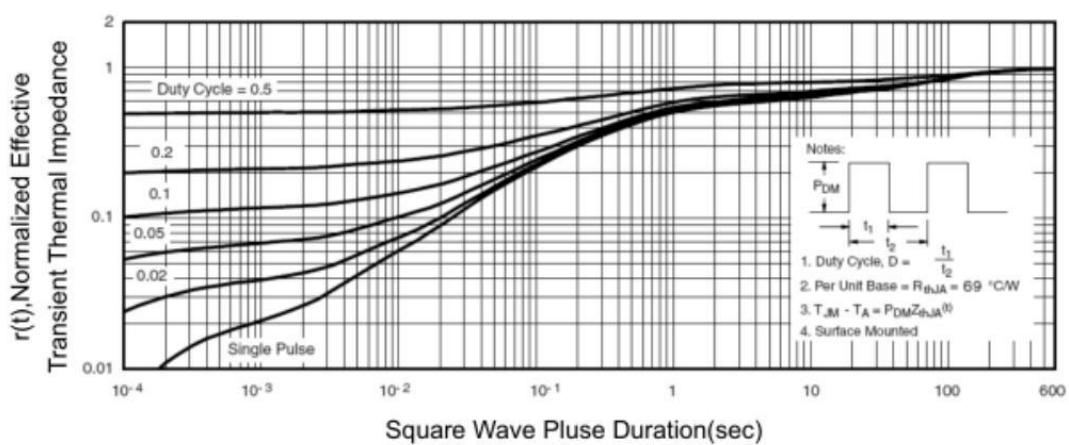
Vds Drain-Source Voltage (V)

**Figure 8 Capacitance vs Vds**

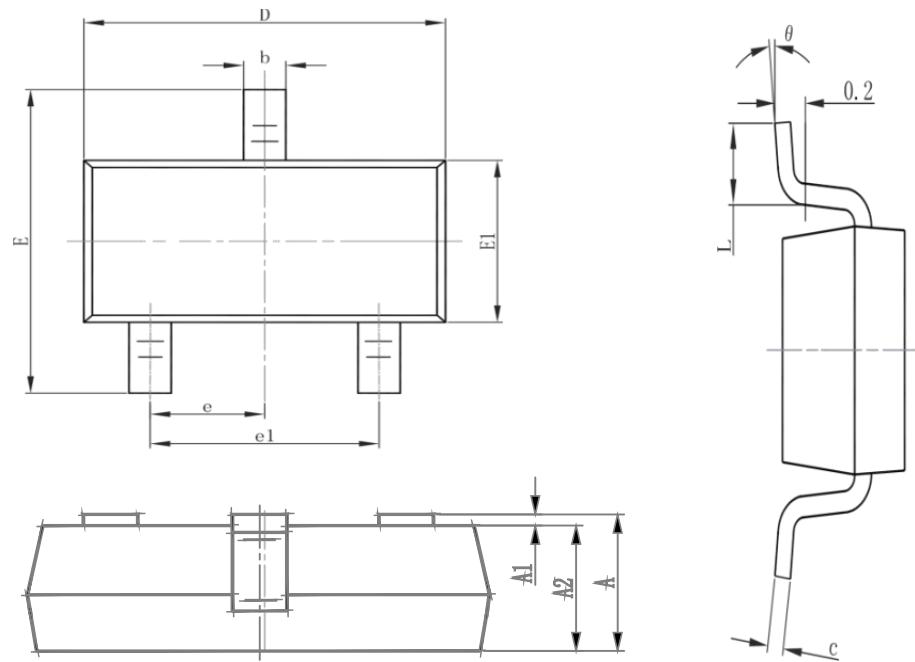
Qg Gate Charge (nC)

**Figure 9 Gate Charge**

Vsd Source-Drain Voltage (V)

**Figure 10 Source- Drain Diode Forward****Figure 12 Normalized Maximum Transient Thermal Impedance**

## MOSFET Package Mechanical Data-SOT23-3



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E1	1.500	1.700	0.059	0.067
E	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

### Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
TAPING	SOT23		3000