



50N10

N- Channel Advanced Power MOSFET

MAIN CHARACTERISTICS

I _D	50A
V _{DSS}	100V
R _{DSON-typ} (@V _{GS} =10V)	14mΩ

FEATURES

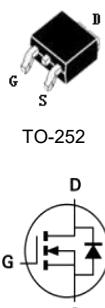
- Ultra-Low RDS(ON)
- Low Gate Charge
- High Current Capability

APPLICATIONS

- Power Management in Telecom.,Industrial Automation
- Motor Driving in Power Tool,E-vehicle,Robotics
- Current Switching in DC/DC&AC/DC(SR) Sub-systems

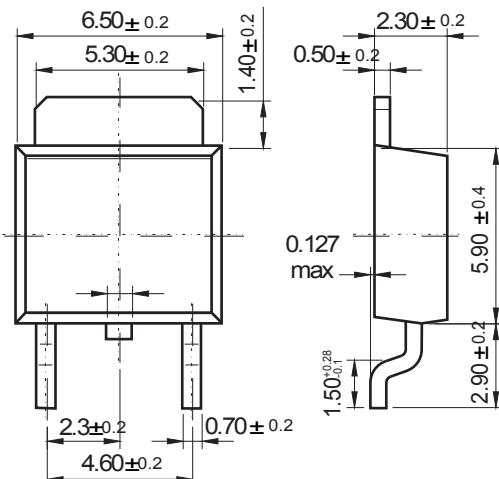
MECHANICAL DATA

- Case: Molded plastic
- Mounting Position: Any
- Molded Plastic: UL Flammability Classification Rating 94V-0
- Lead free in compliance with EU RoHS 2011/65/EU directive
- Solder bath temperature 275°C maximum,10s per JESD 22-B106



TO-252

Unit: mm



Maximum Ratings at T_c=25°C unless otherwise specified

Characteristics	Symbol	Value	Unit
Drain-Source Voltage	V _{DS}	100	V
Gate-Source Voltage	V _{GS}	±20	V
Continue Drain Current	I _D	50	A
Pulsed Drain Current (Note1)	I _{DM}	200	A
Power Dissipation	P _D	82	W
Single Pulse Avalanche Energy (Note5)	E _{AS}	74	mJ
Operating Temperature Range	T _J	150	°C
Storage Temperature Range	T _{STG}	-55 to +150	°C
Thermal Resistance, Junction to Case(Note 2)	R _{θJC}	1.5	°C/W
Thermal Resistance, Junction to Ambient	R _{θJA}	39	°C/W

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Electrical Characteristics at $T_c=25^\circ\text{C}$ unless otherwise specified

Characteristics	Test Condition	Symbol	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	BV_{DSS}	100	-	-	V
Drain-Source Leakage Current	$V_{DS} = 100\text{V}, V_{GS} = 0 \text{ V}$	I_{DSS}	-	-	1	μA
	$V_{DS}=100\text{V}, T_c=125^\circ\text{C}$		-	-	100	μA
Gate Leakage Current	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	I_{GSS}	-	-	± 100	nA
Gate-Source Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$	$V_{GS(\text{th})}$	1.2	-	2.2	V
Drain-Source On-State Resistance (Note 3)	$V_{GS} = 10 \text{ V}, I_D = 20\text{A}$	$R_{DS(\text{on})}$	-	14	20	$\text{m}\Omega$
	$V_{GS} = 4.5 \text{ V}, I_D = 15\text{A}$		-	18.6	25	$\text{m}\Omega$
Input Capacitance	$V_{GS} = 0 \text{ V}, V_{DS} = 50 \text{ V}, f = 1\text{MHz}$	C_{iss}	-	992	-	pF
Output Capacitance		C_{oss}	-	330	-	pF
Reverse Transfer Capacitance		C_{rss}	-	19.2	-	pF
Turn-on Delay Time	$V_{DS}=50\text{V}, ID=20\text{A}$ $V_{GS}=10\text{V}, RG=6.2\Omega$ (Note3,4)	$t_{d(\text{ON})}$	-	7	-	ns
Rise Time		t_r	-	18	-	ns
Turn-Off Delay Time		$t_{d(\text{OFF})}$	-	21	-	ns
Fall Time		t_f	-	9	-	ns
Total Gate Charge	$V_{DS}=50\text{V}, ID=20\text{A},$ $V_{GS}=10\text{V}$ (Note3,4)	Q_G	-	19	-	nC
Gate to Source Charge		Q_{GS}	-	4	-	nC
Gate to Drain Charge		Q_{GD}	-	5	-	nC

Source-Drain Diode Characteristics at $T_a=25^\circ\text{C}$ unless otherwise specified

Characteristics	Test Condition	Symbol	Min.	Typ.	Max.	Unit
Maximum Body-Diode Continuous Current (Note 2)		I_S	-	-	50	A
Maximum Body-Diode Pulsed Current		I_{SM}	-	-	200	A
Drain-Source Diode Forward Voltage	$I_{SD} = 20 \text{ A}$	V_{SD}	-	-	1.2	V
Reverse Recovery Time	$I_S = I_F, ISD=20\text{A}, V_{GS} = 0 \text{ V},$ $dI / dt = 100 \text{ A}/\mu\text{s}$ (Note3)	trr	-	32	-	ns
Reverse Recovery Charge		Qrr	-	32	-	μC

Note2:Pulse test: 300 μs pulse width, 2 % duty cycle

RATING AND CHARACTERISTIC CURVES (50N10)

Figure 1: Power De-rating

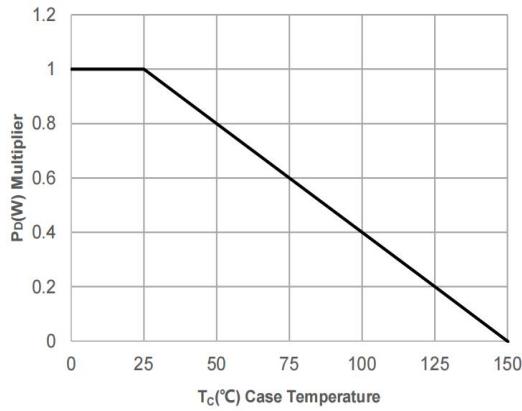


Figure 2: Current De-rating

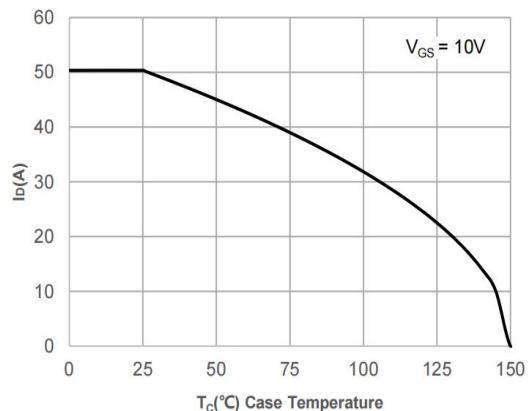


Figure 3: Normalized Maximum Transient Thermal Impedance

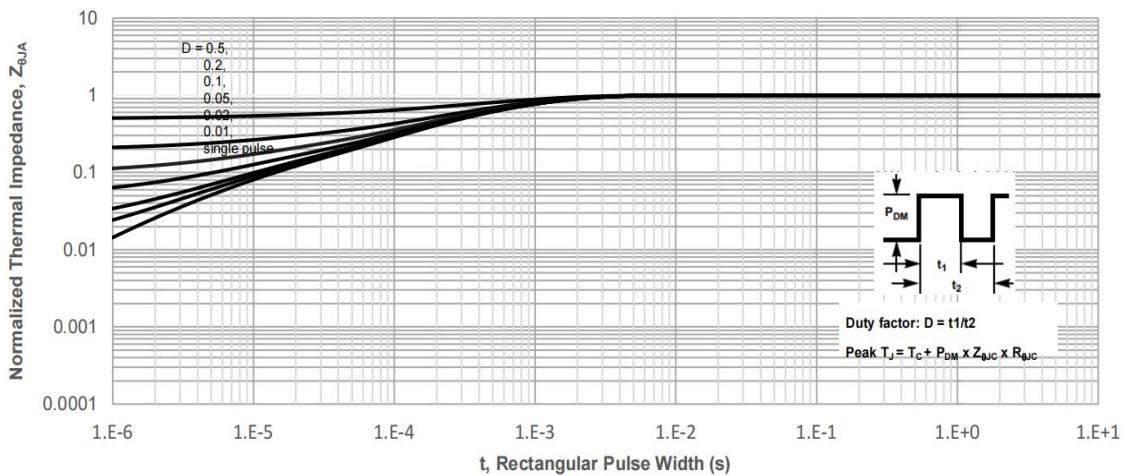
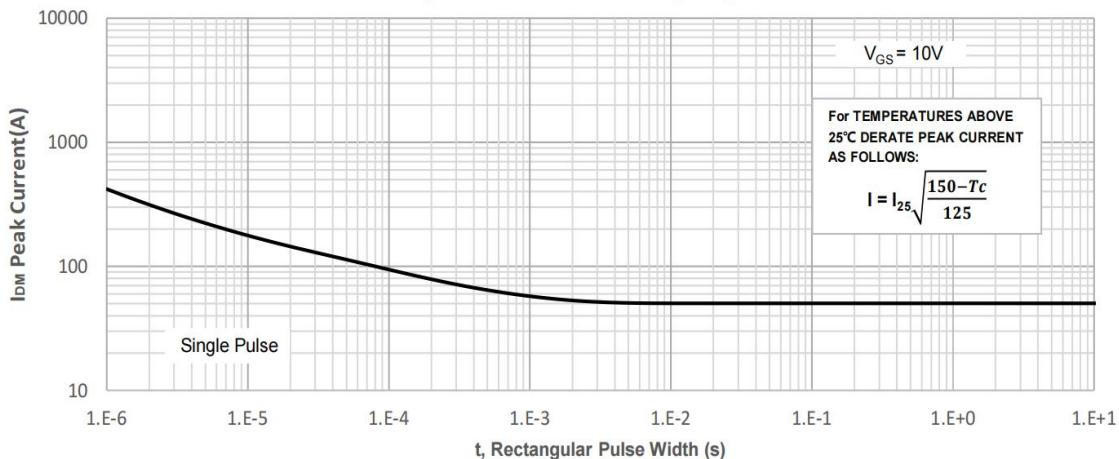


Figure 4: Peak Current Capacity



RATING AND CHARACTERISTIC CURVES (50N10)

Figure 5: Output Characteristics

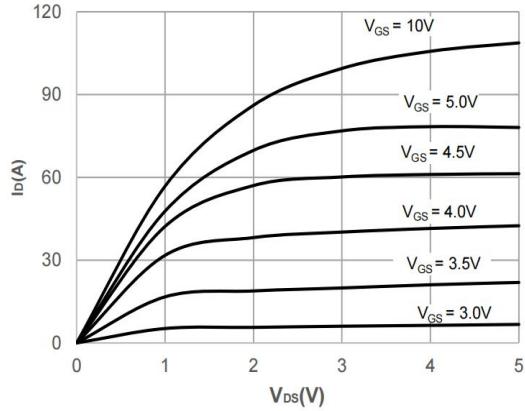


Figure 6: Typical Transfer Characteristics

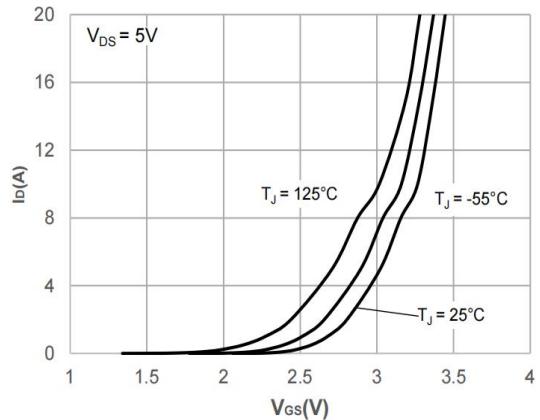


Figure 7: On-resistance vs. Drain Current

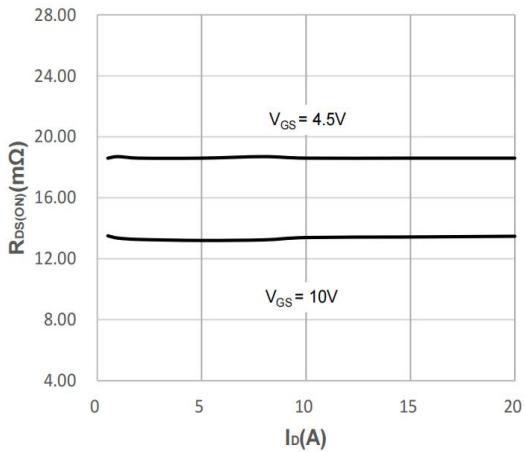


Figure 8: Body Diode Characteristics

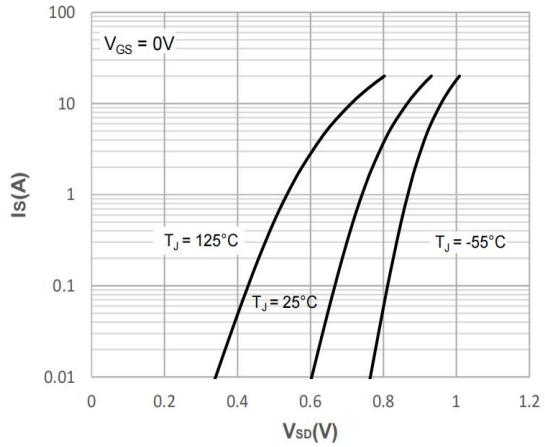


Figure 9: Gate Charge Characteristics

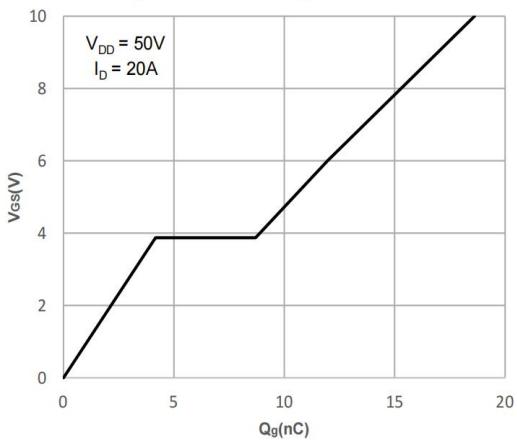
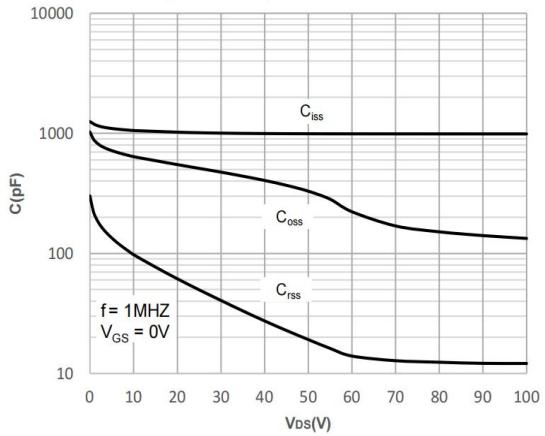


Figure 10: Capacitance Characteristics



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Figure 11: Normalized Breakdown voltage vs. Junction Temperature

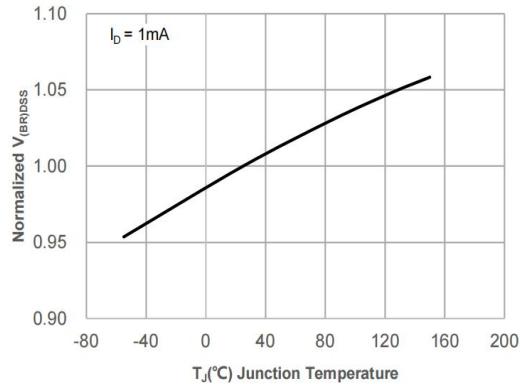


Figure 12: Normalized on Resistance vs. Junction Temperature

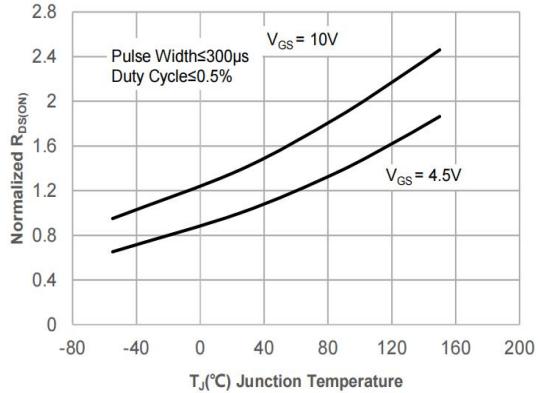


Figure 13: Normalized Threshold Voltage vs. Junction Temperature

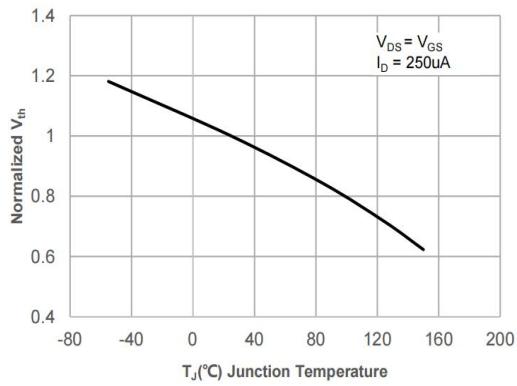


Figure 14: $R_{DS(ON)}$ vs. V_{GS}

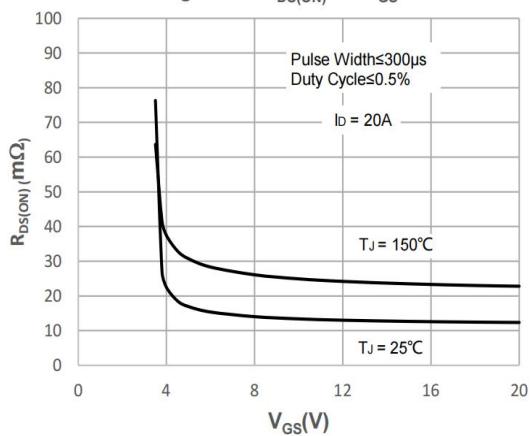


Figure 15: Maximum Safe Operating Area

