

Description

The SX240N10T uses advanced technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 10V. This device is suitable for use as a Battery protection or in other Switching application.

General Features

$V_{DS} = 100V$ $I_D = 240A$

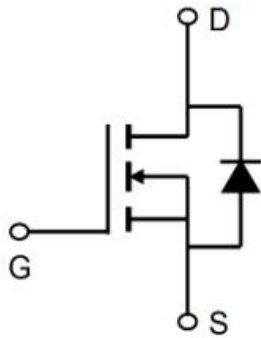
$R_{DS(ON)} < 2.8m\Omega$ @ $V_{GS} = 10V$

Application

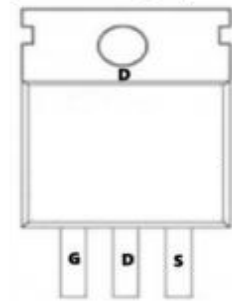
DC/DC Converter

LED Backlighting

Power Management Switches



TO-263-3L



Absolute Maximum Ratings ($T_c = 25^\circ C$ unless otherwise noted)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	100	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D @ T_c = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	240	A
$I_D @ T_c = 100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	180	A
IDM	Pulsed Drain Current	960	A
EAS	Single Pulse Avalanche Energy	2340	mJ
IAS	Avalanche Current	53.4	A
$P_D @ T_c = 25^\circ C$	Total Power Dissipation ⁴	313	W
TSTG	Storage Temperature Range	-55 to 175	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 175	$^\circ C$
$R_{\theta JA}$	Thermal Resistance Junction-Ambient	0.15	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction-Case	40	$^\circ C/W$

Electrical Characteristics (T_C=25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V(BR)DSS	Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = 250μA	100	107	-	V
IGSS	Gate-body Leakage current	V _{DS} = 0V, V _{GS} = ±20V	-	-	±100	nA
IDSS	Zero Gate Voltage Drain Current T _J =25°C	V _{DS} = 100V, V _{GS} = 0V	-	-	1	μA
	Zero Gate Voltage Drain Current T _J =100°C		-	-	100	
VGS(th)	Gate-Threshold Voltage	V _{DS} = V _{GS} , I _D = 250μA	2.0	3.0	4.0	V
RDS(on)	Drain-Source on-Resistance ⁴	V _{GS} = 10V, I _D = 50A	-	2.2	2.8	mΩ
gfs	Forward Transconductance ⁴	V _{DS} = 10V, I _D = 50A	-	190	-	S
Ciss	Input Capacitance	V _{DS} = 50V, V _{GS} = 0V, f = 1MHz	-	11300	-	pF
Coss	Output Capacitance		-	1400	-	
Crss	Reverse Transfer Capacitance		-	48	-	
R _g	Gate Resistance	f = 1MHz	-	2	-	Ω
Q _g	Total Gate Charge	V _{GS} = 10V, V _{DS} = 50V, I _D = 20A	-	165	-	nC
Q _{gs}	Gate-Source Charge		-	67	-	
Q _{gd}	Gate-Drain Charge		-	35	-	
td(on)	Turn-on Delay Time	V _{GS} = 10V, V _{DD} = 50V, R _G = 3Ω, I _D = 20A	-	37	-	ns
t _r	Rise Time		-	112	-	
td(off)	Turn-off Delay Time		-	85	-	
t _f	Fall Time		-	115	-	
trr	Body Diode Reverse Recovery Time	I _F = 30A, dI/dt=500A/μs	-	100	-	ns
Q _{rr}	Body Diode Reverse Recovery Charge	I _F = 30A, dI/dt=100A/μs	-	323	-	nC
VSD	Diode Forward Voltage ⁴	I _S = 50A, V _{GS} = 0V	-	-	1.2	V
IS	Continuous Source Current T _C =25°C	-	-	-	300	A

Notes:

- 1、The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2、The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
- 3、The EAS data shows Max. rating . The test condition is V_{DD}=50V, V_{GS}=10V, L=0.5mH, I_{AS}=50A
- 4、The power dissipation is limited by 150°C junction temperature
- 5、The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

Typical Characteristics

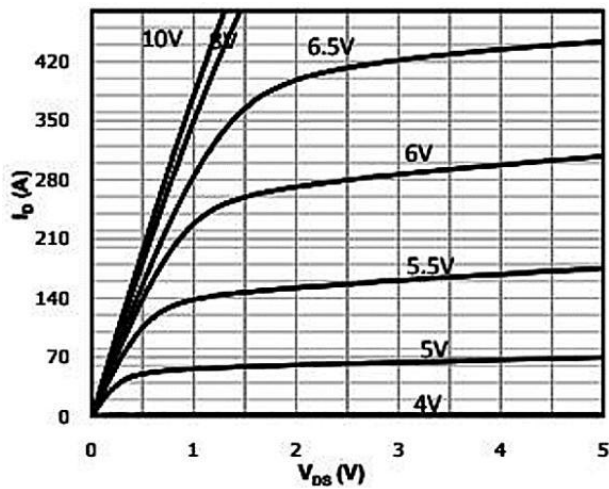


Figure 1. Output Characteristics

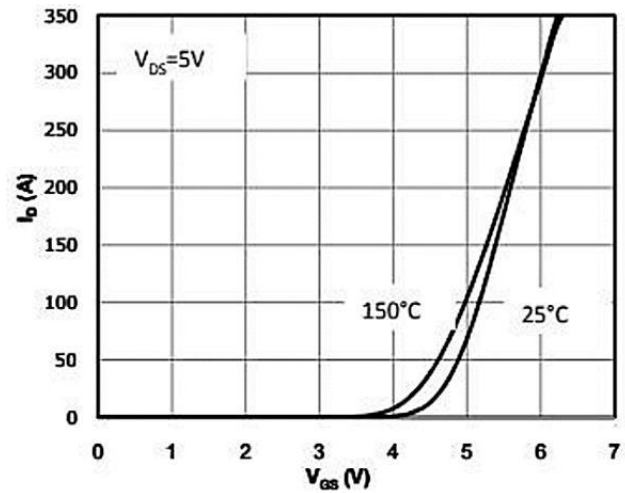


Figure 2. Transfer Characteristics

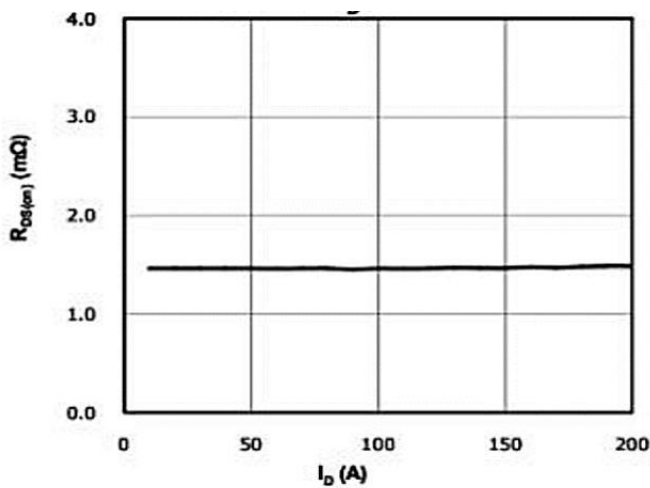


Figure 3. R_DS (ON) VS Drain Current and V_GS

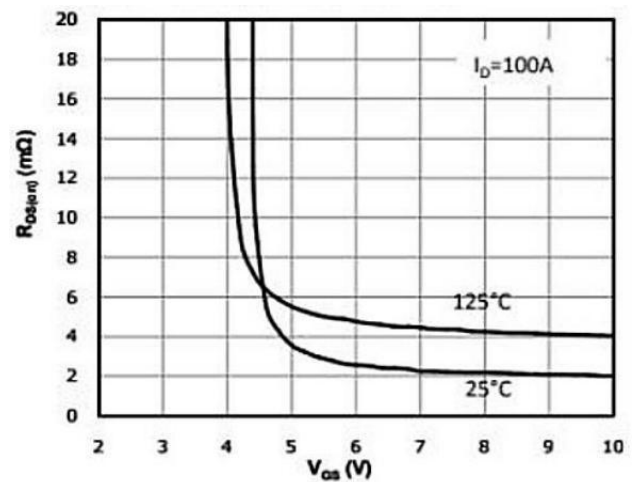


Figure 4. R_DS(ON) vs. V_GS

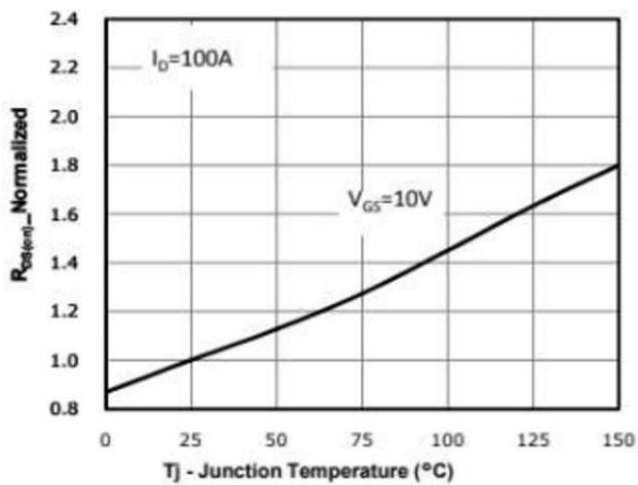


Figure 5. R_DS(ON) vs. Temperature

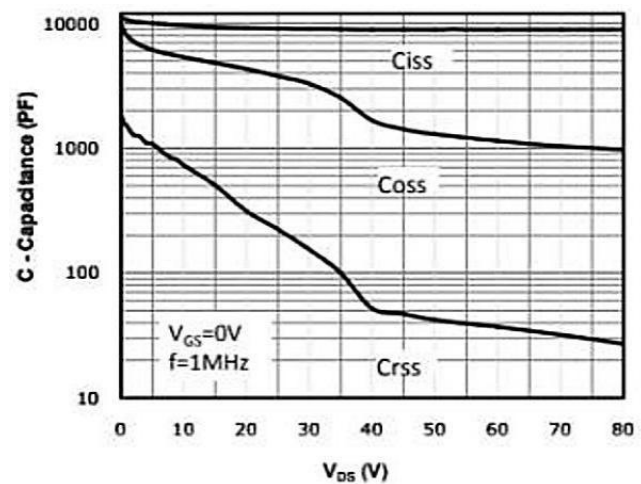


Figure 6. Capacitance Characteristics

Typical Characteristics

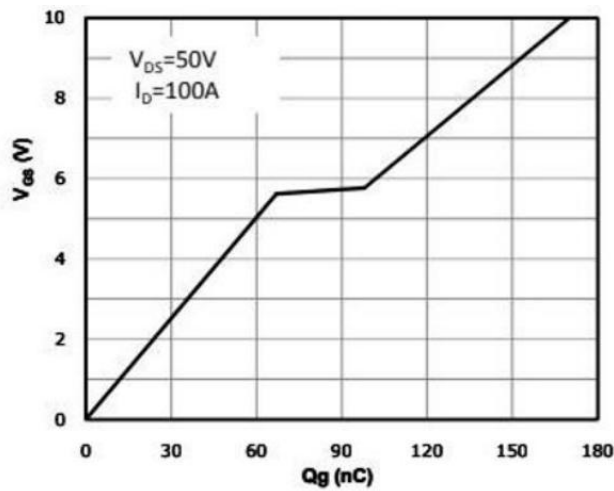


Figure 7. Gate Charge Characteristics

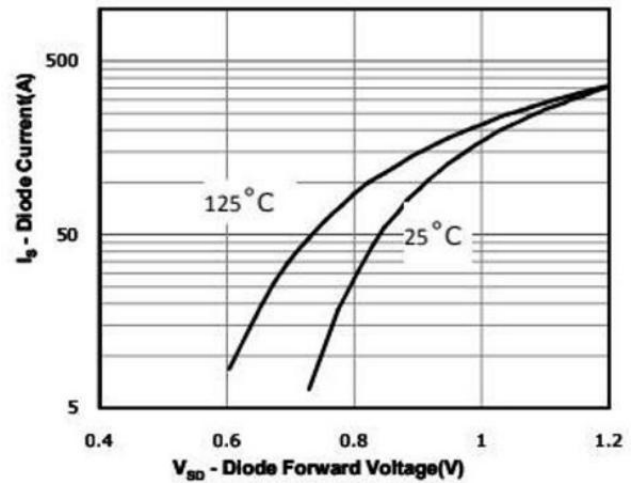


Figure 8. Body-Diode Forward Characteristics

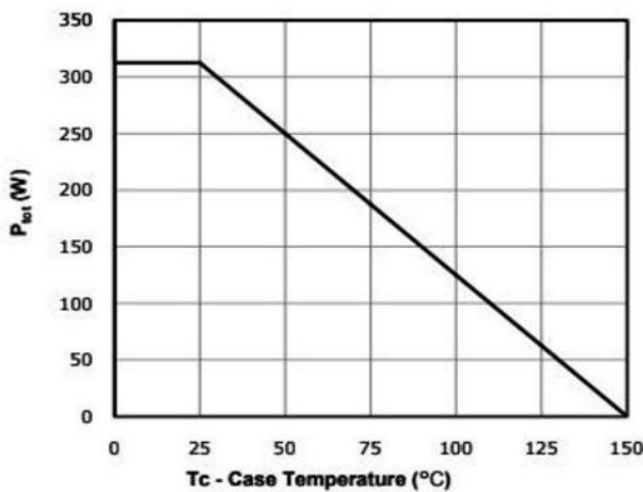


Figure 9. Power Dissipation

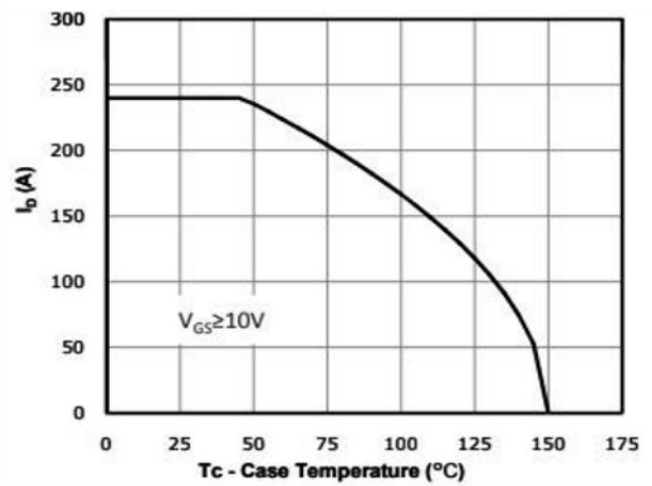


Figure 10. Drain Current Degrating

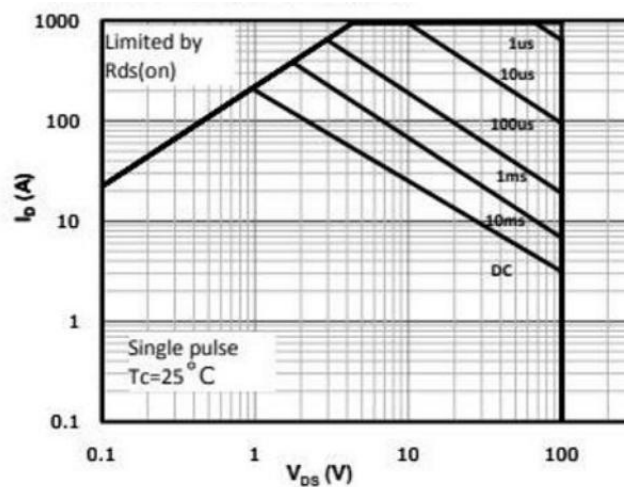
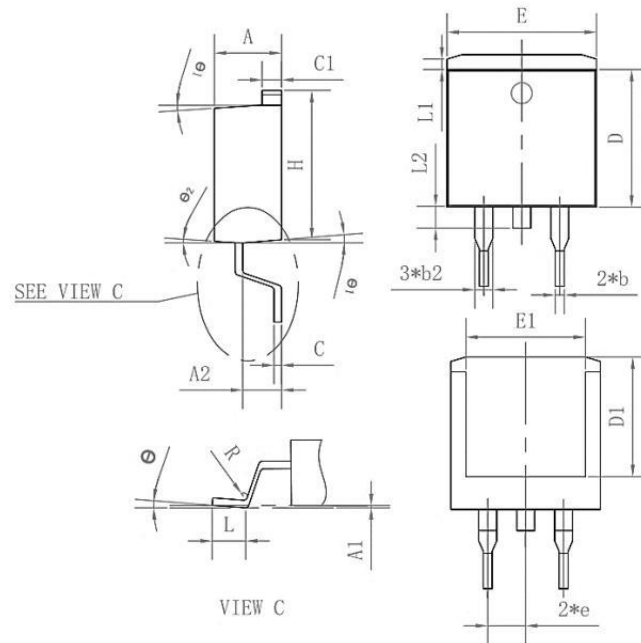


Figure 11. Safe Operating Area

Package Mechanical Data-TO-263-3L-SLK



Symbol	Common		
	mm		
	Mim	Nom	Max
A	4.35	4.47	4.60
A1	0.09	0.10	0.11
A2	2.30	2.40	2.70
b	0.70	0.80	1.00
b2	1.25	1.36	1.50
C	0.45	0.50	0.65
C1	1.29	1.30	9.40
D	9.10	9.20	9.30
D1	7.90	8.00	8.10
E	9.85	10.00	10.20
E1	7.90	8.00	8.10
H	15.30	15.50	15.70
e	-	2.54	-
L	2.34	2.54	2.74
L1	1.00	1.10	1.20
L2	1.30	1.40	1.50
R	0.24	0.25	0.26
θ	0°	4°	8°
θ1	4°	7°	10°
θ2	0°	3°	6°

Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
TAPING	TO-263-3L		800