

16mΩ, 100V, N-Channel Power MOSFET
SRT10N160LTC-E
General Description

The Sanrise SRT10N160LTC-E is a low voltage power MOSFET, fabricated using advanced split gate trench technology. The resulting device has extremely low on resistance, low gate charge and fast switching time, making it especially suitable for applications which require superior power density and synchronous rectification.

The SRT10N160LTC-E break down voltage is 100V and it has a high rugged avalanche characteristics.

The SRT10N160LTC-E is available in TO-220C package.

Features

- Ultra Low $R_{DS(ON)}$
 $R_{DS(ON_TYP)} = 13.8m\Omega @ V_{GS} = 10V.$
 $R_{DS(ON_TYP)} = 18.6m\Omega @ V_{GS} = 4.5V$
- Ultra Low Gate Charge,
 $Q_g = 16.1nC \text{ typ} @ V_{GS} = 10V.$
 $Q_g = 8.0nC \text{ typ} @ V_{GS} = 4.5V.$
- Fast switching capability
- Robust design with better EAS performance

Application

- Charger/Adapter
- DC/DC Power Supply
- E-Tools
- BMS

Ordering Information

SRT10N160L □ □ - □		
Circuit Type	□ □	E: Lead Free
Package	□ □	G: Green
TC: TO-220C		Blank: Tube
		TR: Tape & Reel

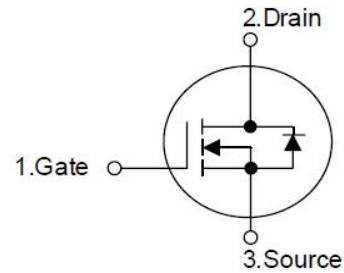
Symbol


Figure 1 Symbol of SRT10N160LTC-E

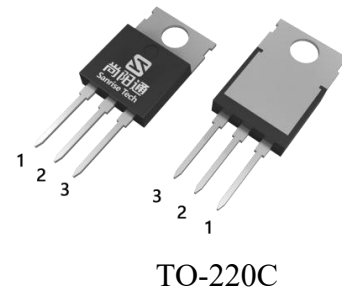
Package Type


Figure2 Package Type of SRT10N160LTC-E

Package	Part Number	Marking ID	Packing Type
TO-220C	SRT10N160LTC-E	SRT10N160LTCE	Tube

Absolute Maximum Ratings

Parameter		Symbol	Rating	Unit
Drain-Source Voltage		V_{DSS}	100	V
Gate-Source Voltage		V_{GSS}	±20	V
Continuous Drain Current	$T_C=25^{\circ}\text{C}$	I_D	42	A
	$T_C=100^{\circ}\text{C}$		26.6	
Pulsed Drain Current (Note 2)		I_{DM}	126	A
Avalanche Energy, Single Pulse (Note 3)		E_{AS}	40	mJ
Avalanche Energy, Repetitive (Note 2)		E_{AR}	0.1	mJ
Avalanche Current, Repetitive (Note 2)		I_{AR}	15	A
Continuous Diode Forward Current		I_S	40	A
Diode Pulse Current		$I_{S,PULSE}$	120	A
Operating Junction Temperature		T_J	150	°C
Storage Temperature		T_{STG}	-55 to 150	°C
Lead Temperature (Soldering, 10 sec)		T_{LEAD}	260	°C

Note:

1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
2. Repetitive Rating: Pulse width limited by maximum junction temperature
3. $I_{AS}=15.0\text{A}$, $V_{DD}=50\text{V}$, $R_G=25\Omega$, Starting $T_J=25^{\circ}\text{C}$

Thermal Resistance

Parameter	Symbol	Min	Typ	Max	Unit
Thermal Resistance, Junction-to-Case	R_{thJC}			2.2	°C/W
Thermal Resistance, Junction-to-Ambient	R_{thJA}			62	°C/W

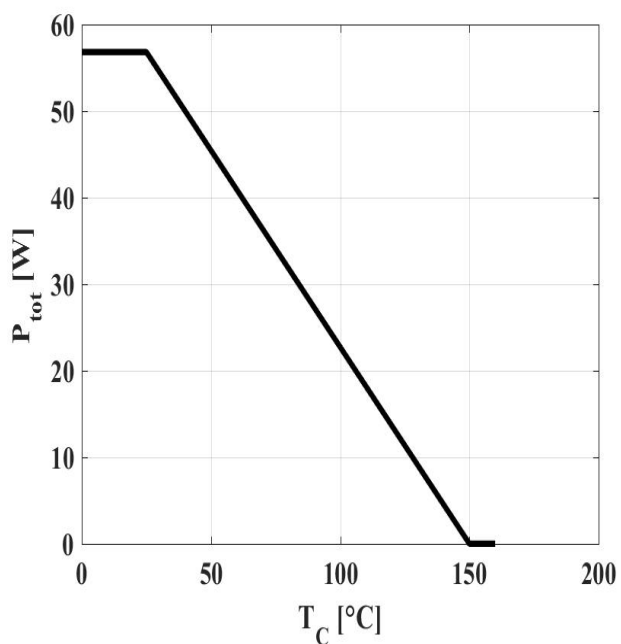
16mΩ, 100V, N-Channel Power MOSFET
SRT10N160LTC-E
Electrical Characteristics

$T_J = 25^\circ\text{C}$, unless otherwise specified.

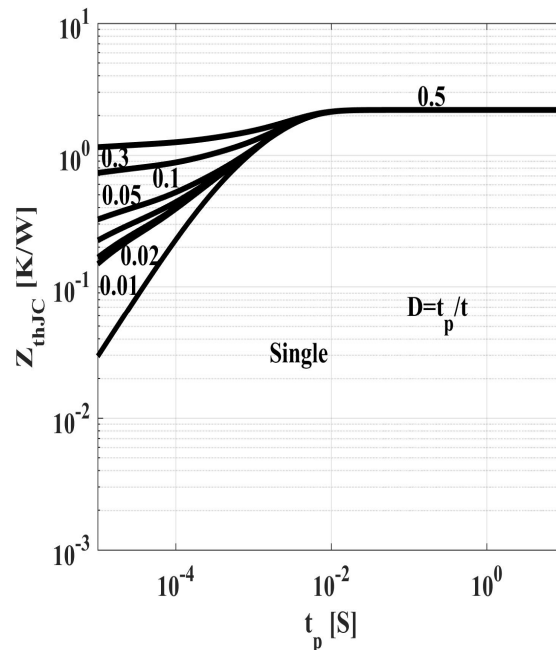
Parameter		Symbol	Test Conditions	Min	Typ	Max	Unit
Statistic Characteristics							
Drain-Source Breakdown Voltage		BV _{DSS}	V _{GS} =0V, I _D =250uA	100			V
Zero Gate Voltage Drain Current		I _{DSS}	V _{DS} =100V, V _{GS} =0V			1	uA
Gate-Body Leakage Current	Forward	I _{GSSF}	V _{GS} =20V, V _{DS} =0V			100	nA
	Reverse	I _{GSSR}	V _{GS} =-20V, V _{DS} =0V			-100	
Gate Threshold Voltage		V _{GS(TH)}	V _{DS} =V _{GS} , I _D =0.25mA	1.2	1.8	2.4	V
Static Drain-Source On-Resistance		R _{DS(ON)}	V _{GS} =4.5V, I _D =10A		18.6	24.5	mΩ
		R _{DS(ON)}	V _{GS} =10V, I _D =20A		13.8	16.0	mΩ
Gate Resistance		R _G	f=1MHz, Open Drain		1.0		Ω
Dynamic Characteristics							
Input Capacitance		C _{ISS}	V _{DS} =50V, V _{GS} =0V, f=1MHz		1000		pF
Output Capacitance		C _{OSS}			310		pF
Reverse Transfer Capacitance		C _{RSS}			6		pF
Effective output capacitance, energy related ^{NOTE5}		C _{O(er)}	V _{GS} =0V, V _{DS} =0...60V		422		pF
Effective output capacitance, time related ^{NOTE6}		C _{O(tr)}			497		
Turn-on Delay Time		t _{d(on)}	V _{DD} =50V, I _D =20A R _G =3.0Ω, V _{GS} =10V		5.9		nS
Rise Time		t _r			3.9		
Turn-off Delay Time		t _{d(off)}			14.8		
Fall Time		t _f			3.1		
Gate Charge Characteristics							
Gate to Source Charge		Q _{gs}	V _{DD} =50V, I _D =20A V _{GS} =0 to 4.5V		3.5		nC
Gate to Drain Charge		Q _{gd}			2.8		
Gate Charge Total		Q _g			8.0		
Gate Charge Total		Q _g	V _{DD} =50V, I _D =20A V _{GS} =0 to 10V		16.1		nC
Gate Charge Total, sync FET		Q _g	V _{DD} =0.1V, V _{GS} =0 to 10V		14.3		nC
Reverse Diode Characteristics							
Drain-Source Diode Forward Voltage		V _{SD}	V _{GS} =0V, I _{SD} =20A		0.89	1.1	V
Reverse Recovery Time		t _{rr}	V _R =50V, I _F =20A dI _F /dt=100A/us		25		nS
Reverse Recovery Charge		Q _{rr}			24		nC
Peak Reverse Recovery Current		I _{rrm}			1.9		A

Note:

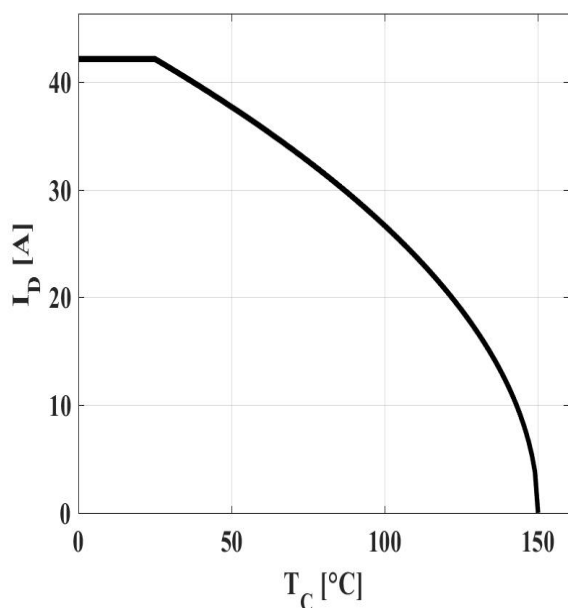
- $C_{O(er)}$ is a fixed capacitance that gives the same stored energy as C_{OSS} while V_{DS} is rising from 0 to 60V
- $C_{O(tr)}$ is a fixed capacitance that gives the same charging time as C_{OSS} while V_{DS} is rising from 0 to 60V

Typical Performance Characteristics
Figure 3: Power Dissipation


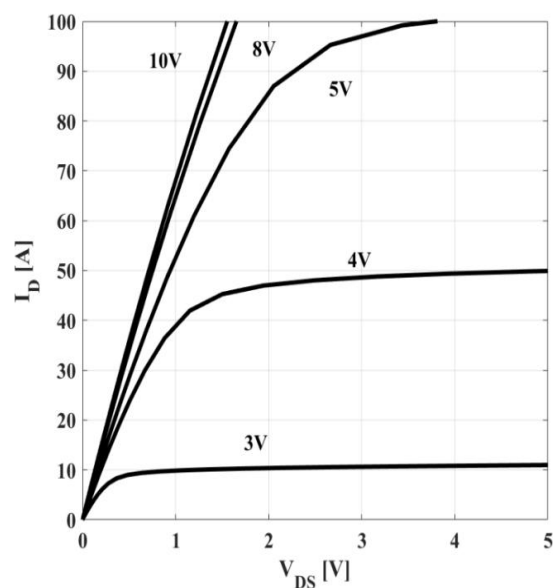
$$P_{tot}=f(T_C) \quad (\text{DFN5*6})$$

Figure 4: Max. Transient Thermal Impedance


$$Z_{(th)JC}=f(t_p); \text{parameter: } D=t_p/T \quad (\text{DFN5*6})$$

Figure5: Drain Current


$$I_D=f(T_C); V_{GS} \geq 10V$$

Figure6: Typ. Output Characteristics


$$I_D=f(V_{DS}); T_j=25^{\circ}C; \text{parameter: } V_{GS}$$

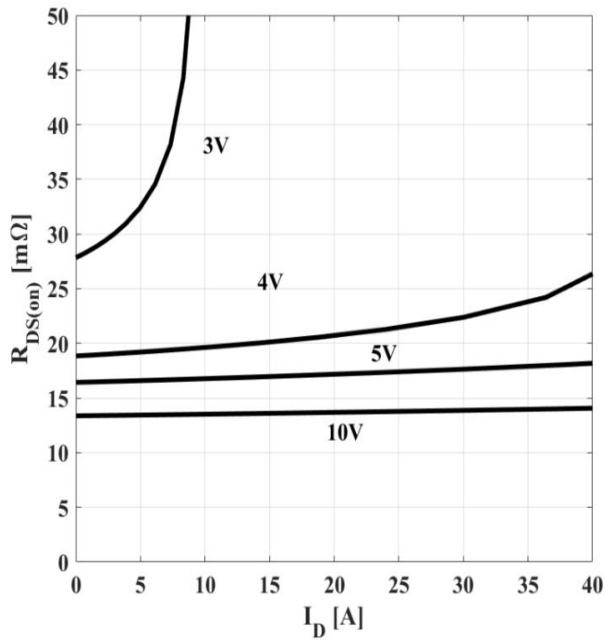
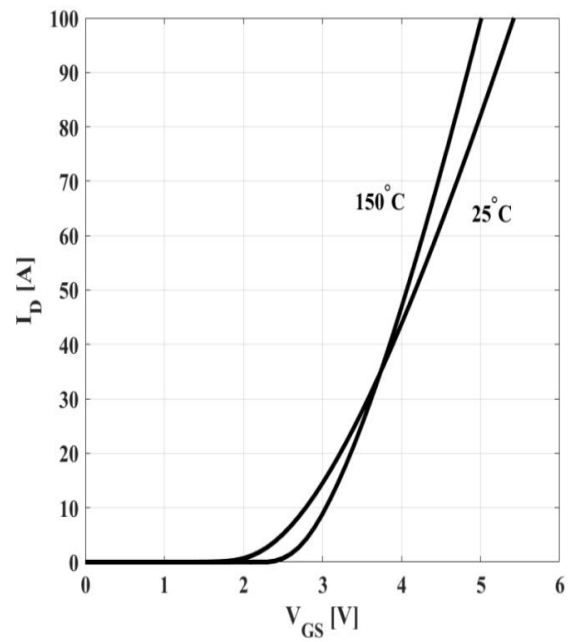
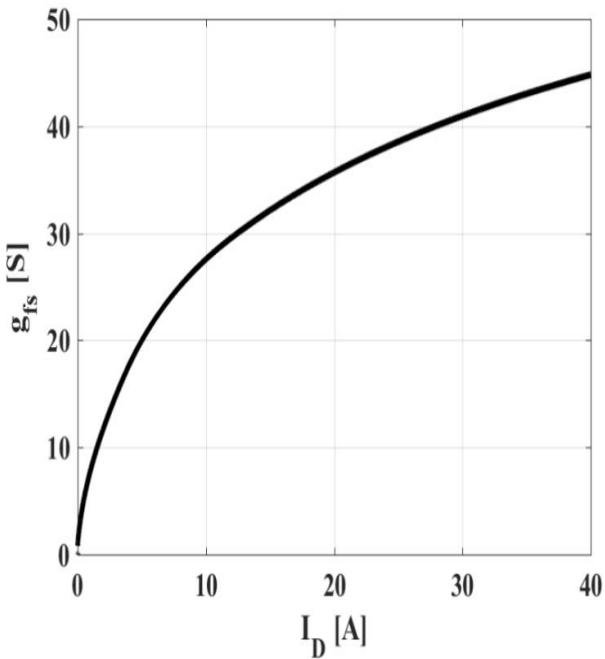
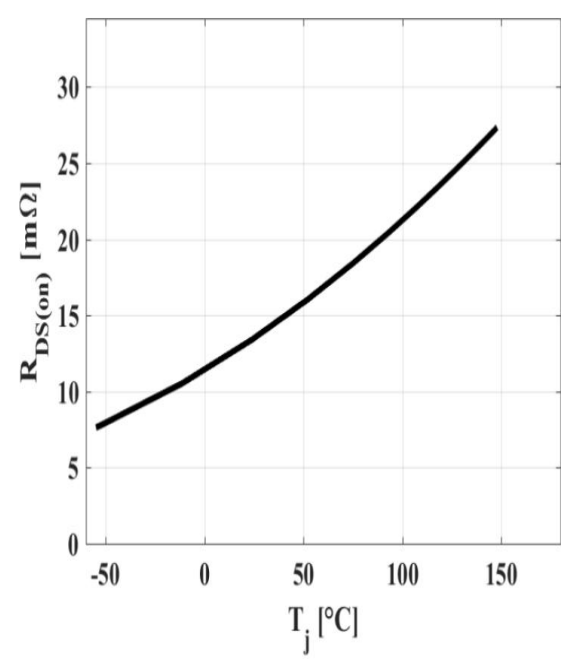
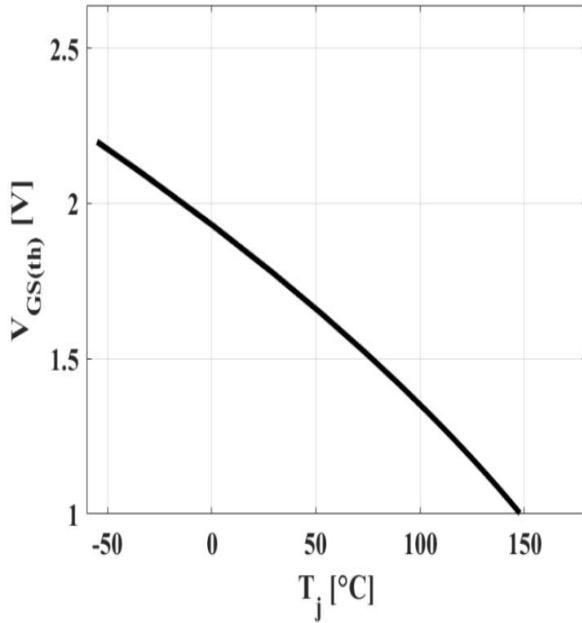
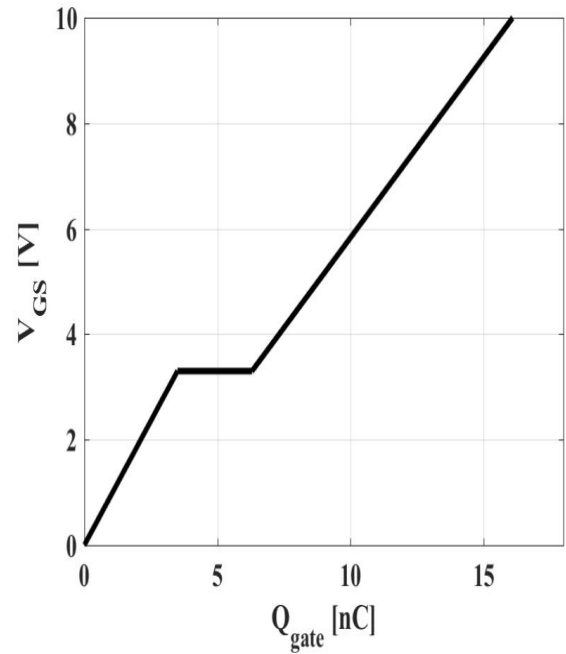
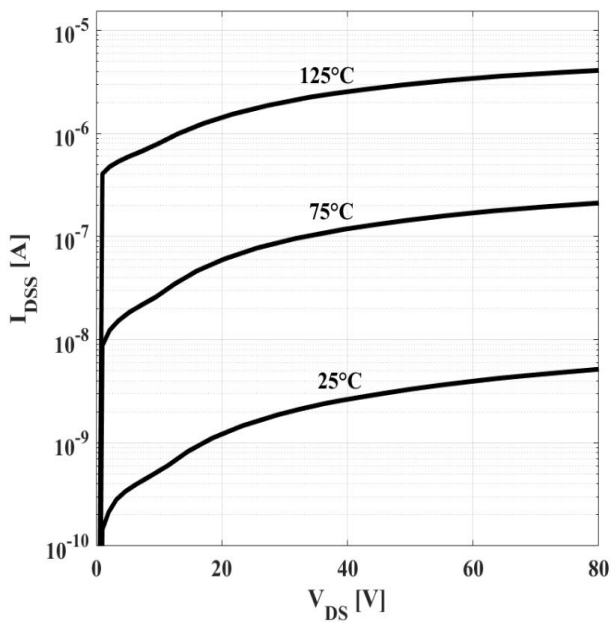
Figure7: Typ. Drain-Source On-State Resistance

 $R_{DS(ON)}=f(I_D); T_j=25^{\circ}\text{C}; \text{parameter: } V_{GS}$
Figure8: Typ. Transfer Characteristics

 $I_D=f(V_{GS}); |V_{DS}|>2|I_D|R_{DS(on)max}; \text{parameter: } T_j$
Figure9: Typ. Forward Transconductance

 $g_{fs}=f(I_D); T_j=25^{\circ}\text{C}$
Figure10: Typ. Drain-Source On-State Resistance

 $R_{DS(ON)}=f(T_j); I_D=20\text{A}; V_{GS}=10\text{V}$

Figure 11: Typ. Gate Threshold Voltage


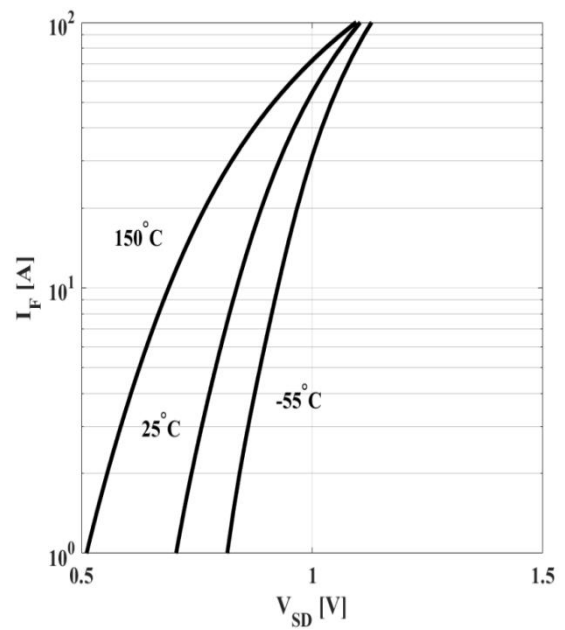
$$V_{GS(th)} = f(T_j); V_{GS} = V_{DS}; I_{DS} = 250\mu A$$

Figure 12: Typ. Gate Charge


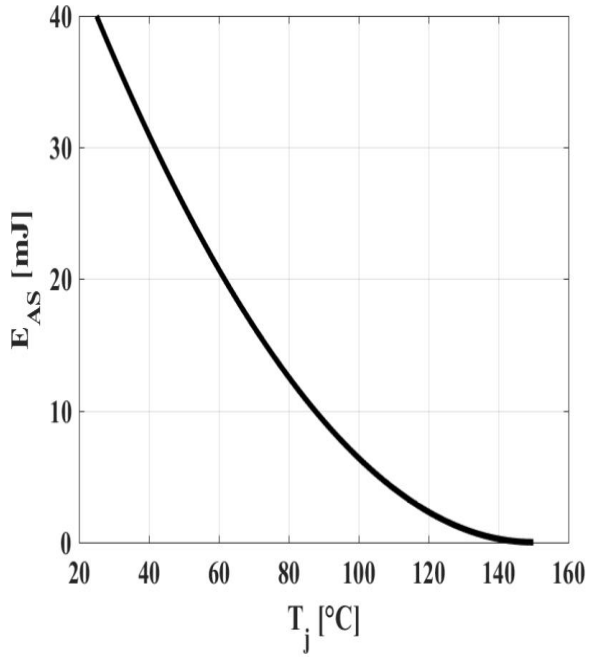
$$V_{GS} = f(Q_{gate}), I_D = 20A \text{ pulsed}$$

Figure 13: Drain-Source Leakage Current


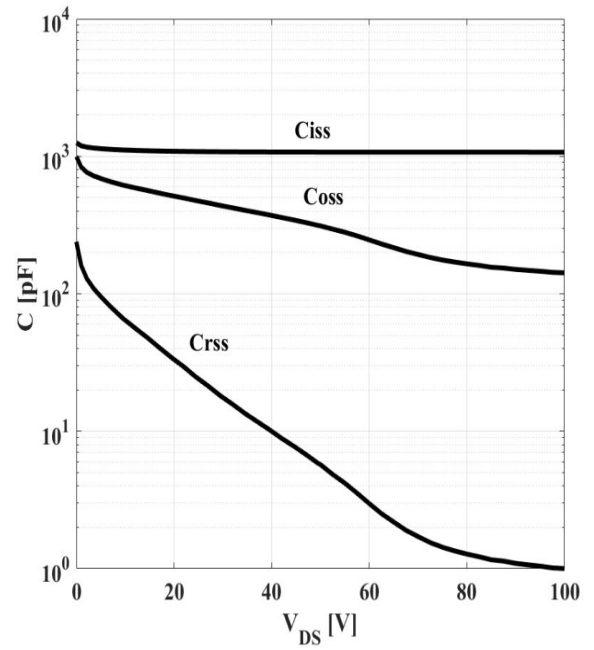
$$I_{DSS} = f(V_{DS}); V_{GS} = 0V; \text{parameter: } T_j$$

Figure 14: Forward Characteristics of Reverse Diode


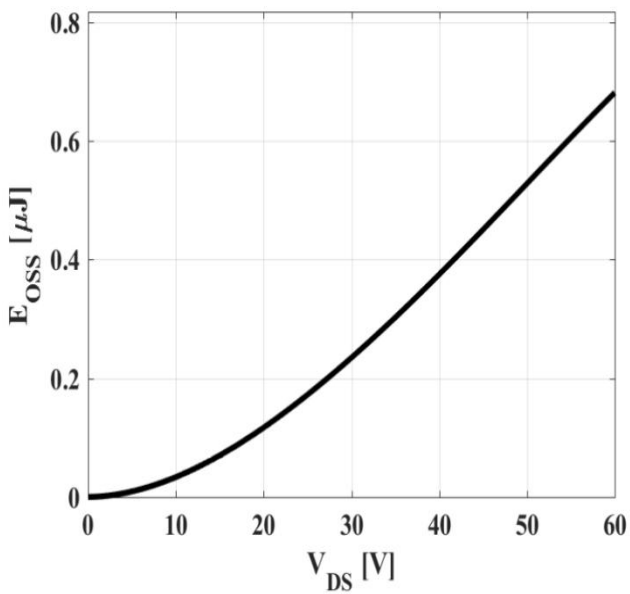
$$I_F = f(V_{SD}); \text{parameter: } T_j$$

Figure 15: Avalanche Energy


$$E_{AS}=f(T_j); I_D=15.0A; V_{DD}=50V$$

Figure 16: Typ. Capacitances


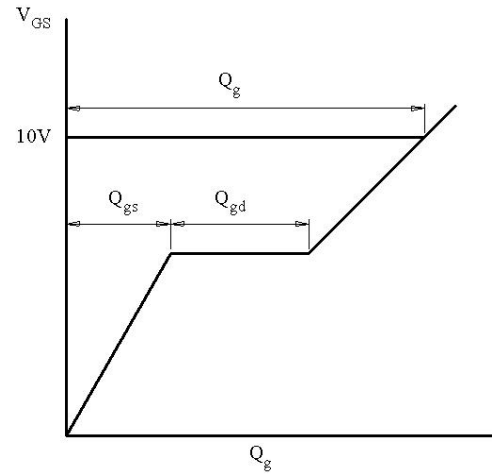
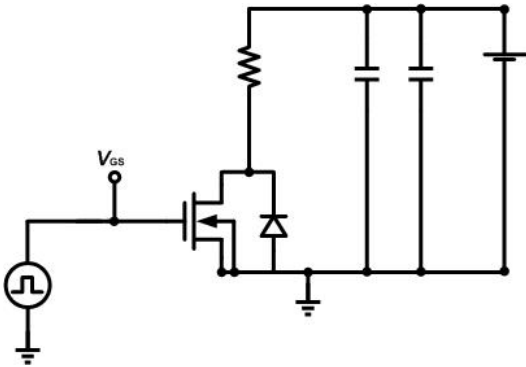
$$C=f(V_{DS}); V_{GS}=0; f=1MHz$$

Figure 17: Coss Stored Energy


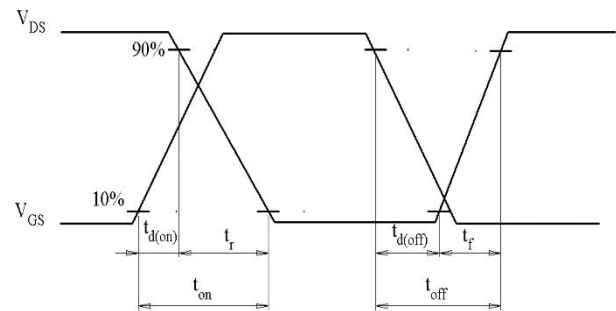
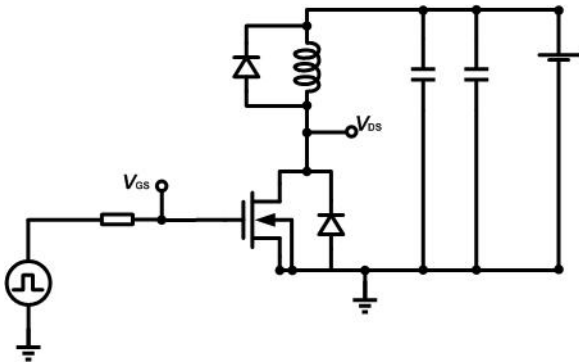
$$E_{OSS}=f(V_{DS})$$

Test Circuits

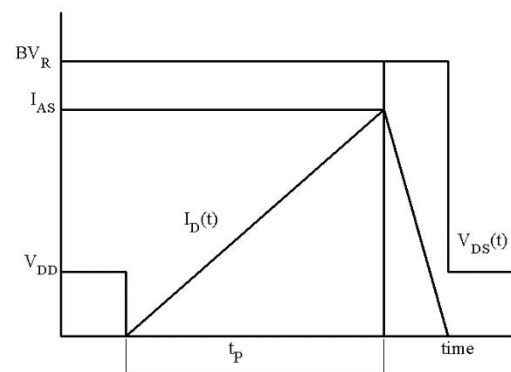
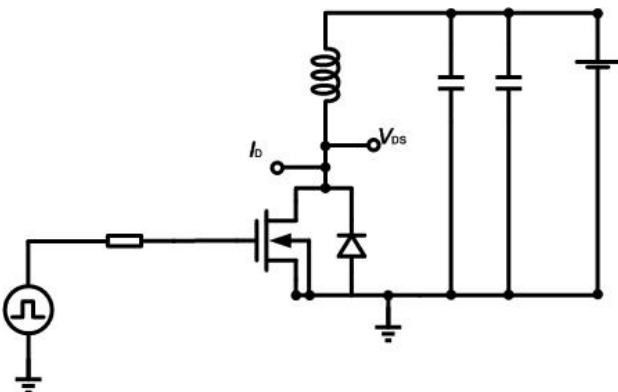
1. Gate Charge Test Circuit & Waveform



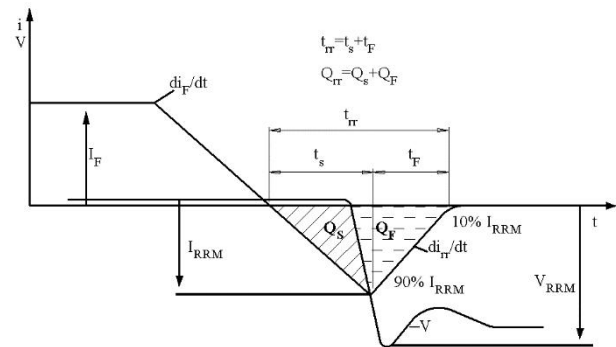
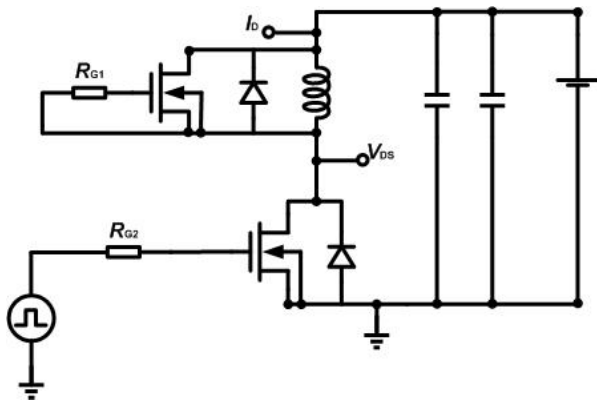
2. Switch Time Test Circuit



3. Unclamped Inductive Switching Test Circuit & Waveforms



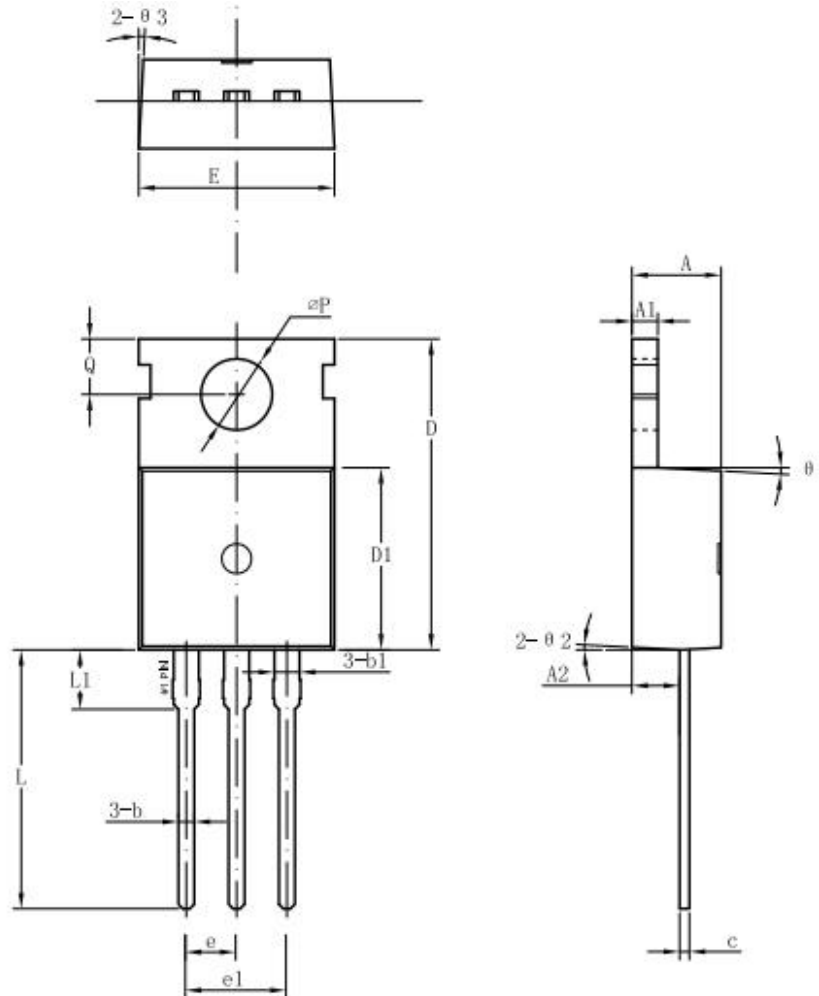
4. Test Circuit and Waveform for Diode Characteristics



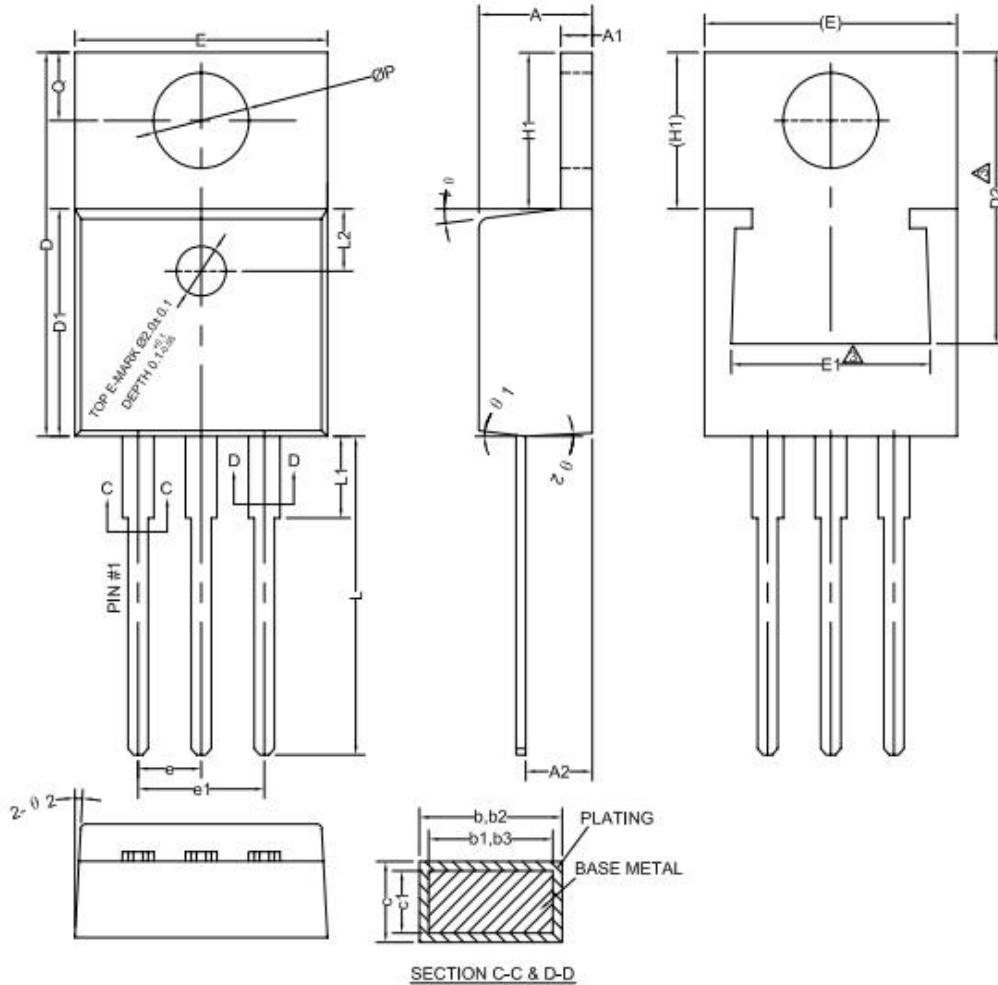
Mechanical Dimensions

TO-220C(Package1)

Unit: mm



Symbol	Dimensions (mm)			Symbol	Dimensions (mm)		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	4.30	4.50	4.70	e	—	2.54	—
A1	1.25	1.30	1.40	e1	—	5.08	—
A2	2.20	2.40	2.60	L	12.60	13.08	13.60
b	0.70	0.80	0.95	L1	—	3.00	—
b1	—	1.27	—	Φ P	3.50	3.60	3.80
c	0.40	0.50	0.65	Q	2.60	2.80	3.00
D	15.20	15.70	16.20	θ 1	—	3°	—
D1	9.00	9.20	9.40	θ 2	—	3°	—
E	9.70	10.00	10.10	θ 3	—	3°	—

Mechanical Dimensions
TO-220C(Package2)
Unit: mm


Symbol	Dimensions (mm)			Symbol	Dimensions (mm)		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	4.40	4.57	4.70	E	9.96	10.16	10.36
A1	1.22	—	1.32	E1	6.86	—	8.89
A2	2.59	2.69	2.79	e	2.44	2.54	2.64
b	0.77	—	0.90	e1	4.98	5.08	5.18
b1	0.76	0.81	0.86	H1	6.10	6.30	6.50
b2	1.23	—	1.36	L	12.70	—	13.12
b3	1.22	1.27	1.32	L1	—	—	3.90
c	0.34	—	0.47	L2	—	2.50REF	—
c1	0.33	0.38	0.43	ΦP	3.80	3.84	3.88
D	15.15	15.45	15.75	Q	2.60	—	2.90
D1	9.05	9.15	9.25	θ 1	5°	7°	9°
D2	11.40	—	12.88	θ 2	1°	3°	5°



Sanrise Technology Limited Company

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