



# **PRODUCT DATA SHEET**



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Datasheet Resource

Please note: Please check the JINGAO Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.jg-semi.cn. Please email any questions regarding the system integration to JINGAO\_questions@jgsemi.com.

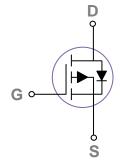


#### **General Description**

These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

#### PPAK3X3





BVDSS	RDSON	ID
-20V	$8$ m $\Omega$	-60A

#### **Features**

- -20V,-60A,  $RDS(ON) = 8m\Omega@VGS = -4.5V$
- Improved dv/dt capability
- Fast switching
- Green Device Available
- Suit for -1.8V Gate Drive Applications

## **Applications**

- Notebook
- Load Switch
- Networking
- Hand-Held Instruments

## Absolute Maximum Ratings Tc=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	-20	V
V <sub>G</sub> s	Gate-Source Voltage	±12	V
I-	Drain Current – Continuous (Tc=25°C)	-60	Α
D	Drain Current – Continuous (Tc=100°C)	-38	Α
рм	Drain Current – Pulsed <sup>1</sup>	-240	Α
D	Power Dissipation (Tc=25°C)	62.5	W
<b>D</b> D	Power Dissipation – Derate above 25°C	0.5	W/°C
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	℃

#### **Thermal Characteristics**

Symbol	ymbol Parameter		Max.	Unit
R <sub>0JA</sub>	Thermal Resistance Junction to ambient		62	°C/W
Rejc	Thermal Resistance Junction to Case		2	°C/W



## **Electrical Characteristics** (T<sub>J</sub>=25 °C, unless otherwise noted)

### **Off Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	ige V <sub>GS</sub> =0V , I <sub>D</sub> =-250uA				٧
△BV <sub>DSS</sub> /△T <sub>J</sub>	ss/△T」BV <sub>DSS</sub> Temperature Coefficient Reference to 25°C , I <sub>D</sub> =-1mA			-0.01		V/°C
IDSS	Drain Source Leakage Current	V <sub>DS</sub> =-20V , V <sub>GS</sub> =0V , T <sub>J</sub> =25°C			-1	uA
	Drain-Source Leakage Current	V <sub>DS</sub> =-16V , V <sub>GS</sub> =0V , T <sub>J</sub> =125°C			-10	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±12V , V <sub>DS</sub> =0V			±100	nA

### **On Characteristics**

ļ		V <sub>GS</sub> =-4.5V , I <sub>D</sub> =-8A		6	8	
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =-2.5V , I <sub>D</sub> =-5A		8	11	mΩ
		V <sub>GS</sub> =-1.8V , I <sub>D</sub> =-3A		11	16	
$V_{GS(th)}$	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =-250uA	-0.3	-0.6	-1.0	V
gfs	Forward Transconductance	V <sub>DS</sub> =-10V , I <sub>S</sub> =-5A		20		S

# **Dynamic and switching Characteristics**

Qg	Total Gate Charge <sup>2,3</sup>			44.4	80	
Qgs	Gate-Source Charge <sup>2, 3</sup>	V <sub>DS</sub> =-10V , V <sub>GS</sub> =-4.5V , I <sub>D</sub> =-5A		7.2	14	nC
$Q_{gd}$	Gate-Drain Charge <sup>2, 3</sup>			10.2	20	
$T_{d(on)}$	Turn-On Delay Time <sup>2, 3</sup>			13.2	26	
Tr	Rise Time <sup>2,3</sup>	$V_{DD}$ =-10 $V$ , $V_{GS}$ =-4.5 $V$ , $R_{G}$ =25 $\Omega$		68	120	nS
T <sub>d(off)</sub>	Turn-Off Delay Time <sup>2, 3</sup>	I <sub>D</sub> =-1A		160	320	113
Tf	Fall Time <sup>2, 3</sup>			154	300	
Ciss	Input Capacitance			4060	8000	
Coss	Output Capacitance	V <sub>DS</sub> =-15V , V <sub>GS</sub> =0V , F=1MHz		520	1000	pF
Crss	Reverse Transfer Capacitance			400	800	

## **Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions		Тур.	Max.	Unit
Is	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			-60	Α
I <sub>SM</sub>	Pulsed Source Current	VG=VD=UV, FOICE Current			-120	Α
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V , I <sub>S</sub> =-1A , T <sub>J</sub> =25°C			-1	V

### Note:

- 1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
- 2. The data tested by pulsed , pulse width  $\leq 300$ us , duty cycle  $\leq 2\%$ .
- 3. Essentially independent of operating temperature.



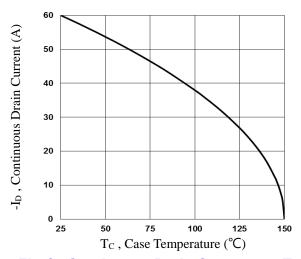


Fig.1 Continuous Drain Current vs. Tc

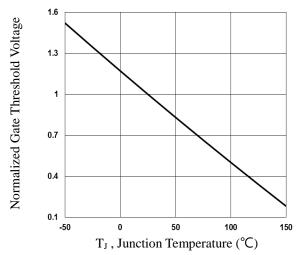


Fig.3 Normalized  $V_{th}$  vs.  $T_J$ 

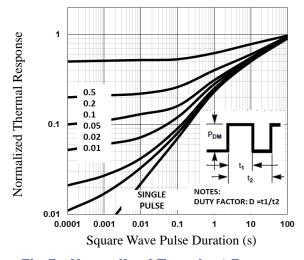


Fig.5 Normalized Transient Response

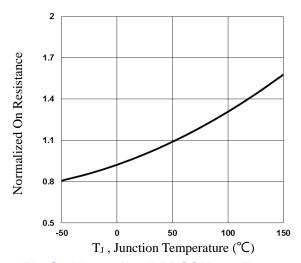


Fig.2 Normalized RDSON vs. T<sub>J</sub>

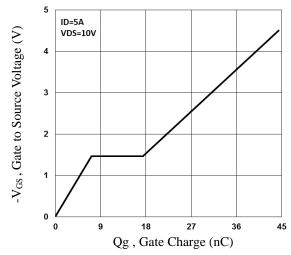


Fig.4 Gate Charge Waveform

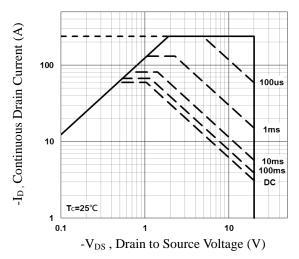
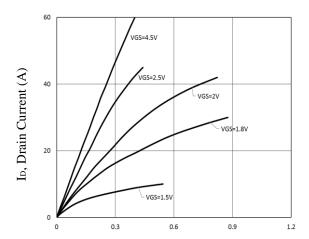


Fig.6 Maximum Safe Operation Area





VDS, Drain to Source Voltage

Fig.7 Typical Output Characteristics

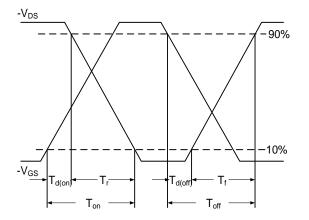
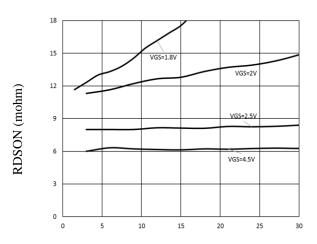


Fig.9 Switching Time Waveform



ID, Drain Current (A)

Fig.8 RDSON vs. Drain Current

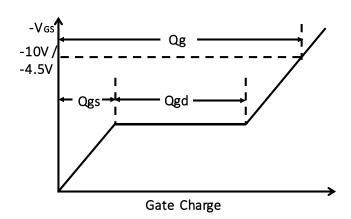
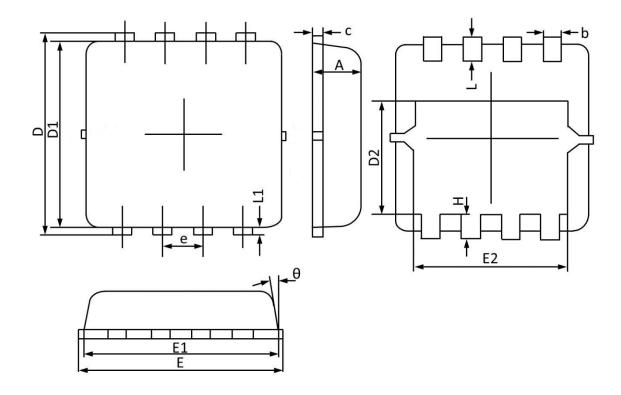


Fig.10 Gate Charge Waveform



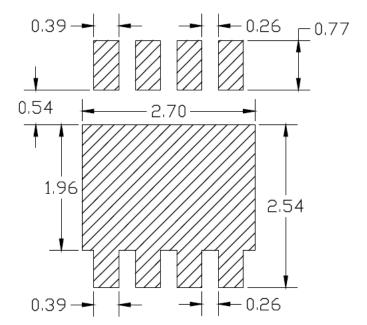
# PPAK3x3 PACKAGE INFORMATION



Symbol	Dimensions	In Millimeters	Dimension	s In Inches
Symbol	MAX	MIN	MAX	MIN
Α	0.900	0.700	0.035	0.028
b	0.350	0.250	0.014	0.010
С	0.250	0.100	0.010	0.004
D	3.500	3.050	0.138	0.120
D1	3.200	2.900	0.126	0.114
D2	1.950	1.350	0.077	0.053
E	3.400	3.000	0.134	0.118
E1	3.300	2.900	0.130	0.114
E2	2.600	2.350	0.102	0.093
е	0.65	BSC	0.02	6BSC
Н	0.750	0.300	0.030	0.012
L	0.600	0.300	0.024	0.012
L1	0.200	0.060	0.008	0.002
θ	14°	6°	14°	6°



# PPAK3X3 RECOMMENDED LAND PATTERN



unit: mm



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