

## Description

The SX250N06GF uses advanced technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 10V. This device is suitable for use as a Battery protection or in other Switching application.

## General Features

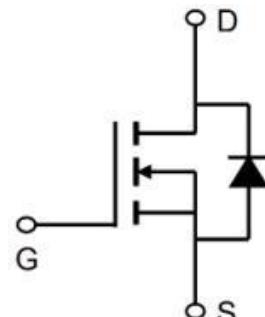
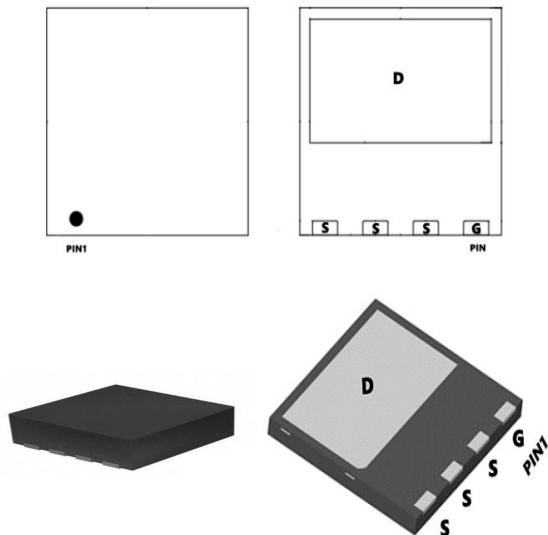
$V_{DS} = 60V$   $I_D = 250A$

$R_{DS(ON)} < 2.7m\Omega$  @  $V_{GS}=10V$

## Application

Battery protection

UPS



## Absolute Maximum Ratings ( $T_c=25^\circ C$ unless otherwise noted)

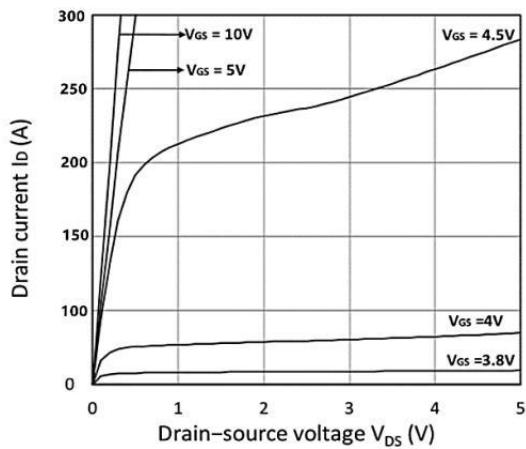
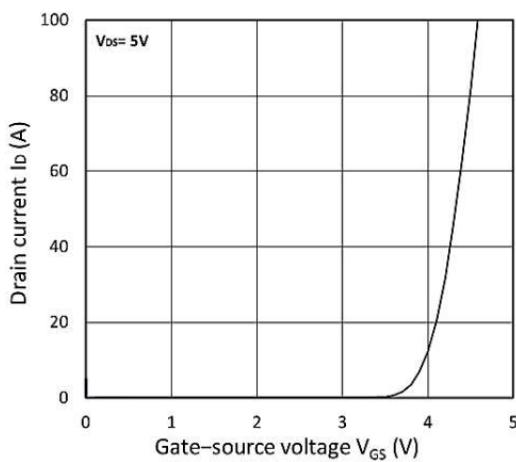
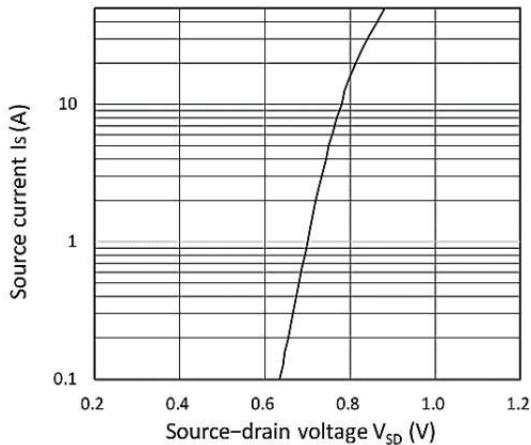
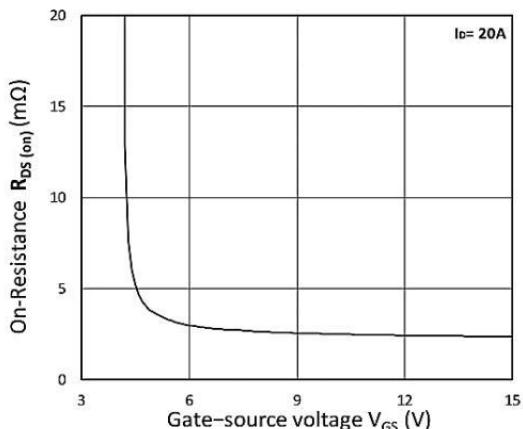
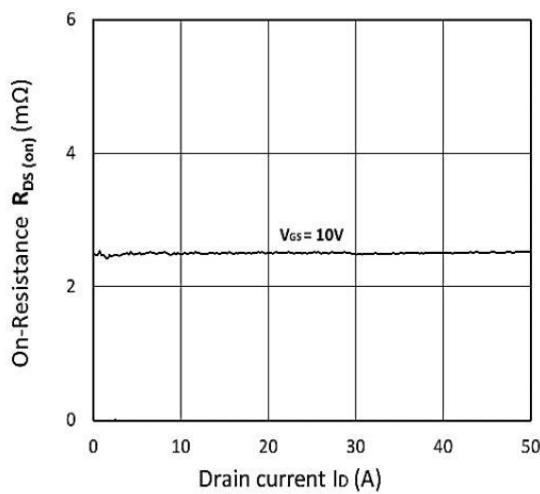
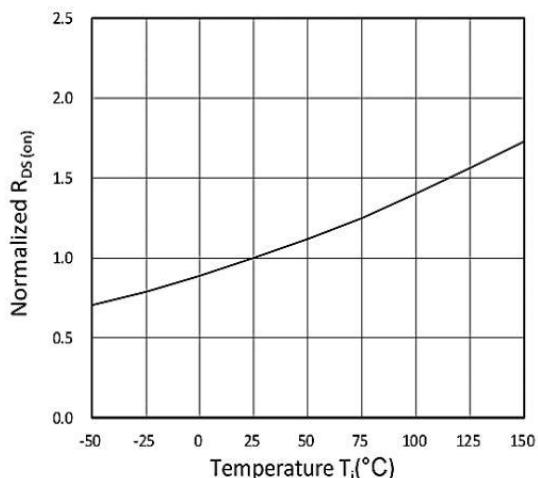
Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	60	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D @ T_c=25^\circ C$	Continuous Drain Current <sup>1,6</sup>	250	A
$I_D @ T_c=100^\circ C$	Continuous Drain Current <sup>1,6</sup>	140	A
$IDM$	Pulsed Drain Current <sup>2</sup>	240	A
EAS	Single Pulse Avalanche Energy <sup>3</sup>	585	mJ
IAS	Avalanche Current	55	A
$P_D @ T_c=25^\circ C$	Total Power Dissipation <sup>4</sup>	168	W
TSTG	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C
$R_{eJA}$	Thermal Resistance Junction-Ambient <sup>1</sup>	12	°C/W
$R_{eJC}$	Thermal Resistance Junction-Case <sup>1</sup>	1.5	°C/W

**Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise noted)**

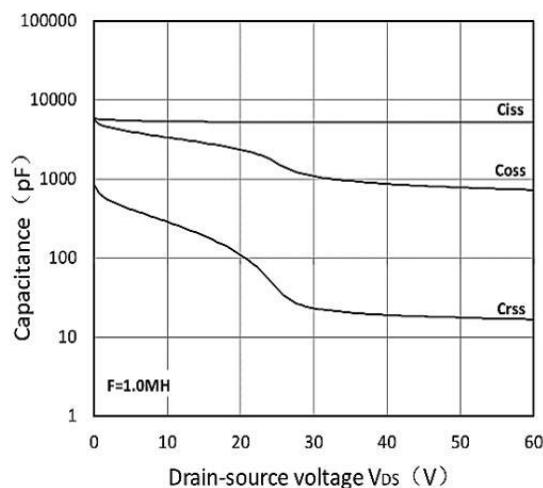
Symbol	Parameter	Test Conditions	Min	Type	Max	Unit
V(BR)DSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA	60	66	-	V
IGSS	Gate-body Leakage Current	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±20V	-	-	±100	nA
IDSS T <sub>J</sub> =25°C	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 60V, V <sub>GS</sub> = 0V	-	-	1	μA
IDSS T <sub>J</sub> =100°C	Zero Gate Voltage Drain Current		-	-	100	
V <sub>GS(th)</sub>	Gate-Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	2.0	2.8	4.0	V
RDS(on)	Drain-Source On-Resistance <sup>4</sup>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 20A	-	1.9	2.7	mΩ
g <sub>fs</sub>	Forward Transconductance <sup>4</sup>	V <sub>DS</sub> = 5V, I <sub>D</sub> = 20A	-	78	-	S
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V, f = 1MHz	-	5245	-	pF
C <sub>oss</sub>	Output Capacitance		-	1090	-	
C <sub>rss</sub>	Reverse Transfer Capacitance		-	25	-	
R <sub>G</sub>	Gate Resistance	f = 1MHz	-	2.2	-	Ω
Q <sub>g</sub>	Total Gate Charge	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 30V, I <sub>D</sub> = 20A	-	72.5	-	nC
Q <sub>gs</sub>	Gate-Source Charge		-	19.5	-	
Q <sub>gd</sub>	Gate-Drain Charge		-	14	-	
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>GS</sub> = 10V, V <sub>DD</sub> = 30V, R <sub>G</sub> = 3Ω, I <sub>D</sub> = 20A	-	26.5	-	ns
t <sub>r</sub>	Rise Time		-	15	-	
t <sub>d(off)</sub>	Turn-off Delay Time		-	73	-	
t <sub>f</sub>	Fall Time		-	18	-	
t <sub>rr</sub>	Body Diode Reverse Recovery Time	I <sub>F</sub> = 20A, dI/dt = 100A/μs	-	25	-	ns
Q <sub>rr</sub>	Body Diode Reverse Recovery Charge		-	90	-	nC
V <sub>SD</sub>	Diode Forward Voltage <sup>4</sup>	I <sub>S</sub> = 20A, V <sub>GS</sub> = 0V	-	-	1.2	V
I <sub>S</sub>	Continuous Source Current T <sub>c</sub> =25°C		-	-	125	A

**Note :**

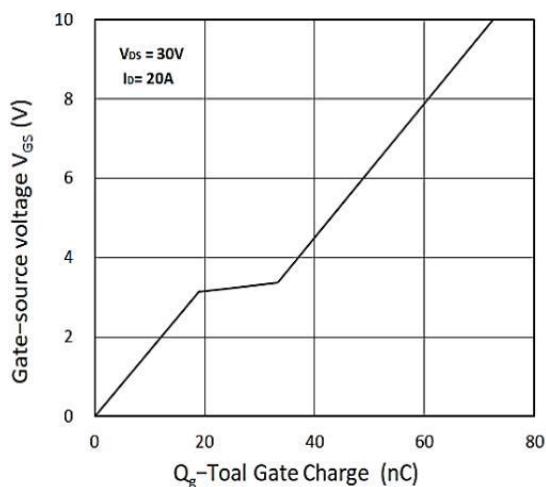
- 1、The data tested by surface mounted on a 1 inch 2 FR-4 board with 2OZ copper.
- 2、The data tested by pulsed , pulse width .The EAS data shows Max. rating .
- 3、The power dissipation is limited by 175°C junction temperature
- 4、EAS condition: TJ=25°C , VDD=48V, VG=10V, RG=25Ω, L=0.1mH, IAS= 55A
- 5、The data is theoretically the same as ID and IDM , in real applications , should be limited by total power dissipation.

**Typical Characteristics****Figure 1. Output Characteristics****Figure 2. Transfer Characteristics****Figure 3. Forward Characteristics of Reverse****Figure 4. RDS(ON) vs. VGS****Figure 5. R DS(ON) vs. ID****Figure 6. Normalized R DS(ON) vs. Temperature**

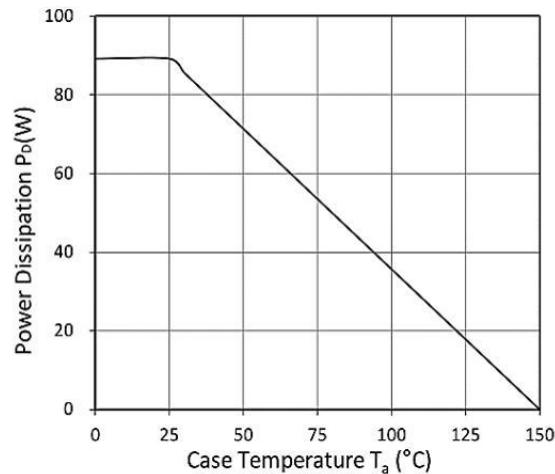
## Typical Characteristics



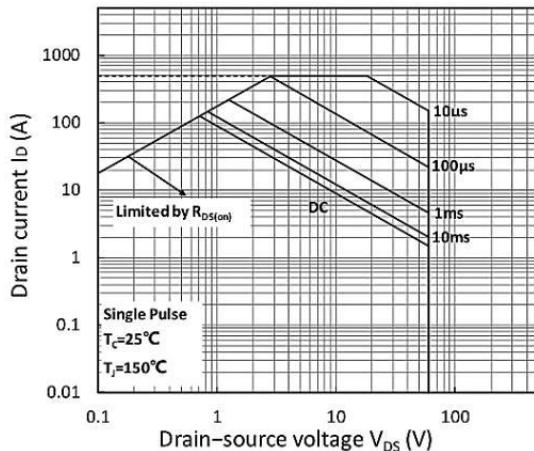
**Figure 7. Capacitance Characteristics**



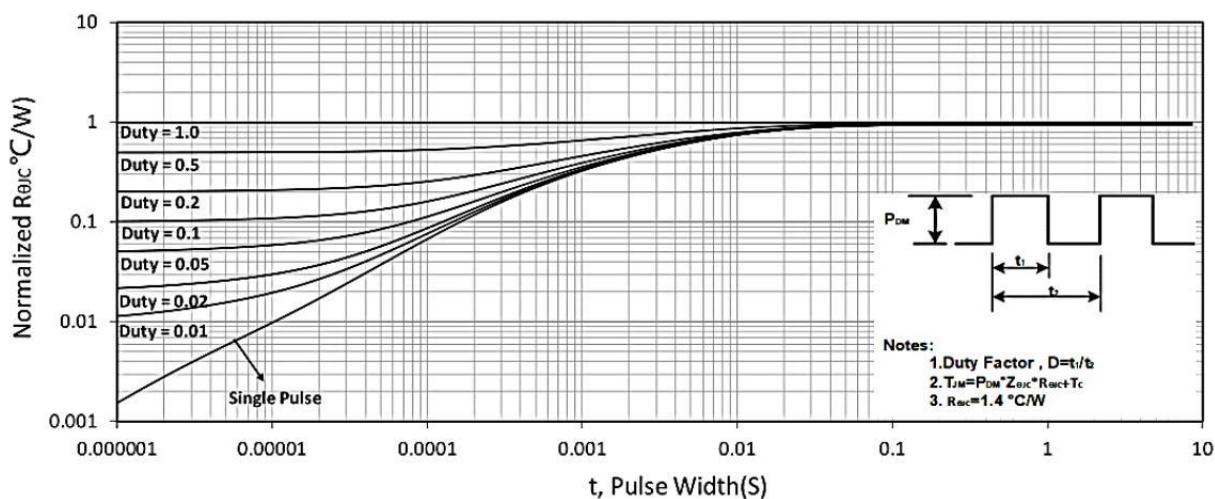
**Figure 8. Gate Charge Characteristics**



**Figure 9. Power Dissipation**

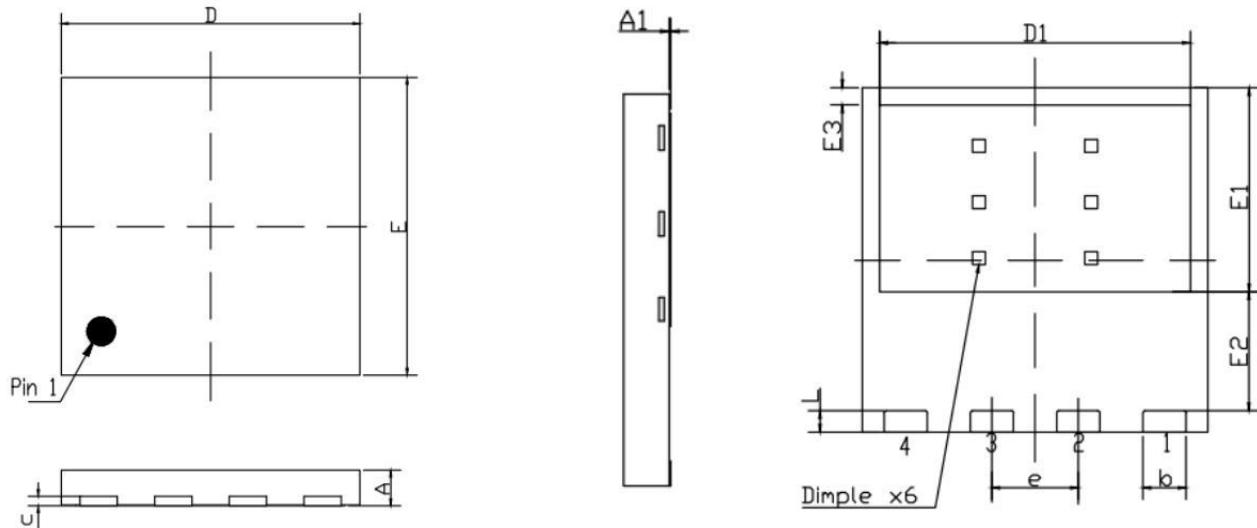


**Figure 10. Safe Operating Area**



**Figure 11. Normalized Maximum Transient Thermal Impedance**

## Package Mechanical Data-DFN8\*8-4L



Symbols	Dimension in mm			Dimension in inches		
	Min	Nom	Max	Min	Nom	Max
A	0.800	0.850	1.10	0.031	0.033	0.043
A1	---	---	0.050	----	----	0.002
D	7.900	8.000	8.100	0.311	0.315	0.319
D1	7.10	7.20	7.30	0.279	0.283	0.287
E	7.900	8.000	8.100	0.311	0.315	0.319
E1	4.65	4.75	4.85	0.183	0.187	0.191
E2	2.65	2.75	2.85	0.104	0.108	0.112
E3	0.30	0.40	0.50	0.012	0.016	0.020
b	0.900	1.000	1.10	0.035	0.039	0.043
e	2.00 BSC			0.079 BSC		
c	0.15	0.25	0.35	0.006	0.010	0.014
L	0.40	0.50	0.60	0.016	0.020	0.024

### Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
TAPING	DFN8X8-4L		5000