

isc Silicon PNP Power Transistor

2N6377

DESCRIPTION

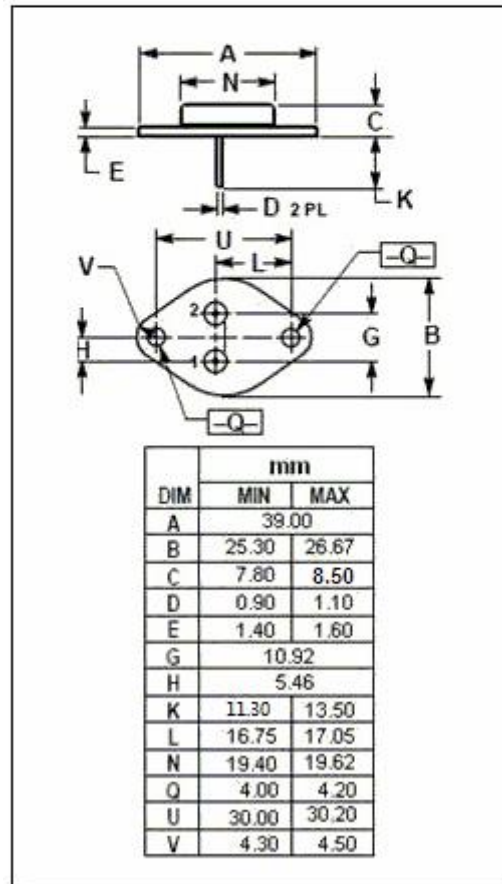
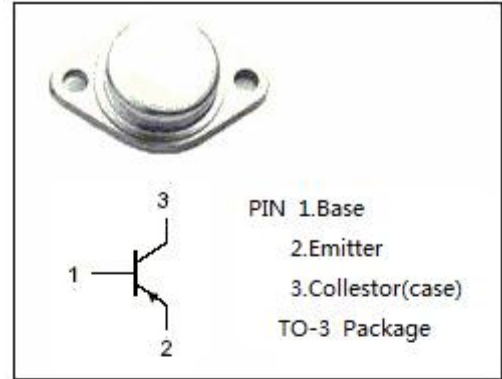
- Collector-Emitter Breakdown Voltage-
: $V_{CEO} = -80V$ (Min)
- Minimum Lot-to-Lot variations for robust device
Performance and reliable operation

APPLICATIONS

- Power amplifier and switching applications

ABSOLUTE MAXIMUM RATINGS($T_a = 25^\circ C$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	-100	V
V_{CEO}	Collector-Emitter Voltage	-80	V
V_{EBO}	Emitter-Base Voltage	-6	V
I_C	Collector Current-Continuous	-50	A
P_D	Collector Power Dissipation @ $T_C = 25^\circ C$	250	W
T_J	Junction Temperature	-65~200	$^\circ C$
T_{stg}	Storage Temperature Range	-65~200	$^\circ C$



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ELECTRICAL CHARACTERISTICS

 $T_C=25^{\circ}\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
$V_{(BR)CEO}$	Collector-Emitter Breakdown Voltage	$I_C=-50\text{mA}$	-80			V
I_{EBO}	Emitter-Base Cutoff Current	$V_{BE}=-6\text{V}$			-100	μA
I_{CEO}	Collector-Emitter Cutoff Current	$V_{CE}=-50\text{V}$			-0.05	mA
$V_{CE(sat)-1}$	Collector-Emitter Saturation Voltage	$I_C=-20\text{A}; I_B=-2\text{A}$			-1.2	V
$V_{CE(sat)-2}$	Collector-Emitter Saturation Voltage	$I_C=-50\text{A}; I_B=-10\text{A}$			-3	V
$V_{BE(sat)-1}$	Base-Emitter Saturation Voltage	$I_C=-20\text{A}; I_B=-2\text{A}$			-1.8	V
$V_{BE(sat)-2}$	Base-Emitter Saturation Voltage	$I_C=-50\text{A}; I_B=-10\text{A}$			-3.5	V
h_{FE-1}	DC Current Gain	$I_C=-1\text{A}; V_{CE}=-4\text{V}$	50			
h_{FE-2}	DC Current Gain	$I_C=-20\text{A}; V_{CE}=-4\text{V}$	30		120	
h_{FE-3}	DC Current Gain	$I_C=-50\text{A}; V_{CE}=-4\text{V}$	10			

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