

isc Silicon PNP Power Transistor

2N6422

DESCRIPTION

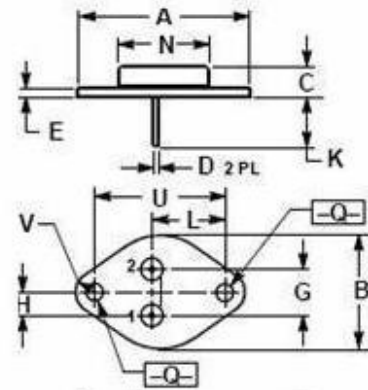
- Collector-Emitter Breakdown Voltage-
: $V_{CEO} = -300V(\text{Min})$
- Minimum Lot-to-Lot variations for robust device
Performance and reliable operation

APPLICATIONS

- Power amplifier and switching applications

ABSOLUTE MAXIMUM RATINGS($T_a = 25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	-500	V
V_{CEO}	Collector-Emitter Voltage	-300	V
V_{EBO}	Emitter-Base Voltage	-6	V
I_C	Collector Current-Continuous	-2	A
P_D	Collector Power Dissipation @ $T_C = 25^\circ\text{C}$	35	W
T_J	Junction Temperature	-65~200	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-65~200	$^\circ\text{C}$



DIM	mm	
	MIN	MAX
A	31.40	31.80
B	17.30	17.90
C	6.70	7.10
D	0.70	0.90
E	1.40	1.80
G	5.08	
H	2.54	
K	9.80	10.50
L	14.70	14.90
N	12.40	12.70
Q	3.60	3.80
U	24.30	24.50
V	3.50	3.70

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ELECTRICAL CHARACTERISTICS

 $T_C=25^{\circ}\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
$V_{(BR)CEO}$	Collector-Emitter Breakdown Voltage	$I_C=-50\text{mA}$	-300			V
I_{EBO}	Emitter -Base Cutoff Current	$V_{BE}=-6\text{V}$			-500	μA
I_{CEO}	Collector-Emitter Cutoff Current	$V_{CB}=-150\text{V}$			-5	mA
$V_{CE(sat)-1}$	Collector-Emitter Saturation Voltage	$I_C=-0.75\text{A}; I_B=-0.075\text{A}$			-0.75	V
$V_{CE(sat)-2}$	Collector-Emitter Saturation Voltage	$I_C=-1\text{A}; I_B=-0.125\text{A}$			-0.75	V
$V_{BE(sat)-1}$	Base-Emitter Saturation Voltage	$I_C=-0.75\text{A}; I_B=-0.075\text{A}$			-1.4	V
$V_{BE(sat)-2}$	Base-Emitter Saturation Voltage	$I_C=-1\text{A}; I_B=-0.125\text{A}$			-1.4	V
h_{FE-1}	DC Current Gain	$I_C=-1\text{A}; V_{CE}=-2\text{V}$	8		80	
h_{FE-2}	DC Current Gain	$I_C=-1\text{A}; V_{CE}=-10\text{V}$	25		100	

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