

isc Silicon PNP Power Transistor

2SB1007

DESCRIPTION

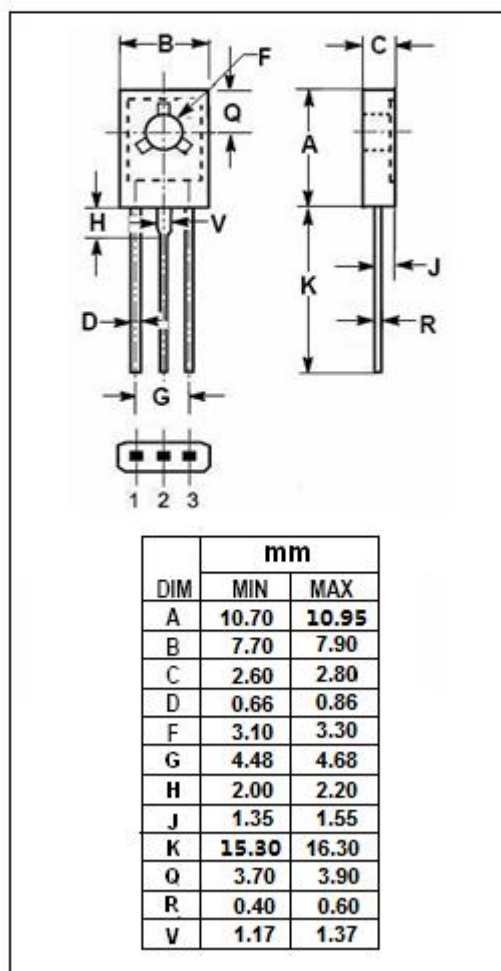
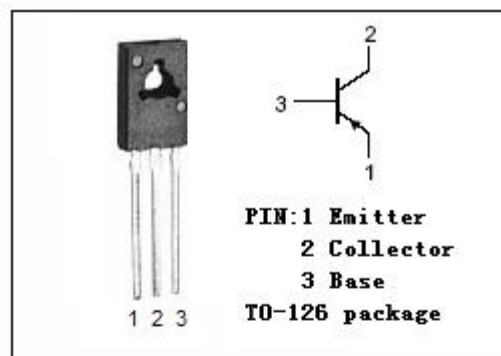
- Collector-Emitter Breakdown Voltage-
: $V_{(BR)CEO} = -80V(\text{Min})$
- Good Linearity of h_{FE}
- Complement to Type 2SD1378
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for low frequency power amplifier applications.

ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	-80	V
V_{CEO}	Collector-Emitter Voltage	-80	V
V_{EBO}	Emitter-Base Voltage	-5	V
I_C	Collector Current-Continuous	-0.7	A
P_C	Collector Power Dissipation @ $T_C=25^\circ\text{C}$	10	W
	Collector Power Dissipation @ $T_a=25^\circ\text{C}$	1	
T_J	Junction Temperature	150	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-55~150	$^\circ\text{C}$



isc Silicon PNP Power Transistor**2SB1007****ELECTRICAL CHARACTERISTICS****T_C=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CBO}	Collector-Base Breakdown Voltage	I _C = -50 μA; I _E = 0	-80			V
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = -2mA; I _B = 0	-80			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = -50 μA; I _C =0	-5			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = -500mA; I _B = -50mA			-0.4	V
I _{CBO}	Collector Cutoff Current	V _{CB} = -50V; I _E = 0			-0.5	μA
I _{EBO}	Emitter Cutoff Current	V _{EB} = -4V; I _C = 0			-0.5	μA
h _{FE}	DC Current Gain	I _C = -100mA; V _{CE} = -3V	82		390	

◆ h_{FE} Classifications

P	Q	R
82-180	120-270	180-390

NOTICE:

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