

isc Silicon PNP Power Transistor

2SB1096

DESCRIPTION

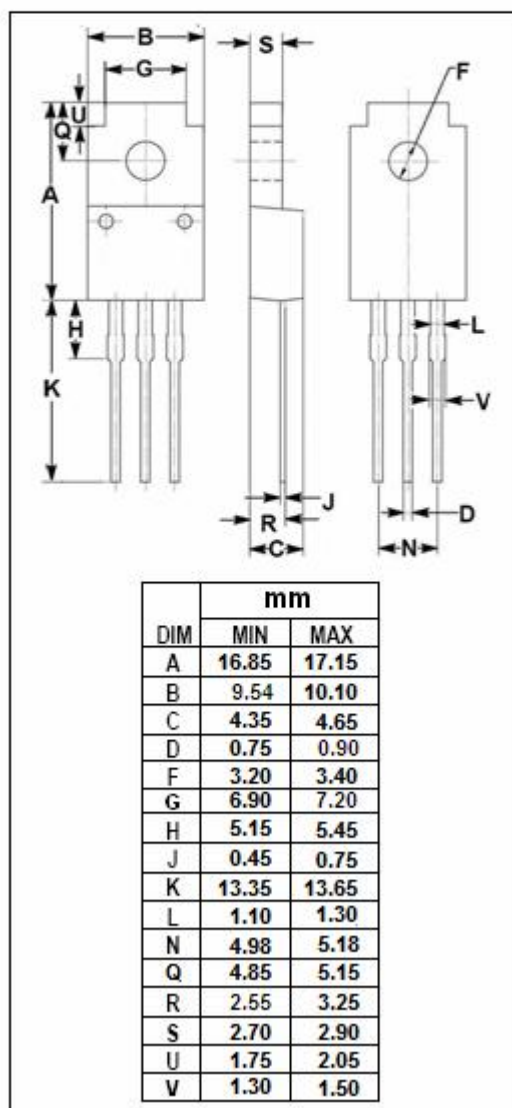
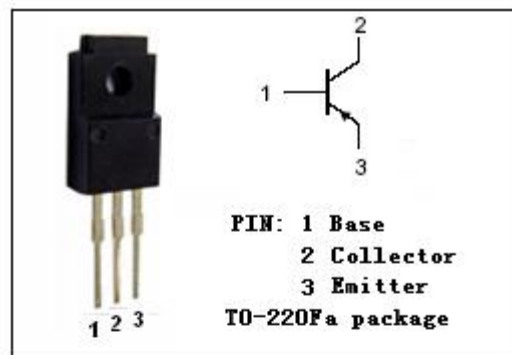
- High Collector Current: $I_C = -2A$
- Good Linearity of h_{FE}
- Complement to Type 2SD1587
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for color TV vertical deflection output applications.

ABSOLUTE MAXIMUM RATINGS($T_a=25^{\circ}C$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	-200	V
V_{CEO}	Collector-Emitter Voltage	-150	V
V_{EBO}	Emitter-Base Voltage	-5	V
I_C	Collector Current-Continuous	-2	A
I_{CM}	Collector Current-Pulse	-3	A
I_B	Base Current-Continuous	-1.0	A
P_C	Total Power Dissipation @ $T_a=25^{\circ}C$	2	W
	Total Power Dissipation @ $T_C=25^{\circ}C$	25	
T_J	Junction Temperature	150	$^{\circ}C$
T_{stg}	Storage Temperature Range	-55~150	$^{\circ}C$



isc Silicon PNP Power Transistor**2SB1096****ELECTRICAL CHARACTERISTICS****T_C=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = -0.5A; I _B = -50mA			-1.0	V
I _{CBO}	Collector Cutoff Current	V _{CB} = -150V; I _E = 0			-50	μ A
I _{EBO}	Emitter Cutoff Current	V _{EB} = -4V; I _C = 0			-50	μ A
h _{FE}	DC Current Gain	I _C = -0.4A; V _{CE} = -10V	40		200	
f _T	Current-Gain—Bandwidth Product	I _C = -0.4A; V _{CE} = -10V		5		MHz

◆ h_{FE} Classifications

M	L	K
40-80	60-120	100-200

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