

isc Silicon NPN Power Transistor

2SC1172

DESCRIPTION

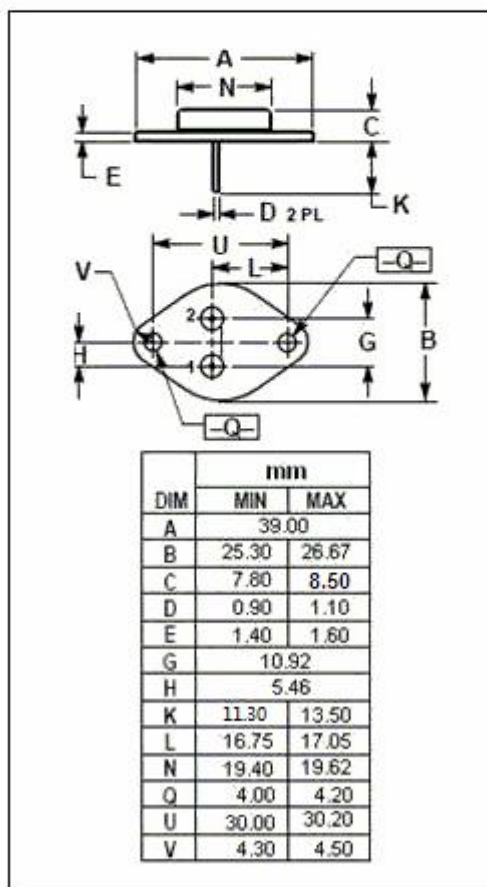
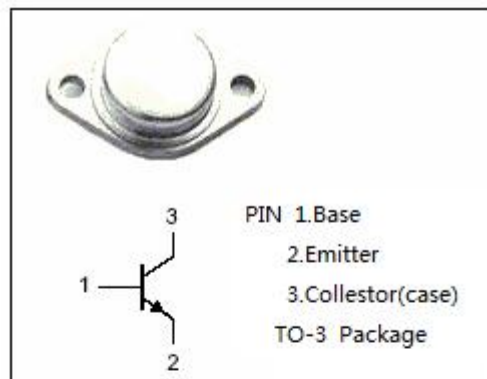
- With TO-3 Package
- High voltage
- Wide area of safe operation
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- For power amplifier applications

ABSOLUTE MAXIMUM RATINGS($T_a=25^{\circ}\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	1500	V
V_{CEO}	Collector-Emitter Voltage	600	V
V_{EBO}	Emitter-Base Voltage	6	V
I_C	Collector Current-Continuous	5	A
P_C	Collector Power Dissipation	50	W
T_J	Junction Temperature	150	$^{\circ}\text{C}$
T_{stg}	Storage Temperature Range	-55~150	$^{\circ}\text{C}$



isc Silicon NPN Power Transistor**2SC1172****ELECTRICAL CHARACTERISTICS****T_c=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C =4.0A; I _B = 1.0A			5.0	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C =4.0A; I _B = 1.0A			1.5	V
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 25mA; I _B = 0	600			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = 1mA; I _C = 0	6			V
h _{FE}	DC Current Gain	I _C =2A; V _{CE} = 10V	10			
f _T	Current-Gain—Bandwidth Product	I _C = 0.5A; V _{CE} = 10V; f _{test} = 1MHz		3		MHz

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