

isc Silicon NPN Power Transistor**2SC1212****DESCRIPTION**

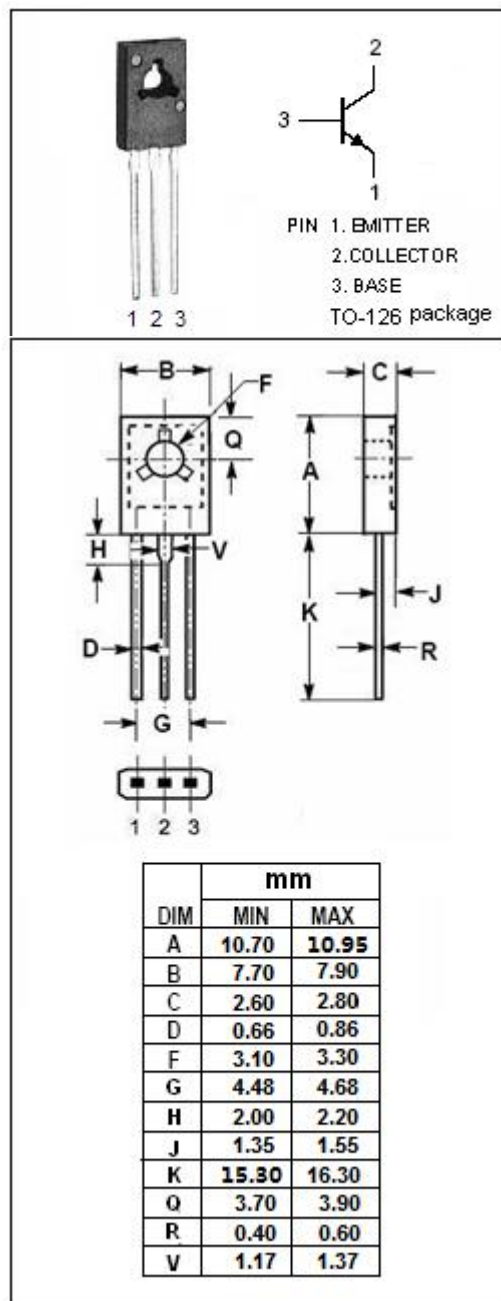
- High Collector Current $I_C = 1A$
- Collector-Emitter Breakdown Voltage-
: $V_{(BR)CEO} = 50V(\text{Min})$
- Good Linearity of h_{FE}
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for low frequency power amplifier applications.

ABSOLUTE MAXIMUM RATINGS($T_a = 25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	50	V
V_{CEO}	Collector-Emitter Voltage	50	V
V_{EBO}	Emitter-Base Voltage	4	V
I_C	Collector Current-Continuous	1	A
P_C	Collector Power Dissipation @ $T_C = 25^\circ\text{C}$	8	W
	Collector Power Dissipation @ $T_a = 25^\circ\text{C}$	0.75	
T_J	Junction Temperature	150	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-55~150	$^\circ\text{C}$



isc Silicon NPN Power Transistor**2SC1212****ELECTRICAL CHARACTERISTICS****T_C=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CBO}	Collector-Base Breakdown Voltage	I _C = 1mA ; I _E = 0	50			V
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 10mA ; R _{BE} = ∞	50			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = 1mA ; I _C = 0	4			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 1A ; I _B = 0.1A			1.5	V
V _{BE(on)}	Base-Emitter On Voltage	I _C = 50mA ; V _{CE} = 4V			1.0	V
I _{CBO}	Collector Cutoff Current	V _{CB} = 50V ; I _E = 0			5	μ A
h _{FE-1}	DC Current Gain	I _C = 50mA ; V _{CE} = 4V	60		200	
h _{FE-2}	DC Current Gain	I _C = 1A ; V _{CE} = 4V	20			
f _T	Current-Gain—Bandwidth Product	I _C = 30mA ; V _{CE} = 4V		160		MHz

◆ h_{FE-1} Classifications

B	C
60-120	100-200

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