

isc Silicon NPN Power Transistor

2SC3540

DESCRIPTION

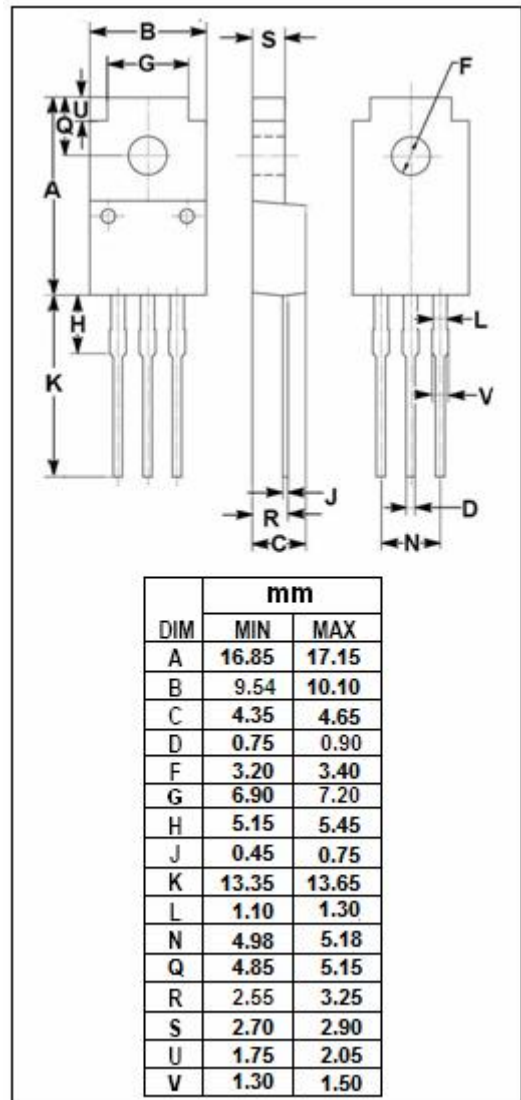
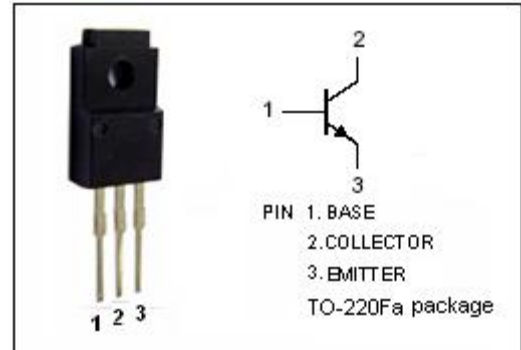
- Low Collector Saturation Voltage
: $V_{CE(sat)} = 0.4V(\text{Max}) @ I_C = 3A$
- Complement to Type 2SA1388
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for high current switching applications

ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	100	V
V_{CEO}	Collector-Emitter Voltage	80	V
V_{EBO}	Emitter-Base Voltage	7	V
I_C	Collector Current-Continuous	5	A
I_{CM}	Collector Current-Peak	8	A
I_B	Base Current-Continuous	1	A
P_C	Collector Power Dissipation @ $T_c=25^\circ\text{C}$	25	W
	Collector Power Dissipation @ $T_a=25^\circ\text{C}$	2	
T_J	Junction Temperature	150	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-55~150	$^\circ\text{C}$



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ELECTRICAL CHARACTERISTICS

T_c=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 10mA; I _B = 0	80			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 3A; I _B = 0.15A			0.4	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 3A; I _B = 0.15A			1.2	V
I _{CBO}	Collector Cutoff Current	V _{CB} = 100V; I _E = 0			1	μA
I _{EBO}	Emitter Cutoff Current	V _{EB} = 7V; I _C = 0			1	μA
h _{FE-2}	DC Current Gain	I _C = 1A; V _{CE} = 1V	70		240	
h _{FE-3}	DC Current Gain	I _C = 3A; V _{CE} = 1V	40			
f _T	Current-Gain—Bandwidth Product	I _C = 1A; V _{CE} = 4V		120		MHz
C _{OB}	Output Capacitance	I _E = 0; V _{CB} = 10V; f _{test} = 1.0MHz		80		pF

Switching times

t _{on}	Turn-on Time	I _{B1} = -I _{B2} = 0.15A; R _L = 10 Ω; V _{CC} ≈30V		0.2		μs
t _{stg}	Storage Time			1.0		μs
t _f	Fall Time			0.1		μs

◆ h_{FE-1} classifications

O	Y
70-140	120-240

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