

isc Silicon NPN Power Transistor

2SC3569

DESCRIPTION

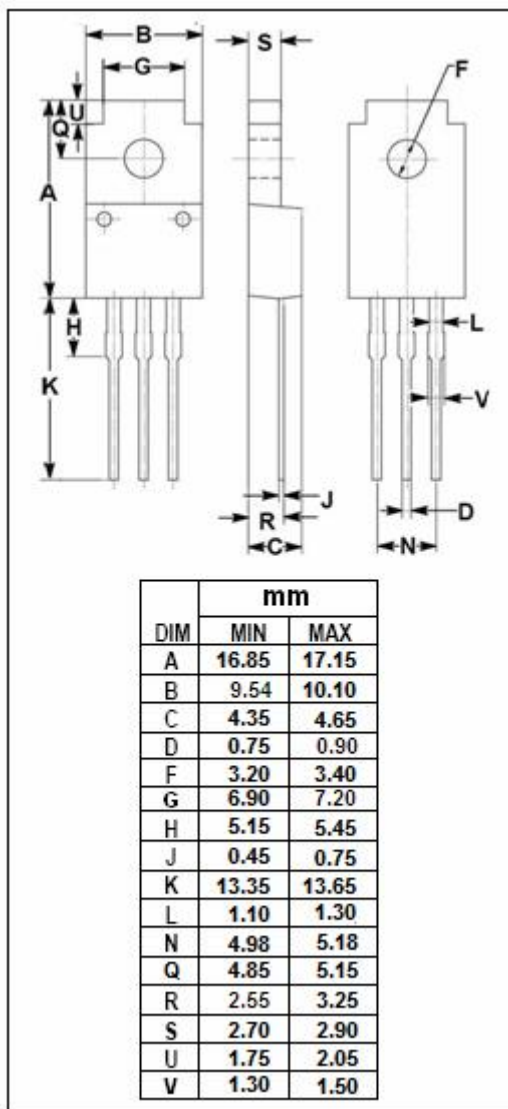
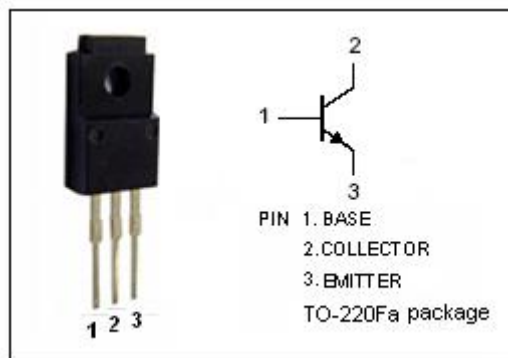
- Low Collector Saturation Voltage
- High switching speed
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Especially suited for high voltage, high speed and high power switching applications

ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	500	V
V_{CEO}	Collector-Emitter Voltage	400	V
V_{EBO}	Emitter-Base Voltage	7	V
I_C	Collector Current-Continuous	2	A
I_{CM}	Collector Current-Pulse	4	A
I_B	Base Current-Continuous	0.5	A
P_C	Collector Power Dissipation @ $T_c=25^\circ\text{C}$	15	W
T_J	Junction Temperature	150	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-55~150	$^\circ\text{C}$



isc Silicon NPN Power Transistor**2SC3569****ELECTRICAL CHARACTERISTICS****T_C=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 10mA ; I _B = 0	400			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 0.7A; I _B = 0.14A			1.0	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 0.7A; I _B = 0.14A			1.2	V
I _{CBO}	Collector Cutoff Current	V _{CB} = 500V; I _E = 0			100	μ A
I _{EBO}	Emitter Cutoff Current	V _{EB} =7V; I _C = 0			100	μ A
h _{FE-1}	DC Current Gain	I _C = 0.2A ; V _{CE} = 5V	20		80	
h _{FE-2}	DC Current Gain	I _C = 0.5A ; V _{CE} = 5V	10			

◆ h_{FE} Classifications

M	L	K
20-40	30-60	40-80

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