

isc Silicon NPN Power Transistor

2SC3895

DESCRIPTION

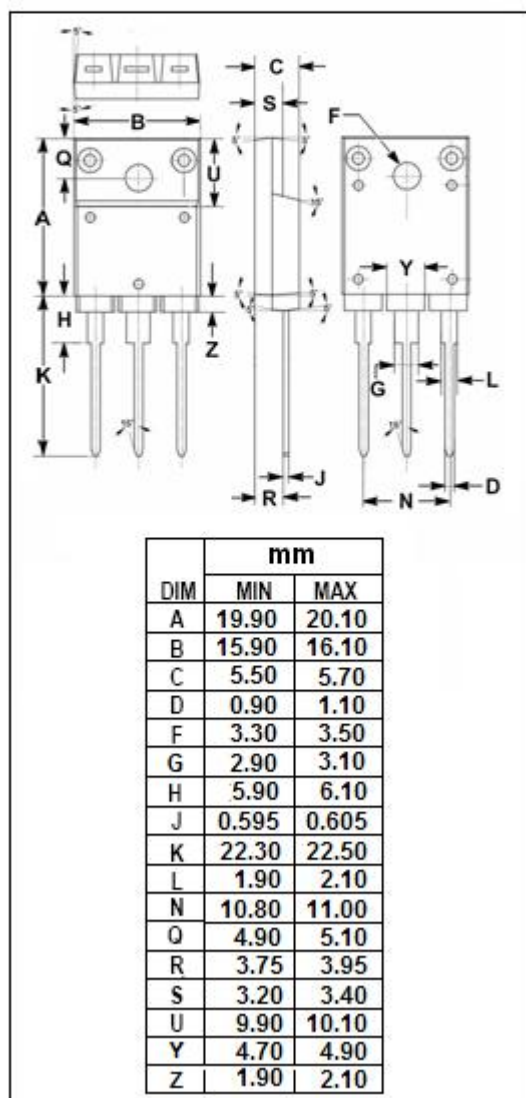
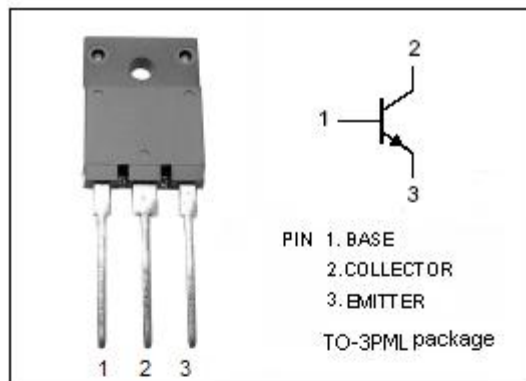
- High Breakdown Voltage
- High Switching Speed
- Wide Area of Safe Operation
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Ultrahigh definition CRT display horizontal deflection output applications

ABSOLUTE MAXIMUM RATINGS($T_a=25^{\circ}\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	1500	V
V_{CEO}	Collector-Emitter Voltage	800	V
V_{EBO}	Emitter-Base Voltage	6	V
I_C	Collector Current- Continuous	7	A
I_{CM}	Collector Current- Peak	16	A
P_C	Collector Power Dissipation @ $T_C=25^{\circ}\text{C}$	60	W
T_J	Junction Temperature	150	$^{\circ}\text{C}$
T_{stg}	Storage Temperature Range	-55~150	$^{\circ}\text{C}$



isc Silicon NPN Power Transistor**2SC3895****ELECTRICAL CHARACTERISTICS****T_c=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 5A; I _B = 1.2A			5.0	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 5A; I _B = 1.2A			1.5	V
I _{CBO}	Collector Cutoff Current	V _{CB} = 800V; I _E = 0			100	μ A
I _{EBO}	Emitter Cutoff Current	V _{EB} =4V; I _C = 0			100	uA
h _{FE-1}	DC Current Gain	I _C = 1A; V _{CE} = 5V	8			
h _{FE-2}	DC Current Gain	I _C = 5A; V _{CE} = 5V	4		8	
Switching times						
t _{stg}	Storage Time	I _C = 4A ; I _{B1} =0.8A; I _{B2} = -1.6A			3	μ s
t _f	Fall Time				0.2	μ s

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