

isc Silicon NPN Power Transistor

2SD2062

DESCRIPTION

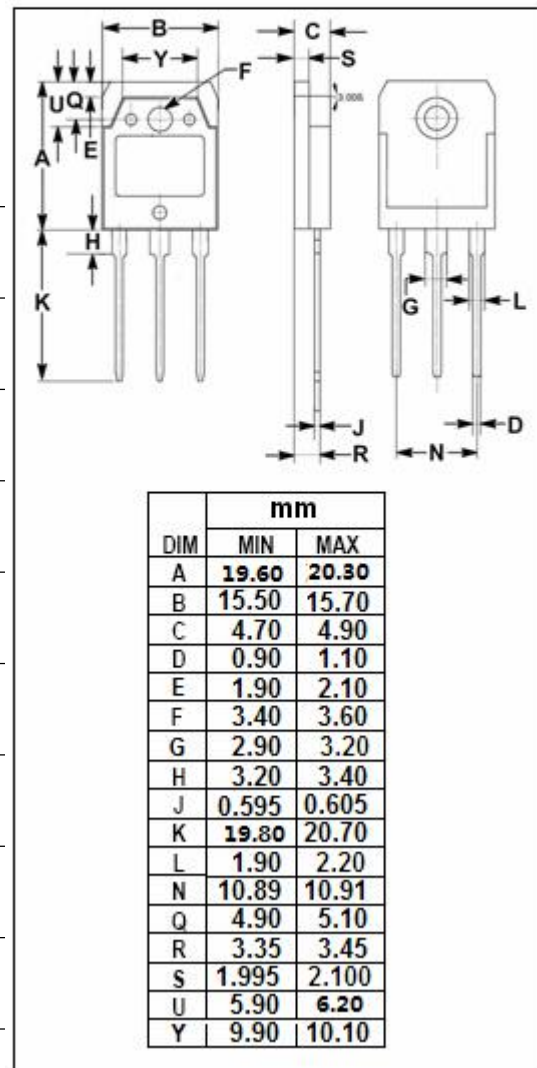
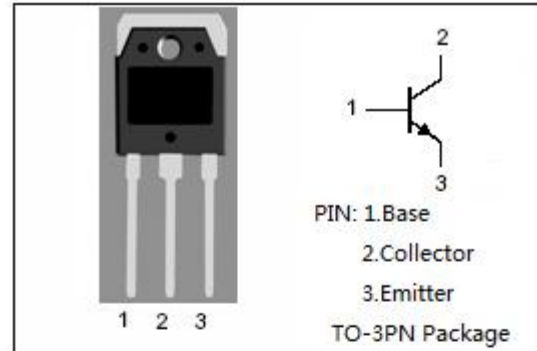
- Collector-Emitter Breakdown Voltage-
: $V_{(BR)CEO} = 80V(\text{Min})$
- Good Linearity of h_{FE}
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for audio and general purpose applications

ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	100	V
V_{CEO}	Collector-Emitter Voltage	80	V
V_{EBO}	Emitter-Base Voltage	6	V
I_C	Collector Current-Continuous	7	A
I_B	Base Current-Continuous	3	A
P_C	Collector Power Dissipation @ $T_C=25^\circ\text{C}$	80	W
T_J	Junction Temperature	150	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-55~150	$^\circ\text{C}$



isc Silicon NPN Power Transistor**2SD2062****ELECTRICAL CHARACTERISTICS****T_C=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 30mA; I _B = 0	80			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 4A; I _B = 0.4A			1.5	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 4A; I _B = 0.4A			2.0	V
I _{CBO}	Collector Cutoff Current	V _{CB} = 100V; I _E = 0			10	μ A
I _{EBO}	Emitter Cutoff Current	V _{EB} = 6V; I _C = 0			10	μ A
h _{FE-1}	DC Current Gain	I _C = 1A; V _{CE} = 5V	60		320	
h _{FE-2}	DC Current Gain	I _C = 4A; V _{CE} = 5V	35			
f _T	Current-Gain—Bandwidth Product	I _E = -0.5A; V _{CE} = 10V		20		MHz

◆ h_{FE-1} Classifications

D	E	F
60-120	100-200	160-320

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