

iscN-Channel MOSFET Transistor

2SK3565, I2SK3565

• FEATURES

- Low drain-source on-resistance:
 $R_{DS(ON)} = 2.0\Omega$ (typ.)
- Enhancement mode:
 $V_{th} = 2.0$ to $4.0V$ ($V_{DS} = 10V$, $I_D = 1.0mA$)
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

• DESCRIPTION

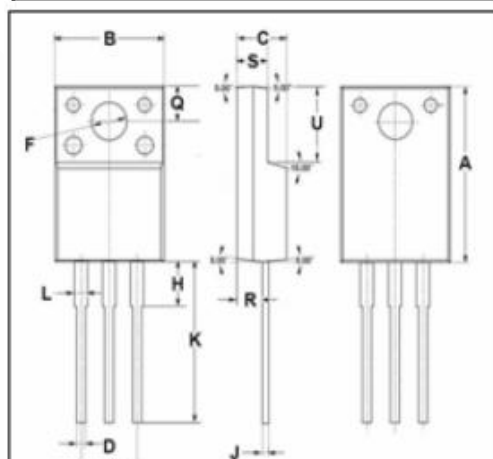
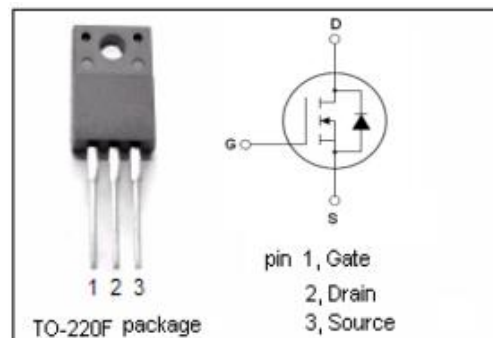
- Switching Voltage Regulators

• ABSOLUTE MAXIMUM RATINGS($T_a = 25^\circ C$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{DS}	Drain-Source Voltage	900	V
V_{GS}	Gate-Source Voltage	± 30	V
I_D	Drain Current-Continuous	5	A
I_{DM}	Drain Current-Single Pulsed	15	A
P_D	Total Dissipation @ $T_c = 25^\circ C$	45	W
T_j	Max. Operating Junction Temperature	150	$^\circ C$
T_{stg}	Storage Temperature	-55~150	$^\circ C$

• THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(ch-c)}$	Channel-to-case thermal resistance	2.78	$^\circ C/W$
$R_{th(ch-a)}$	Channel-to-ambient thermal resistance	62.5	$^\circ C/W$



DIM	mm	
	MIN	MAX
A	14.95	15.05
B	10.00	10.10
C	4.40	4.60
D	0.75	0.90
F	3.10	3.30
H	3.70	3.90
J	0.50	0.70
K	13.4	13.6
L	1.10	1.30
N	5.00	5.20
Q	2.70	2.90
R	2.20	2.40
S	2.65	2.90
U	6.40	6.60

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ELECTRICAL CHARACTERISTICS

 $T_C=25^{\circ}\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V; I_D=10mA$	900			V
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=10V; I_D=1.0mA$	2.0		4.0	V
$R_{DS(on)}$	Drain-Source On-Resistance	$V_{GS}=10V; I_D=3A$		2000	2500	$m\Omega$
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 25V; V_{DS}=0V$			± 10	μA
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=720V; V_{GS}=0V$			100	μA
V_{SDF}	Diode forward voltage	$I_{DR}=5A, V_{GS}=0V$			1.7	V

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