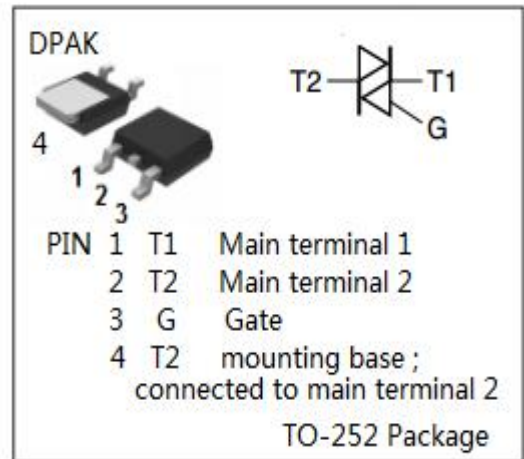


**DESCRIPTION**

- High blocking voltage capability
- Surface-mountable package
- Low holding current for low current loads and lowest EMI at commutation.
- Triggering in all four quadrants
- Very sensitive gate
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

**FEATURES**

- General purpose motor control
- General purpose switching

**ABSOLUTE MAXIMUM RATINGS(Ta=25°C)**

SYMBOL	PARAMETER	MIN	UNIT
V <sub>DRM</sub>	Repetitive peak off-state voltage	800	V
I <sub>T(RMS)</sub>	RMS on-state current (full sine wave; T <sub>mb</sub> ≤107°C)	4	A
I <sub>TSM</sub>	Non-repetitive peak on-state current(T <sub>j</sub> =25°C; T <sub>p</sub> =20ms)	40	A
I <sup>2</sup> t	I <sup>2</sup> t for fusing tp=10ms;sine-wave pulse	8	A <sup>2</sup> S
dI/dt	T <sub>j</sub> =125°C	100	A/us
I <sub>GM</sub>	Peak gate current	4	A
P <sub>G(AV)</sub>	Average gate power dissipation	0.5	W
T <sub>j</sub>	Operating junction temperature	-40~125	°C
T <sub>stg</sub>	Storage temperature	-40~150	°C

**ELECTRICAL CHARACTERISTICS (T<sub>c</sub>=25°C unless otherwise specified)**

SYMBOL	PARAMETER		CONDITIONS	MAX				UNIT
I <sub>GT</sub>	Gate trigger current	I	V <sub>D</sub> =12V; I <sub>T</sub> = 0.1A, R <sub>L</sub> = 100 Ω	T	E	C	B	mA
		II		5	10	35	50	
		III						
		IV						
V <sub>GT</sub>	Gate trigger voltage		V <sub>D</sub> =12V; I <sub>T</sub> = 0.1A	1.5				V
I <sub>DRM</sub>	Repetitive peak off-state current		V <sub>D</sub> =V <sub>DRM</sub> , V <sub>D</sub> =V <sub>DRM</sub> , T <sub>j</sub> =125°C	5 500				uA
V <sub>TM</sub>	On-state voltage		I <sub>T</sub> = 8A	1.5				V
I <sub>H</sub>	Holding current		I <sub>GT</sub> = 0.5A, V <sub>D</sub> = 12V	20				mA

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