

isc Thyristors BT151S-500R

APPLICATIONS

Mesa glass passivation technology;

Have high blocking voltage and high temperature stability cleaner;

Electric tools such as motor speed controller;

Solid state relay;

Heating controller (temperature);

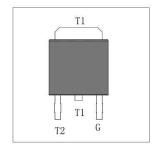
Other phase control circuit

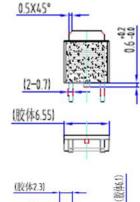
Minimum Lot-to-Lot variations for robust device

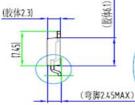
performance and reliable operation

ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

SYMBO				
L	PARAMETER	MIN	UNIT	
V_{DRM}	Repetitive peak off-state voltage	500	V	
V_{RRM}	Repetitive peak reverse voltage	500	V	
I _{T(AV)}	On-state current Tc=80 ℃	7.5	Α	
I _{TSM}	Surge non-repetitive on-state current T_P =10ms	80	A	
$P_{G(AV)}$	Average gate power	1	W	
di/dt	Repetitive rate of rise of on-state current after triggering Tj=125℃	50	A/us	
l²t	I^2 t for fusing t = 10 ms	64	A ² S	
I _{GM}	Peak gate current tp=20us ,Tj=125℃	4	Α	
T _j	Operating Junction temperature	-40 ~+125	$^{\circ}$	
T _{stg}	Storage temperature	-40 ~+150	$^{\circ}$ C	







ELECTRICAL CHARACTERISTICS (TC=25℃ unless otherwise specified)

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
I _{RRM}	Repetitive peak reverse current	V _{RRM} =500V, Tj=125℃			1	mA
		V _{RRM} =500V, Tj=25℃			5	uA
I _{DRM}	Repetitive peak off-state current	V _{DRM} =500V, Tj=125℃			1	mA
		V _{DRM} =500V, Tj=25℃			5	uA
V_{TM}	On-state voltage	I _{TM} = 24A			1.5	V
I_{GT}	Gate-trigger current	$V_D=12V; R_L=100\Omega$			6	mA
V_{GT}	Gate-trigger voltage	$V_D=12V; R_L=100\Omega$			1.5	V
I _H	Holding current	I _T =0.5A			30	mA
IL	Latching current	I _G =1.2I _{GT}		60	100	mA
dv/dt	Critical rate of rise of off-state voltage	V _D =2/3V _{DRM} Tj=125℃	500			V/us
R _{th(j-c)}	Thermal resistance junction to mounting base	in free air		1.75		°C/W

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