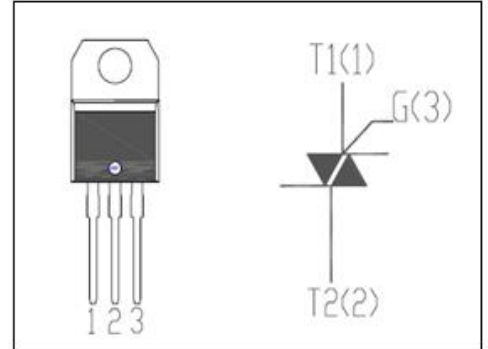


FEATURES

- With TO-220AB insulated package
- Suitable for general purpose where high surge current capability is required. Application such as phase control and static switching on inductive or resistive load.
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	MIN	UNIT
V_{DRM}	Repetitive peak off-state voltage	700	V
V_{RRM}	Repetitive peak reverse voltage	700	V
$I_{\text{T(RMS)}}$	RMS on-state current (full sine wave) $T_j=70^\circ\text{C}$	20	A
I_{TSM}	Non-repetitive peak on-state current $t_p=10\text{ms}$	210	A
T_j	Operating junction temperature	125	$^\circ\text{C}$
T_{stg}	Storage temperature	-45~150	$^\circ\text{C}$
$R_{\text{th(j-c)}}$	Thermal resistance, junction to case	2.8	$^\circ\text{C/W}$
$R_{\text{th(j-a)}}$	Thermal resistance, junction to ambient	60	$^\circ\text{C/W}$

ELECTRICAL CHARACTERISTICS ($T_c=25^\circ\text{C}$ unless otherwise specified)

SYMBOL	PARAMETER		CONDITIONS	MIN	MAX	UNIT
I_{RRM}	Repetitive peak reverse current		$V_R=V_{\text{RRM}}$, $V_R=V_{\text{RRM}}$, $T_j=125^\circ\text{C}$		0.01 3.0	mA
I_{DRM}	Repetitive peak off-state current		$V_D=V_{\text{DRM}}$, $V_D=V_{\text{DRM}}$, $T_j=125^\circ\text{C}$		0.01 3.0	mA
I_{GT}	Gate trigger current	I - II - III	$V_D=12\text{V}$; $R_L=33\ \Omega$	2	50	mA
I_{H}	Holding current		$I_{\text{GT}}=0.5\text{A}$, Gate Open		75	mA
V_{GT}	Gate trigger voltage	I - II - III	$V_D=12\text{V}$; $R_L=33\ \Omega$		1.5	V
V_{TM}	On-state voltage		$I_T=28\text{A}$; $t_p=380\ \mu\text{s}$		1.7	V

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