

isc N-Channel MOSFET Transistor

FCD850N80Z

• FEATURES

- Static drain-source on-resistance:
 $R_{DS(on)} \leq 850\text{m}\Omega$
- Enhancement mode:
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

• APPLICATIONS

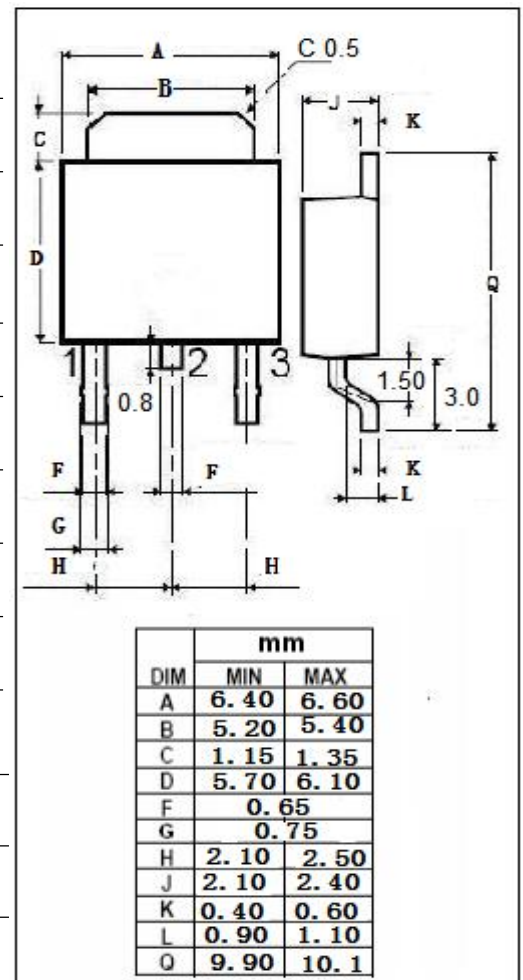
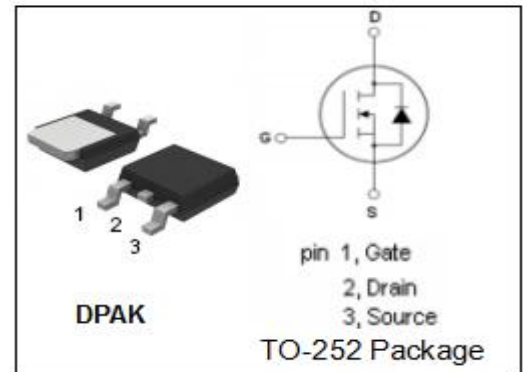
- Power supply
- LED lighting

• ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{DS}	Drain-Source Voltage	800	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Drain Current-Continuous	6	A
I_{DM}	Drain Current-Single Pulsed	18	A
P_D	Total Dissipation @ $T_c=25^\circ\text{C}$	75	W
T_j	Max. Operating Junction Temperature	150	$^\circ\text{C}$
T_{stg}	Storage Temperature	-55~150	$^\circ\text{C}$

• THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(j-c)}$	Channel-to-case thermal resistance	1.65	$^\circ\text{C/W}$
$R_{th(j-a)}$	Channel-to-ambient thermal resistance	100	$^\circ\text{C/W}$



isc N-Channel MOSFET Transistor**FCD850N80Z****ELECTRICAL CHARACTERISTICS** $T_C=25^{\circ}\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V; I_D=1mA$	800			V
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}; I_D=0.6mA$	2.5		4.5	V
$R_{DS(on)}$	Drain-Source On-Resistance	$V_{GS}=10V; I_D=3A$			850	$m\Omega$
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V$			± 10	μA
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=800V; V_{GS}=0V$			25	μA
V_{SD}	Diode forward voltage	$I_{SD}=6A, V_{GS}=0V$			1.2	V

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