

Isc N-Channel MOSFET Transistor

FDD86250

• FEATURES

- With To-252(DPAK) package
- Low input capacitance and gate charge
- Low gate input resistance
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

• APPLICATIONS

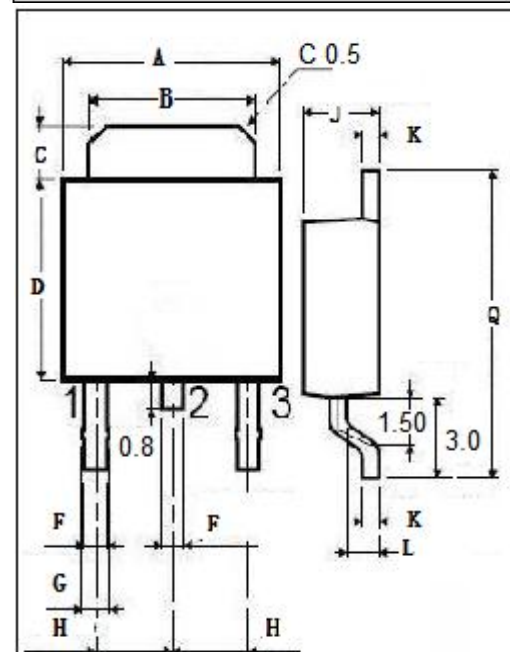
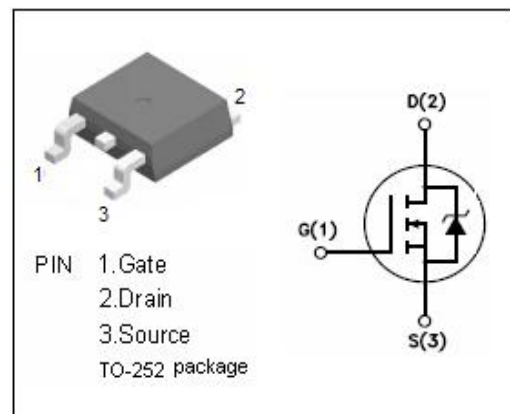
- Switching applications

• ABSOLUTE MAXIMUM RATINGS($T_a=25^{\circ}\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{DS}	Drain-Source Voltage	150	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Drain Current-Continuous	8	A
I_{DM}	Drain Current-Single Pulsed	40	A
P_D	Total Dissipation @ $T_c=25^{\circ}\text{C}$	132	W
T_{ch}	Max. Operating Junction Temperature	150	$^{\circ}\text{C}$
T_{stg}	Storage Temperature	-55~150	$^{\circ}\text{C}$

• THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(ch-c)}$	Channel-to-case thermal resistance	0.94	$^{\circ}\text{C/W}$
$R_{th(ch-a)}$	Channel-to-ambient thermal resistance	40	$^{\circ}\text{C/W}$



DIM	mm	
	MIN	MAX
A	6.40	6.60
B	5.20	5.40
C	1.15	1.35
D	5.70	6.10
F	0.65	
G	0.75	
H	2.10	2.50
J	2.10	2.40
K	0.40	0.60
L	0.90	1.10
Q	9.90	10.1

Isc N-Channel MOSFET Transistor**FDD86250****ELECTRICAL CHARACTERISTICS** $T_c=25^{\circ}\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V; I_D=0.25mA$	150			V
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=5V; I_D=0.25mA$	2.0		4.0	V
$R_{DS(on)}$	Drain-Source On-Resistance	$V_{GS}=10V; I_D=8A$		18.4	22	$m\Omega$
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 30V; V_{DS}=0V$			± 0.1	μA
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=120V; V_{GS}=0V$			1	μA
V_{SDF}	Diode forward voltage	$I_{SD}=8A, V_{GS}=0V$			1.3	V

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