

Isc N-Channel MOSFET Transistor

STP100N10F7

• FEATURES

- With To-220 package
- Low input capacitance and gate charge
- Low gate input resistance
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

• APPLICATIONS

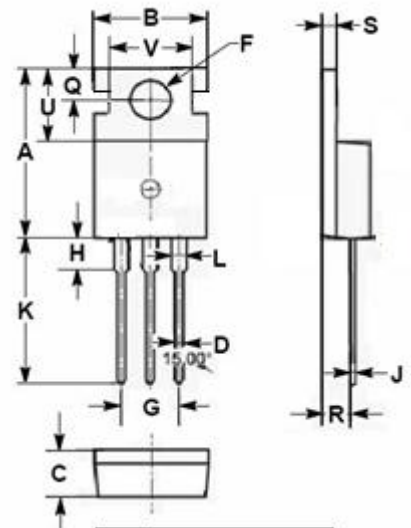
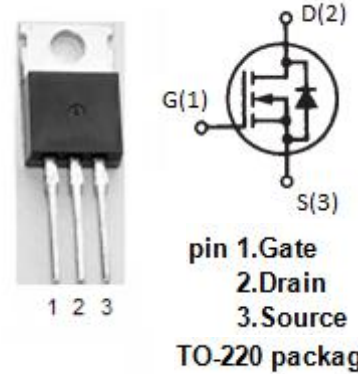
- Switching applications

• ABSOLUTE MAXIMUM RATINGS($T_a=25^{\circ}\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{DS}	Drain-Source Voltage	100	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Drain Current-Continuous@ $T_c=25^{\circ}\text{C}$ $T_c=125^{\circ}\text{C}$	80 62	A
I_{DM}	Drain Current-Single Pulsed	320	A
P_D	Total Dissipation @ $T_c=25^{\circ}\text{C}$	120	W
T_{ch}	Max. Operating Junction Temperature	175	$^{\circ}\text{C}$
T_{stg}	Storage Temperature	-55~175	$^{\circ}\text{C}$

• THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(ch-c)}$	Channel-to-case thermal resistance	1.25	$^{\circ}\text{C/W}$



DIM	mm	
	MIN	MAX
A	15.50	15.90
B	9.80	10.20
C	4.20	4.50
D	0.70	0.90
F	3.40	3.70
G	4.98	5.18
H	2.68	2.90
J	0.44	0.60
K	12.80	13.40
L	1.20	1.45
Q	2.70	2.90
R	2.30	2.70
S	1.29	1.35
U	6.45	6.65
V	8.66	8.86

Isc N-Channel MOSFET Transistor**STP100N10F7****ELECTRICAL CHARACTERISTICS****T_c=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V; I _D = 0.25mA	100			V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = ±20V; I _D =0.25mA	2.5		4.5	V
R _{DS(on)}	Drain-Source On-Resistance	V _{GS} = 10V; I _D =40A			8.0	mΩ
I _{GSS}	Gate-Source Leakage Current	V _{GS} = ±20V; V _{DS} = 0V			±0.1	μA
I _{DSS}	Drain-Source Leakage Current	V _{DS} = 100V; V _{GS} = 0V; T _J =25°C T _J =125°C			1 100	μA
V _{SDF}	Diode forward voltage	I _{SD} =80A, V _{GS} = 0 V			1.2	V

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