

# **Isc N-Channel MOSFET Transistor**

## **TK46E08N1**

#### FEATURES

- Low drain-source on-resistance:
  R<sub>DS</sub>(ON) = 8.4mΩ (V<sub>GS</sub> = 10 V)
- Enhancement mode: Vth = 2.0 to 4.0 V (VDS = 10 V, ID=0.5 mA)
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

### DESCRITION

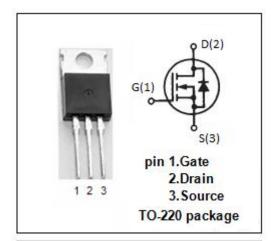
· Switching Voltage Regulators

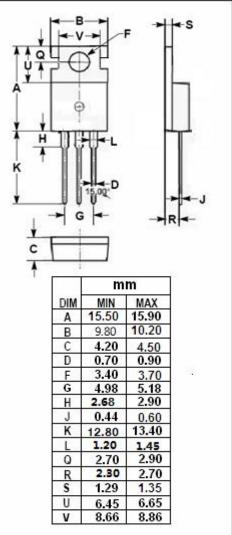
## • ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

SYMBOL	PARAMETER	VALUE	UNIT
V <sub>DSS</sub>	Drain-Source Voltage	80	V
V <sub>GS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub>	Drain Current-Continuous	46	Α
Ірм	Drain Current-Single Pulsed	169	Α
P <sub>D</sub>	Total Dissipation @Tc=25℃		W
Tj	Max. Operating Junction Temperature	150	$^{\circ}$
T <sub>stg</sub>	Storage Temperature	-55~150	$^{\circ}$

### • THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
Rth(ch-c)	Channel-to-case thermal resistance	1.21	°C/W







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#### **ELECTRICAL CHARACTERISTICS**

Tc=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	ТҮР	MAX	UNIT
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V; I <sub>D</sub> = 10mA	80			V
$V_{GS(th)}$	Gate Threshold Voltage	V <sub>DS</sub> = 10V; I <sub>D</sub> =0.5mA	2.0		4.0	V
R <sub>DS(on)</sub>	Drain-Source On-Resistance	V <sub>GS</sub> =10V; I <sub>D</sub> =23A			8.4	m Ω
lgss	Gate-Source Leakage Current	V <sub>GS</sub> = ±20V;V <sub>DS</sub> = 0V			±0.1	μА
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> = 80V; V <sub>GS</sub> = 0V			10	μА
$V_{SDF}$	Diode forward voltage	I <sub>DR</sub> =46A, V <sub>GS</sub> = 0 V			1.2	V



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