

GENERAL DESCRIPTION

Glass passivated thyristors in a plastic envelope, suitable for surface mounting, intended for use in applications requiring high bidirectional blocking voltage capability and high thermal cycling performance. Typical applications include motor control, industrial and domestic lighting, heating and static switching.

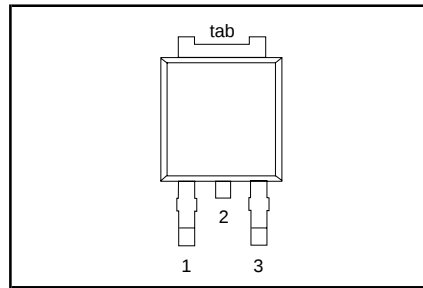
QUICK REFERENCE DATA

SYMBOL	PARAMETER	MAX.	MAX.	MAX.	UNIT
V_{DRM} , V_{RRM}	BT300S (or BT300M)- Repetitive peak off-state voltages	500R 500	600R 600	800R 800	V
$I_{T(AV)}$	Average on-state current	5	5	5	A
$I_{T(RMS)}$	RMS on-state current	8	8	8	A
I_{TSM}	Non-repetitive peak on-state current	65	65	65	A

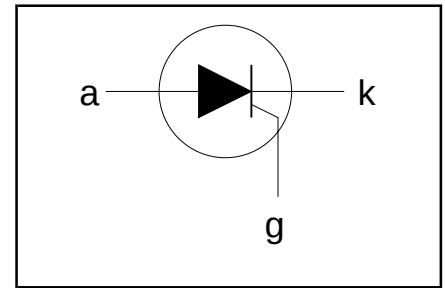
PINNING - TO252

PIN NUMBER	Standard S	Alternative M
1	cathode	gate
2	anode	anode
3	gate	cathode
tab	anode	anode

PIN CONFIGURATION



SYMBOL



LIMITING VALUES

Limiting values in accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.			UNIT
V_{DRM} , V_{RRM}	Repetitive peak off-state voltages		-	-500R 500 ¹	-600R 600 ¹	-800R 800	V
$I_{T(AV)}$	Average on-state current	half sine wave; $T_{mb} \leq 107^\circ\text{C}$	-	5			A
$I_{T(RMS)}$	RMS on-state current	all conduction angles	-	8			A
I_{TSM}	Non-repetitive peak on-state current	half sine wave; $T_j = 25^\circ\text{C}$ prior to surge	-	65			A
I^2t	I^2t for fusing	$t = 10\text{ ms}$	-	71			A
di_T/dt	Repetitive rate of rise of on-state current after triggering	$t = 8.3\text{ ms}$	-	21			A ² s
I_{GM}	Peak gate current	$t = 10\text{ ms}$	-	50			A/μs
V_{GM}	Peak gate voltage	$I_{TM} = 10\text{ A}$; $I_G = 50\text{ mA}$; $di_G/dt = 50\text{ mA}/\mu\text{s}$	-	2			A
V_{RGM}	Peak reverse gate voltage		-	5			V
P_{GM}	Peak gate power		-	5			W
$P_{G(AV)}$	Average gate power	over any 20 ms period	-	0.5			W
T_{stg}	Storage temperature		-40	150			°C
T_j	Operating junction temperature		-	125			°C

¹ Although not recommended, off-state voltages up to 800V may be applied without damage, but the thyristor may switch to the on-state. The rate of rise of current should not exceed 15 A/μs.

THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$R_{th\ j-mb}$	Thermal resistance junction to mounting base	pcb (FR4) mounted; footprint as in Fig.14	-	-	2.2	K/W
$R_{th\ j-a}$	Thermal resistance junction to ambient		-	75	-	K/W

STATIC CHARACTERISTICS

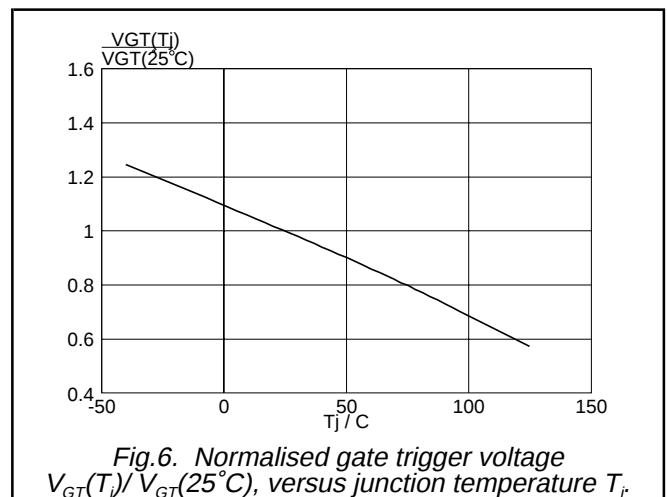
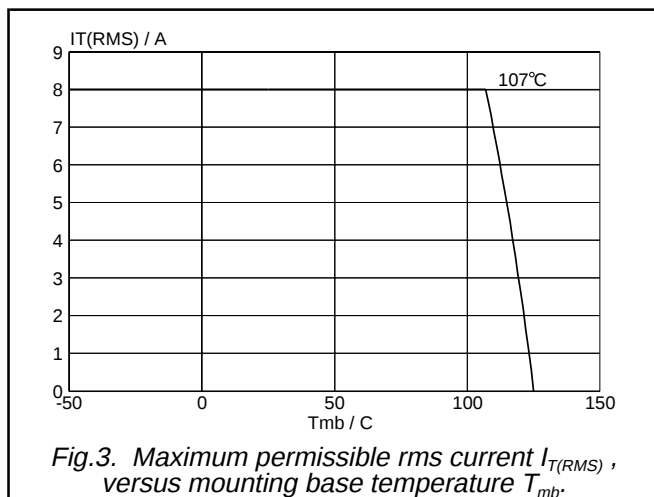
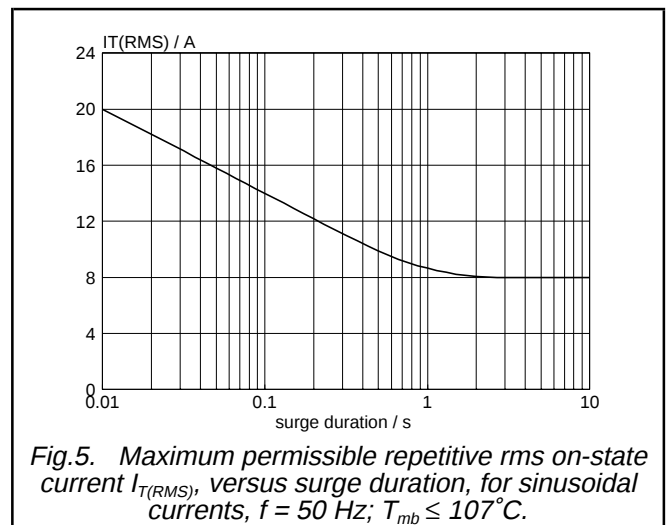
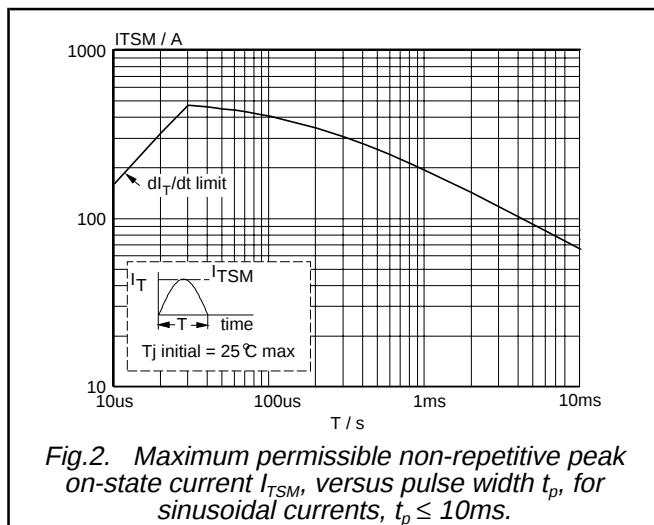
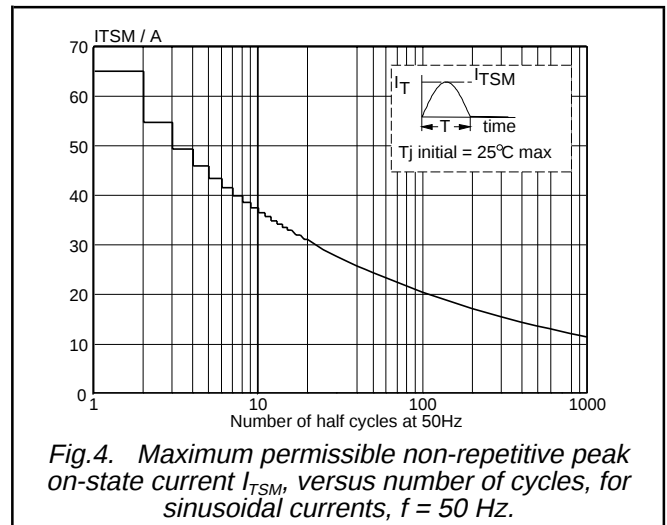
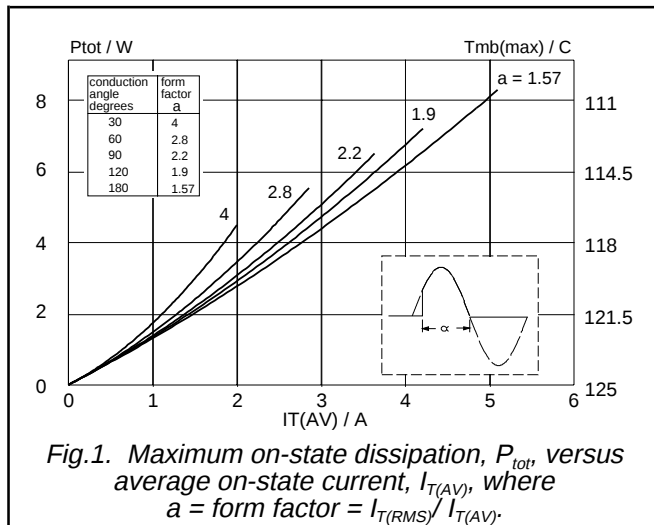
$T_j = 25\text{ }^{\circ}\text{C}$ unless otherwise stated

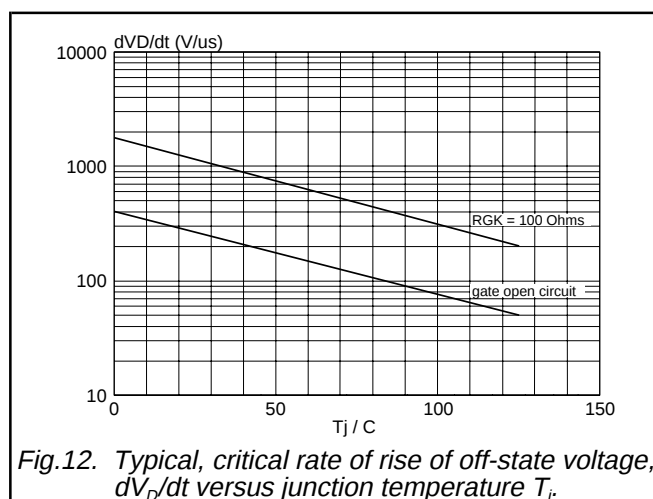
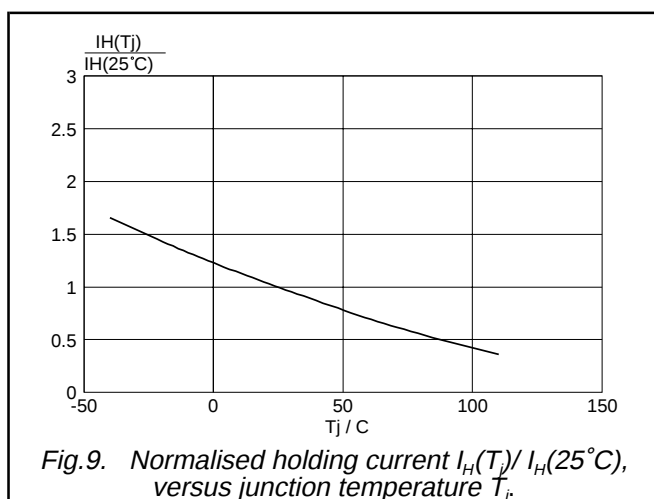
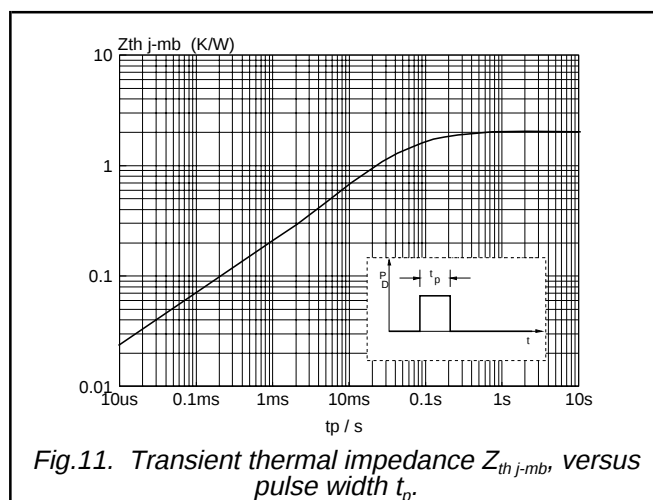
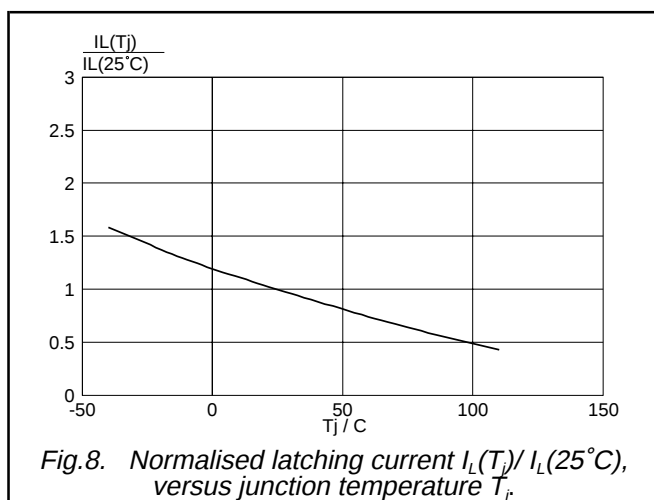
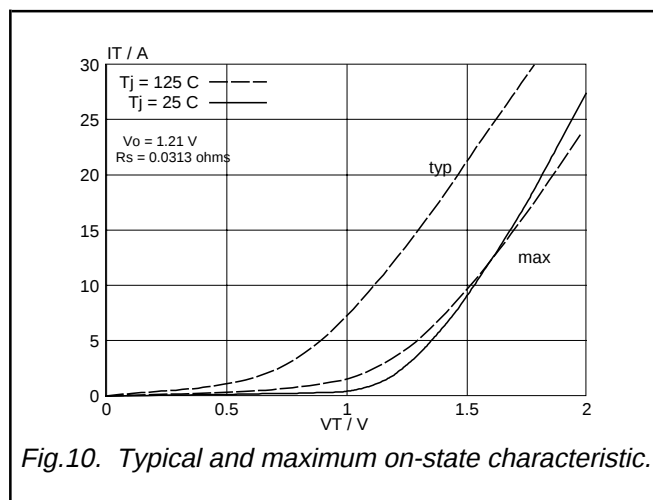
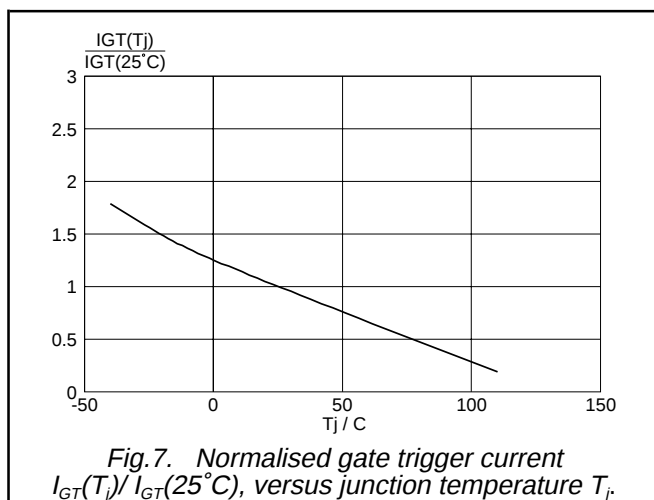
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I_{GT}	Gate trigger current	$V_D = 12\text{ V}; I_T = 0.1\text{ A}$	-	2	15	mA
I_L	Latching current	$V_D = 12\text{ V}; I_{GT} = 0.1\text{ A}$	-	10	40	mA
I_H	Holding current	$V_D = 12\text{ V}; I_{GT} = 0.1\text{ A}$	-	10	20	mA
V_T	On-state voltage	$I_T = 12\text{ A}$	-	1.35	1.6	V
V_{GT}	Gate trigger voltage	$V_D = 12\text{ V}; I_T = 0.1\text{ A}$	-	0.6	1.5	V
I_D, I_R	Off-state leakage current	$V_D = V_{DRM(max)}; I_T = 0.1\text{ A}; T_j = 125\text{ }^{\circ}\text{C}$	0.25	0.4	-	V
		$V_D = V_{DRM(max)}; V_R = V_{RRM(max)}; T_j = 125\text{ }^{\circ}\text{C}$	-	0.1	0.5	mA

DYNAMIC CHARACTERISTICS

$T_j = 25\text{ }^{\circ}\text{C}$ unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
dV_D/dt	Critical rate of rise of off-state voltage	$V_{DM} = 67\% V_{DRM(max)}; T_j = 125\text{ }^{\circ}\text{C};$ exponential waveform.				
t_{gt}	Gate controlled turn-on time	Gate open circuit $R_{GK} = 100\text{ }\Omega$	50	100	-	V/ μs
		$I_{TM} = 10\text{ A}; V_D = V_{DRM(max)}; I_G = 0.1\text{ A};$ $dI_G/dt = 5\text{ A}/\mu\text{s}$	200	1000	-	V/ μs
			-	2	-	μs
t_q	Circuit commutated turn-off time	$V_D = 67\% V_{DRM(max)}; T_j = 125\text{ }^{\circ}\text{C};$ $I_{TM} = 12\text{ A}; V_R = 25\text{ V}; dI_{TM}/dt = 30\text{ A}/\mu\text{s};$ $dV_D/dt = 50\text{ V}/\mu\text{s}; R_{GK} = 100\text{ }\Omega$	-	70	-	μs





MECHANICAL DATA

Dimensions in mm

Net Mass: 1.1 g

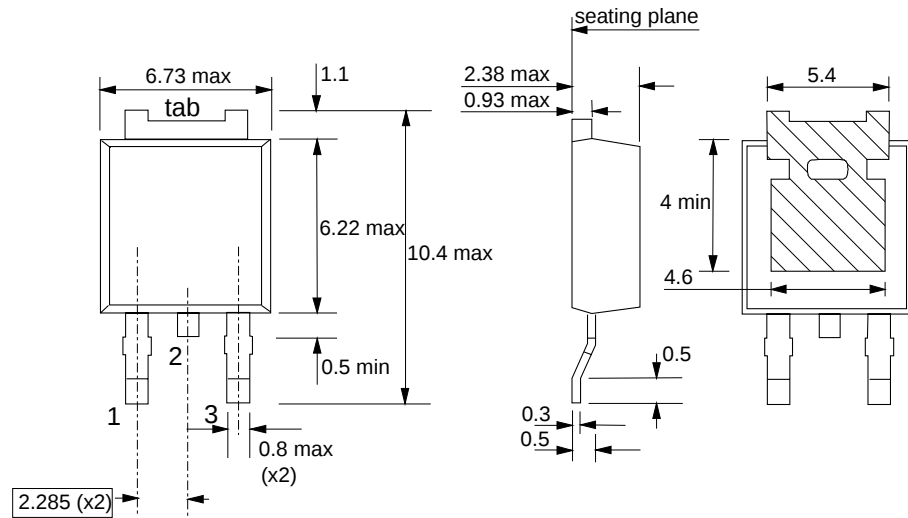


Fig.13. TO252 : centre pin connected to tab.

MOUNTING INSTRUCTIONS

Dimensions in mm

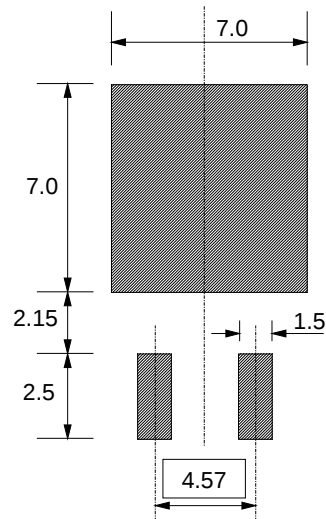


Fig.14. TO252 : minimum pad sizes for surface mounting.

Notes

1. Plastic meets UL94 V0 at 1/8".