

## NCE P-Channel Enhancement Mode Power MOSFET

#### **Description**

The NCE60P05BY uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge .This device is well suited for use as a load switch or in PWM applications.

#### **General Features**

•  $V_{DS} = -60V, I_{D} = -5A$ 

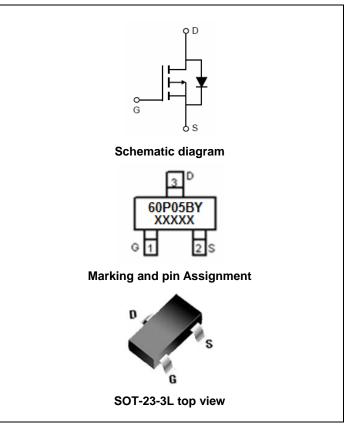
 $R_{DS(ON)}$  <65m $\Omega$  @  $V_{GS}$ =-10V

 $R_{DS(ON)}$  <85m $\Omega$  @  $V_{GS}$ =-4.5V

- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Excellent package for good heat dissipation

## **Application**

- Load switch
- PWM application



**Package Marking and Ordering Information** 

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
60P05BY	NCE60P05BY	SOT-23-3L	Ø180mm	8 mm	3000 units

## Absolute Maximum Ratings (T<sub>C</sub>=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V <sub>DS</sub>	-60	V	
Gate-Source Voltage	V <sub>GS</sub>	±20	V	
Drain Current-Continuous	I <sub>D</sub>	-5	А	
Pulsed Drain Current	I <sub>DM</sub>	-20	Α	
Maximum Power Dissipation	P <sub>D</sub>	1.5	W	
Operating Junction and Storage Temperature Range	$T_{J}$ , $T_{STG}$	-55 To 150	$^{\circ}$ C	

## **Thermal Characteristic**

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{ hetaJA}$	83.3	°C/W

## Electrical Characteristics (T<sub>C</sub>=25 °C unless otherwise noted)

Parameter Symbol		Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =-250μA	-60	-	-	V



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# NCE60P05BY

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-60V,V <sub>GS</sub> =0V	-	-	-1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note 3)	<u>,                                     </u>					
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =-250μA	-1.0	-1.5	-2.0	V
Drain Course On State Besistance		V <sub>GS</sub> =-10V, I <sub>D</sub> =-5A	-	55	65	mΩ
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-5A	-	70	85	mΩ
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =-5V,I <sub>D</sub> =-5A	-	10	-	S
Dynamic Characteristics (Note4)	<u>,                                     </u>					
Input Capacitance	C <sub>lss</sub>		-	1153	-	PF
Output Capacitance	C <sub>oss</sub>	$V_{DS}$ =-30V, $V_{GS}$ =0V,	-	93.7	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>	F=1.0MHz	-	77.7	-	PF
Switching Characteristics (Note 4)	<u>,                                     </u>					
Turn-on Delay Time	t <sub>d(on)</sub>		-	8	-	nS
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =-30V, $R_L$ =6 $\Omega$ ,	-	5	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =-10 $V$ , $R_{G}$ =3 $\Omega$	-	32	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	8	-	nS
Total Gate Charge	Qg	\/ 001 FA	-	15.8	-	nC
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ =-30, $I_{D}$ =-5A, $V_{GS}$ =-10V	-	2.7	-	nC
Gate-Drain Charge	Q <sub>gd</sub>	V <sub>GS</sub> =-10V	-	3.5	-	nC
Drain-Source Diode Characteristics			•			
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =-5A	-		-1.2	V
Diode Forward Current (Note 2)	Is		-	-	-5	Α
Reverse Recovery Time	t <sub>rr</sub>	T <sub>J</sub> = 25°C, I <sub>F</sub> =- 5A	-	27		nS
Reverse Recovery Charge	Qrr	$di/dt = -100A/\mu s^{(Note3)}$	-	32		nC

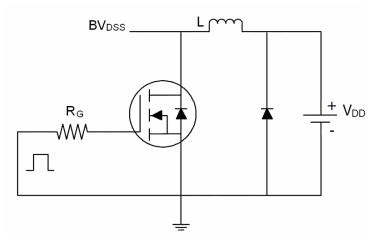
## Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production

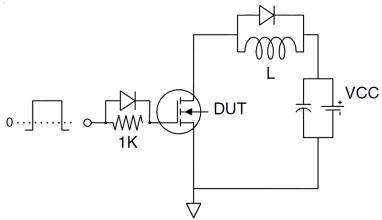


## **Test Circuit**

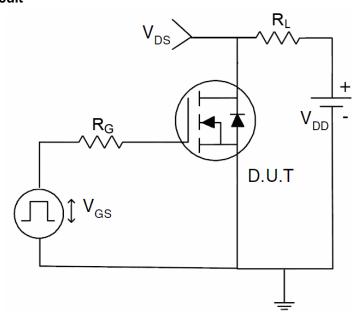
# 1) E<sub>AS</sub> test Circuit



# 2) Gate charge test Circuit

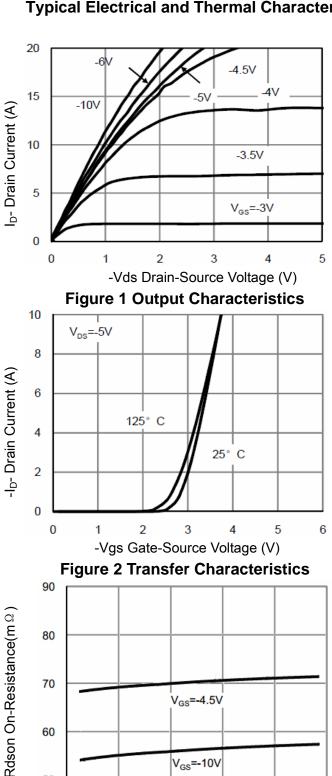


## 3) Switch Time Test Circuit



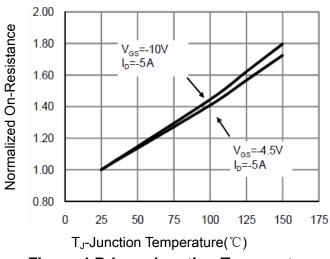


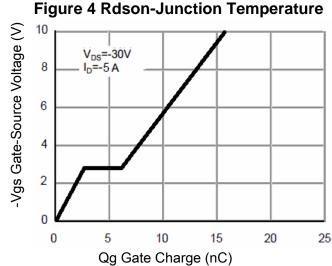
## Typical Electrical and Thermal Characteristics (Curves)



70 V<sub>GS</sub>=-4.5V 60 V<sub>GS</sub>=-10V 50 2 8 0 10 - I<sub>D</sub>- Drain Current (A)

Figure 3 Rdson- Drain Current





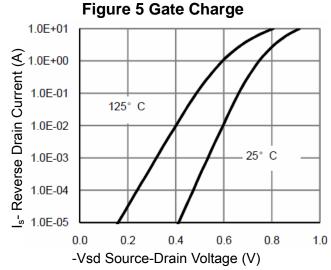


Figure 6 Source- Drain Diode Forward



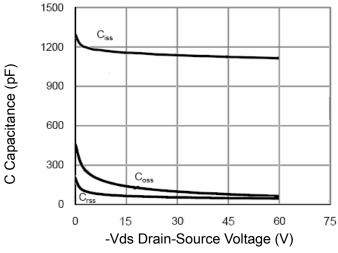
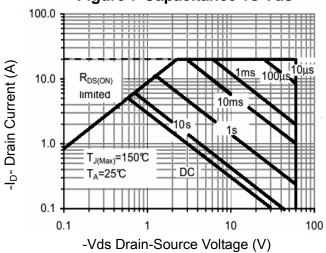


Figure 7 Capacitance vs Vds



**Figure 8 Safe Operation Area** 

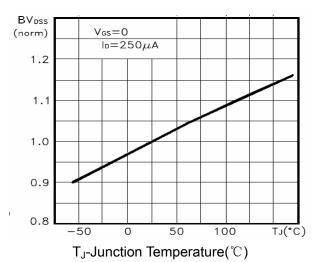


Figure 9 BV<sub>DSS</sub> vs Junction Temperature

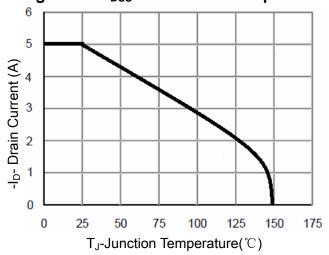
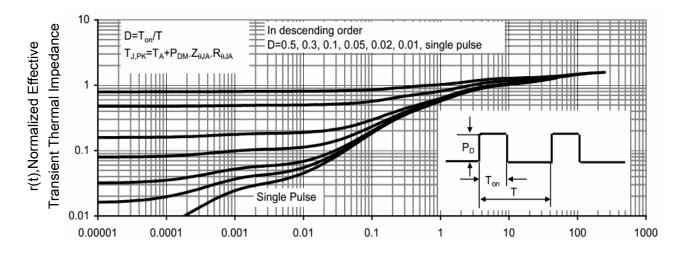


Figure 10 ID Current De-rating

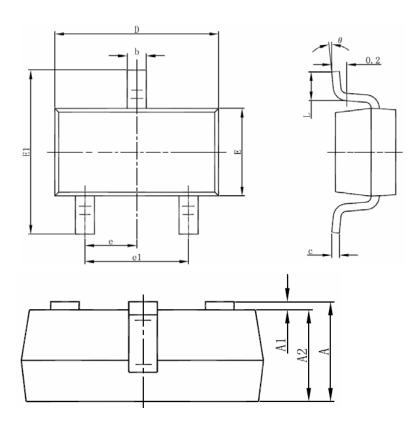


Square Wave Pluse Duration(sec)

Figure 11 Normalized Maximum Transient Thermal Impedance



## **SOT-23-3L Package Information**



Combo o I	Dimensions Ir	n Millimeters	Dimensions In Inches		
Symbol	Min	Max	Min	Max	
Α	1.050	1.250	0.041	0.049	
A1	0.000	0.100	0.000	0.004	
A2	1.050	1.150	0.041	0.045	
b	0.300	0.500	0.012	0.020	
С	0.100	0.200	0.004	0.008	
D	2.820	3.020	0.111	0.119	
E	1.500	1.700	0.059	0.067	
E1	2.650	2.950	0.104	0.116	
е	0.950	(BSC)	0.037(BSC)		
e1	1.800	2.000	0.071	0.079	
L	0.300	0.600	0.012	0.024	
θ	0°	8°	0°	8°	

#### **Notes**

- 1. All dimensions are in millimeters.
- 2. Tolerance  $\pm 0.10$ mm (4 mil) unless otherwise specified
- 3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
- 4. Dimension L is measured in gauge plane.
- $5. \ Controlling \ dimension \ is \ millimeter, \ converted \ inch \ dimensions \ are \ not \ necessarily \ exact.$

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# NCE60P05BY

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