

FEATURES

The plastic package carries Underwriters Laboratory
Flammability Classification 94V-0
For surface mounted applications
Super fast switching for high efficiency
Low reverse leakage
Built-in strain relief,ideal for automated placement
High forward surge current capability
High temperature soldering guaranteed:
260°C/10 seconds at terminals

MECHANICAL DATA

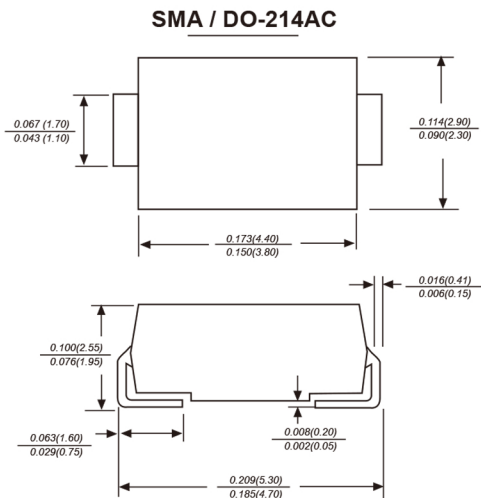
Case: JEDEC DO-214AC molded plastic body

Terminals: Solder plated, solderable per MIL-STD-750,
Method 2026

Polarity: Color band denotes cathode end

Mounting Position: Any

Weight:0.002 ounce, 0.07 grams



Dimensions in inches and (millimeters)

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25°C ambient temperature unless otherwise specified.

Single phase half wave 60Hz,resistive or inductive load,for capacitive load current derate by 20%.

	SYMBOLS	SS32L	SS34L	SS36L	SS38L	SS310L	UNITS
Maximum repetitive peak reverse voltage	V_{RRM}	20	40	60	80	100	V
Maximum RMS voltage	V_{RMS}	14	28	42	56	70	V
Maximum DC blocking voltage	V_{DC}	20	40	60	80	100	V
Maximum average forward rectified current at T_L (see fig.1)	I_{AV}	3.0					A
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load	I_{FSM}	70.0					A
Maximum instantaneous forward voltage at 3.0A	V_F	0.45	0.50	0.70			V
Maximum DC reverse current $T_A=25^\circ\text{C}$ at rated DC blocking voltage $T_A=100^\circ\text{C}$	I_R	0.5 20		0.2 10			mA
Typical junction capacitance (NOTE 1)	C_J	500		300			pF
Typical thermal resistance (NOTE 2)	$R_{\theta JA}$	68.0					°C/W
Operating junction temperature range	T_J	-55 to +150					°C
Storage temperature range	T_{STG}	-55 to +150					°C

Note:1.Measured at 1MHz and applied reverse voltage of 4.0V D.C.

2.P.C.B. mounted with 0.2x0.2"(5.0x5.0mm) copper pad areas

FIG. 1-FORWARD CURRENT DERATING CURVE

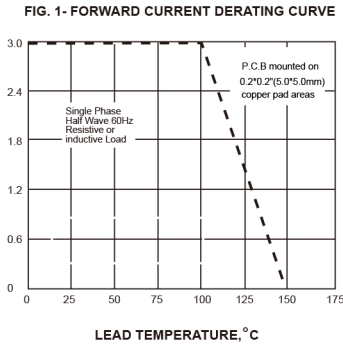


FIG. 2-MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT

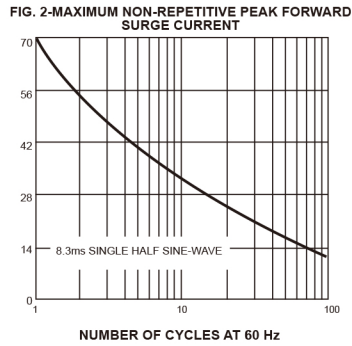


FIG. 3-TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

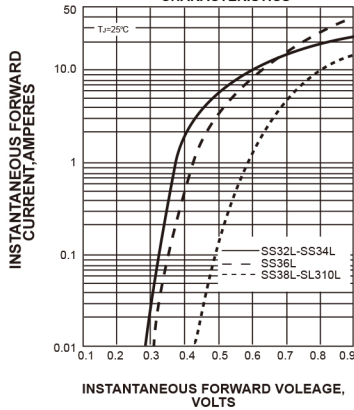


FIG. 4-TYPICAL REVERSE CHARACTERISTICS

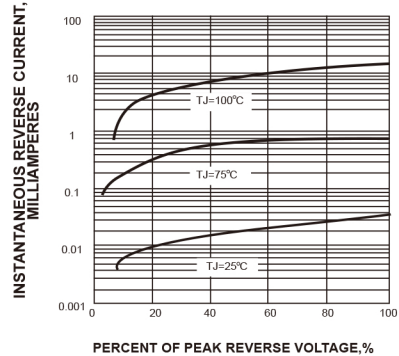


FIG. 5-TYPICAL JUNCTION CAPACITANCE

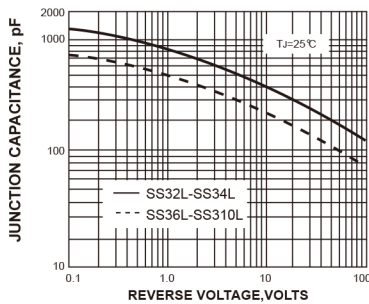


FIG. 6-TYPICAL TRANSIENT THERMAL IMPEDANCE

